

COMP 2825 - Computer Architecture Course Plan

April 2008

Wk	Date	Lecture Description	Text Pages	WebCT Module Description	Text Pages	Quiz	Assign Due
1	Wed 2008-04-16	Multilevel Computer Systems, RISC vs. CISC, program execution	1-13 37-68	1 - Measurements & Numbers			
2	Wed 2008-04-23	Memory organization, cache memory, ECC codes, Boolean Logic	69-81 135-145	2 - Electronic Technology	13-36		
3	Wed 2008-04-30	Digital Logic, functional circuits, adders, signed numbers	146-157	3 - Disk Technology	81-89 93-102	1	
4	Wed 2008-05-07	Latches, memory circuits, types of memory	157-173	4 - Display & Printer Technology	106-117 128-131	2	
5	Wed 2008-05-14	Interfacing memory and I/O chips, system and I/O buses	174-225			3	1
6	Wed 2008-05-21	Mid-Term Exam					
7	Wed 2008-05-28	IJVM Instruction Set, the MIC-1 data path, microinstructions & sequencing	231-255	5 - Data Types	348-351		
8	Wed 2008-06-04	MIC-1 decoding, micro branching, MAL, microcode part 1	255-268	6 - Infix Notation	365-368		
9	Wed 2008-06-11	Microcode part 2, improving performance, MIC-2 thru MIC-4	268-292	7 - Shift & Boolean instructions	376-378	4	
10	Wed 2008-06-18	Caches, branch prediction, supercalar execution, register renaming, ISA intro	292-347	8 - Intel IA64 Architecture	411-421	5	
11	Wed 2008-06-25	The ISA Level, instruction formats, addressing modes, interrupts	351-365 369-376			6	2
12	Wed 2008-07-02	Final Exam	379-408				

Note - This plan is subject to change at the discretion of the instructor