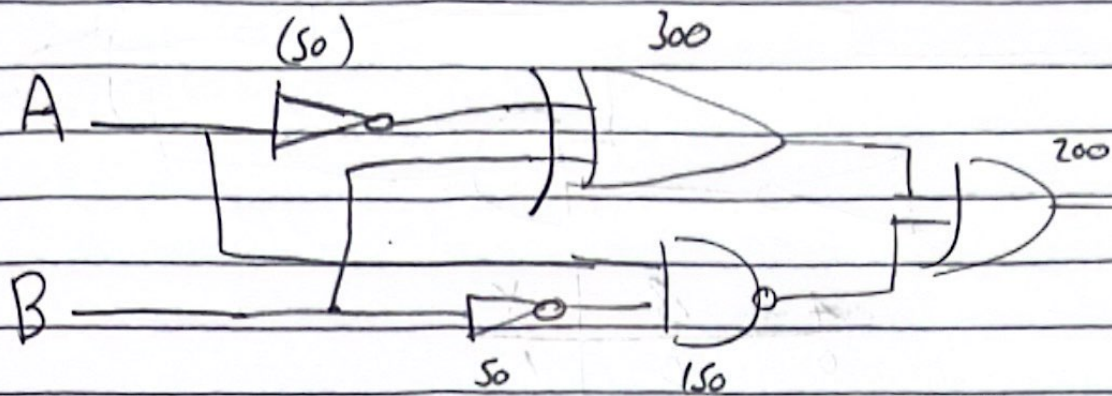


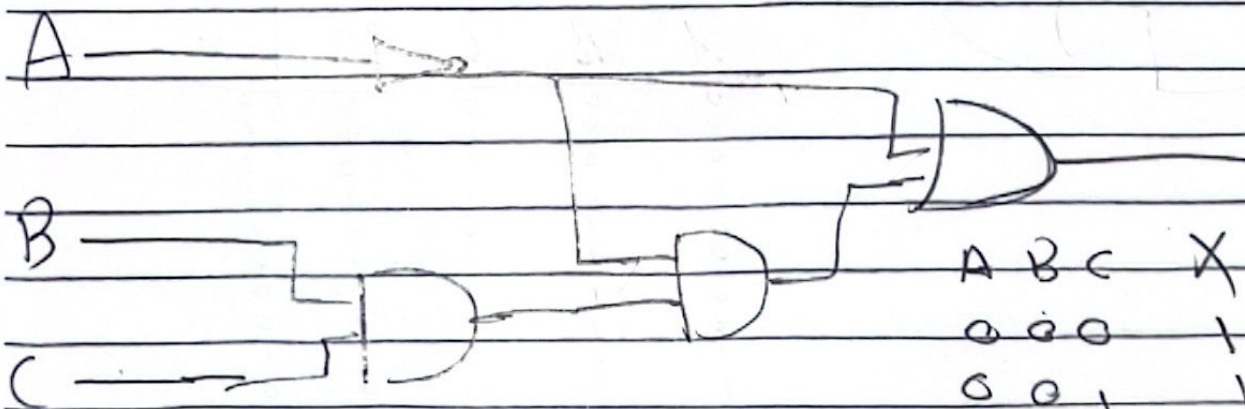
Circuit 1) : A, B



Truth table:

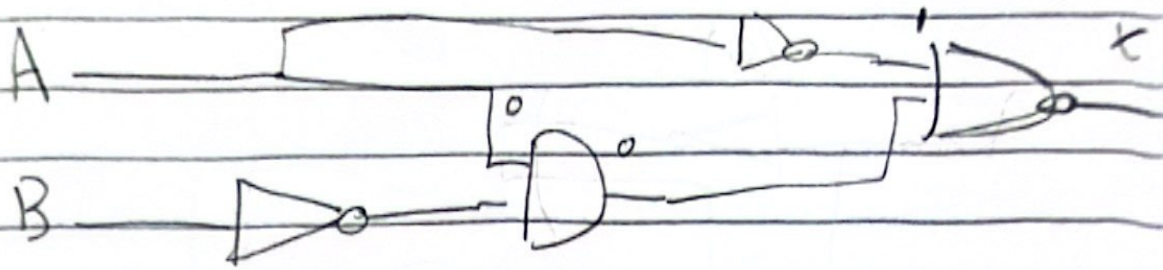
	A	B	\bar{A}	\bar{B}	XOR	NAND	AND
0	0	0	1	1	1	1	1
1	0	1	1	0	0	1	0
2	1	0	0	1	0	0	0
3	1	1	0	0	1	1	1

Circuit 2) : A, B, C



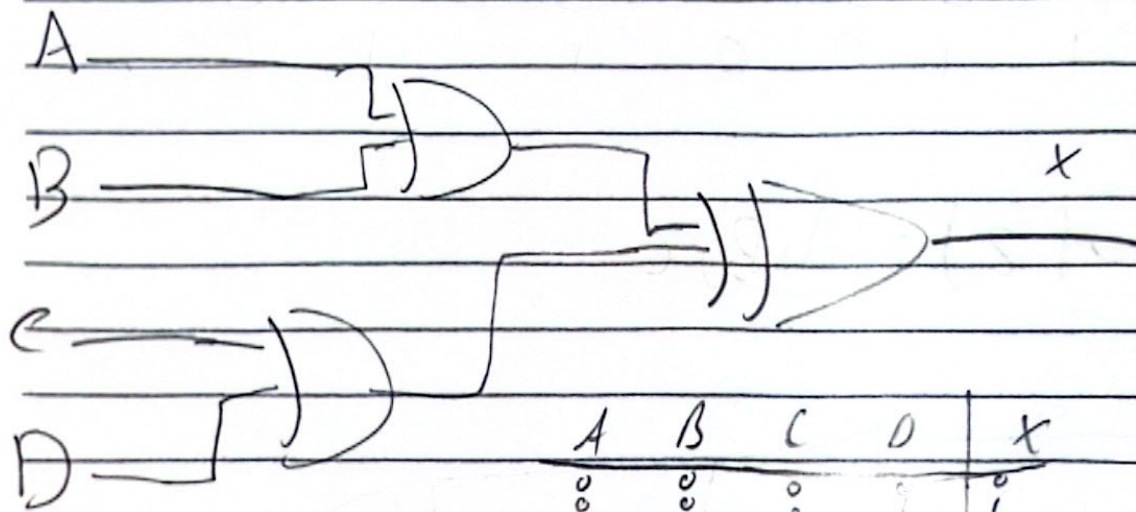
A	B	C	X
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	0

Circuit 3:



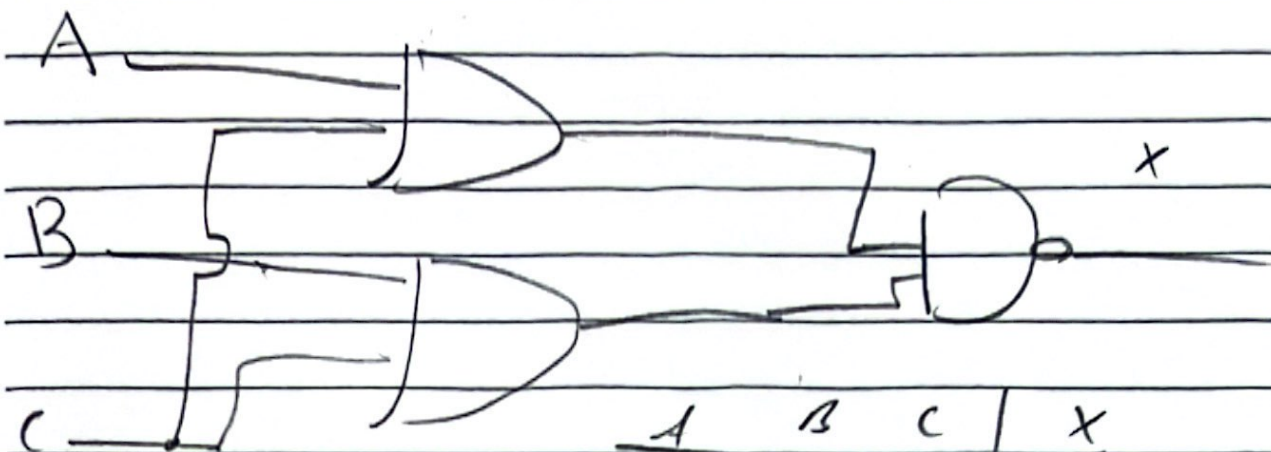
A	B	x
0	0	1
0	1	1
1	0	1
1	1	1

Circuit 4:



A	B	C	D	x
0	0	0	0	0
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	1	0	0
1	0	0	0	1
1	0	0	1	0
1	0	1	0	0
1	1	0	0	1
1	1	0	1	0
1	1	1	0	0

Circuits :-



A	B	C	X
0	0	0	1
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	0