

	Mentor and TA	Mentor and TA	Mentor and TA	Mentor and TA	Mentor and TA
Name	Mr Varma	Mr Dhayal	Ms Skanda	Ms Sameera	Mr Zubair
Experiments	coe16d001@iiitdm.ac.in	edm17d002@iiitdm.ac.in	edm17d001@iiitdm.ac.in	coe18d002@iiitdm.ac.in	coe20d003@iiitdm.ac.in
Exp10	write a perl or python or any scripting language - combinational logic circuit (biparted graph) as adjacent list and enumerate the all the paths from input to output.	write a perl or python or any scripting language - combinational logic circuit (biparted graph) as adjacent list and enumerate the all the paths from input to output.	write a perl or python or any scripting language - combinational logic circuit (biparted graph) as adjacent list and enumerate the all the paths from input to output.	write a perl or python or any scripting language - combinational logic circuit (biparted graph) as adjacent list and enumerate the all the paths from input to output.	write a perl or python or any scripting language - combinational logic circuit (biparted graph) as adjacent list and enumerate the all the paths from input to output.