

UES013 Electrical & Electronics Science Lab

CSE 1st Year (Patiala and Derabassi Campus)

List of Experiments

- 1. To experimentally verify Kirchhoff's Voltage Law (KVL) and Kirchhoff's Current Law (KCL).
(Tinkercad)**
- 2. To experimentally verify Thevenin's theorem. (Tinkercad)**
- 3. To experimentally verify Super position's theorem. (Tinkercad)**
- 4. To plot VI Characteristics of PN Junction Diode (Vlab)**
- 5. To study Half wave and Full Wave Bridge rectifier (Vlab)**
- 6. To study Zener Diode as Voltage Regulator for Line regulation (Vlab)**
- 7. To study BJT in CE Mode and obtain its input and output characteristics (Vlab)**
- 8. Voltage distribution and power consumption in single phase AC series circuit.**
- 9. Current distribution and power consumption in single phase AC parallel circuit.**
- 10. Measurement of power in three-phase balanced R-L load using two wattmeter method.**
- 11. To study OpAmp as Voltage follower and Inverting Amplifier.**