

Sharing and Binding for General Circuits

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Abstract—One of the main part in designing a circuit is designing how resources are shared and binded to reduce number of implemented resource. This paper presents the approaches to high level synthesis with respect to sharing and binding resource for general circuit. For that reasons, research on various model and methodology will be part of this research paper.

Index Terms—component, formatting, style, styling, insert

I. INTRODUCTION

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II. RESOURCE

III. SHARING

IV. BINDING

V. CONCLUSION

ACKNOWLEDGMENT

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