

Q1: Maximum clock frequency is independent of critical path

- A. True
- B. False

Q2: RTL Design only contains combinational circuits and does not include any sequential components

- A. True
- B. False

Q3: A register capable of increasing or decreasing its contents is

- A. Counter
- B. Decoder
- C. Multiplexer
- D. Demultiplexer

Q4: The smallest and largest number that can be represented by IEEE single precision format is

- A.  $10^{-126}$  and  $10^{127}$
- B.  $10^{-127}$  and  $10^{127}$
- C.  $2^{-126}$  and  $2^{127}$
- D.  $2^{-127}$  and  $2^{127}$

Q5: A register capable of increasing or decreasing its contents is

- A. Counter
- B. Decoder
- C. Multiplexer
- D. Demultiplexer

Q6: Which coding scheme is used in computer to represent data internally

- A. Decimal