

SOURCE FILE MANI120211/QRPGLESRC
MEMBER FFREE21

SEQNBR*	...	1	...	2	...	3	...	4	...	5	...	6	...	7	...	8	...	9	...	0	
100		**FREE																			10/29/24
101		Ctl-Opt		NoMain;																	10/30/24
102																					10/30/24
103		Dcl-Pr		AddSp		Packed(2:0);															10/30/24
104		Num4		Packed(2:0);																	10/30/24
105		Num5		Packed(2:0);																	10/30/24
106		Num6		Packed(2:0);																	10/30/24
107		End-Pr;																			10/30/24
108		Dcl-Pr		SubSp		Packed(2:0);															10/30/24
109		Num4		Packed(2:0);																	10/30/24
110		Num5		Packed(2:0);																	10/30/24
111		Num6		Packed(2:0);																	10/30/24
112		End-Pr;																			10/30/24
113		Dcl-Pr		MulSp		Packed(2:0);															10/30/24
114		Num4		Packed(2:0);																	10/30/24
200		Num5		Packed(2:0);																	10/30/24
300		Num6		Packed(2:0);																	10/30/24
400		End-Pr;																			10/30/24
500																					10/30/24
600		Dcl-Proc		AddSp		Export;															10/30/24
700		Dcl-Pi		*N		Packed(2:0);															10/30/24
800		Num4		Packed(2:0);																	10/30/24
900		Num5		Packed(2:0);																	10/30/24
1000		Num6		Packed(2:0);																	10/30/24
1100		End-Pi;																			10/30/24
1101		Dcl-S		Num7		Packed(2:0);															10/30/24
1200		Num6		=		Num4 + Num5;															10/30/24
1201		Return		Num7;																	10/30/24
1300		End-Proc		AddSp;																	10/30/24
1400																					10/30/24
1500		Dcl-Proc		SubSp		Export;															10/30/24
1600		Dcl-Pi		*N		Packed(2:0);															10/30/24
1700		Num4		Packed(2:0);																	10/30/24
1800		Num5		Packed(2:0);																	10/30/24
1900		Num6		Packed(2:0);																	10/30/24
2000		End-Pi;																			10/30/24
2001		Dcl-S		Num7		Packed(2:0);															10/30/24
2100		Num6		=		Num4 - Num5;															10/30/24
2101		Return		Num7;																	10/30/24
2200		End-Proc		SubSp;																	10/30/24
2300																					10/30/24
2400		Dcl-Proc		MulSp		Export;															10/30/24
2500		Dcl-Pi		*N		Packed(2:0);															10/30/24
2600		Num4		Packed(2:0);																	10/30/24
2700		Num5		Packed(2:0);																	10/30/24
2800		Num6		Packed(2:0);																	10/30/24
2900		End-Pi;																			10/30/24
2901		Dcl-S		Num7		Packed(2:0);															10/30/24
3000		Num6		=		Num4 * Num5;															10/30/24
3001		Return		Num7;																	10/30/24
3100		End-Proc		MulSp;																	10/30/24

* * * * E N D O F S O U R C E * * * *

==* <http://pub400.com> *==