

Marie Ma	CO CHP-1
\rightarrow	Personal Computers (PCS) - delivery of good puteron
	7 000 (01)
	- execute third-party software
\rightarrow	Servers - usually acened in network
	-> oriented to carry large workload
	- which consists of single complex application
	or hardling many small john.
	-> based on software from another source:
	Data Base or Simulation System
R ¹	-> greater emphasis on DEPENDABILITY
	- Widest rayor in cost & calcability
	- t widest range in cost & capability
<u></u>	Superiorities >
-1	Has tens of thousands of processors
->	Many terdrites of memory
1	Used for high-end Scientific calculation
-1	represent small fraction of competer market
	1 - 1 morting of particular courses in
-)	Embedded Comp
-1	
-1	They ated with her dware and delinered as a single
	Austery.
-	lowy Lolerance forbards failure
ч	PMD (Personal Mobile Device)
-1	Battery operated, wireless connectivity
ب	Battery apruled, vireless connectivity rely one fouch - sensitue saem
Saughtyle et	

	POPU
	Cloud Computing depends on giant data centers => WSCs (Warthase Sinte Computers)
i V	Eight Ideas Moores law- Integrated arount resources double every 18-24 months.
91	(up & to the right graph)
~ / ~ /	Make the Common (ase Fast: It will Denhance the performance better their optimizing the race case
3)	Performance on performing operations in parallel -> PARALIEL PERFORMANCE
5)	Performance Via Prediction:
<i>b) 1)</i>	Dependability via redundancy

	Operating System = tweeface b/w users program
	and the hardware
	· Handling basic light / subject openion
	· Allo vatry Storage & removey
	· Handling basic liput setput opention · Allo catry strage & remoney · Bro viding for protected sharing tof Cour- among multiple applications using it simultaneously
_	among multiple applications using it simultaneously
	The second of th
	Compiler: Translation of program from thigh level long such as C, C+ + Tz Who pasic instructions that hardware can
	high level long dell as C, C++ the
	Unto pasic instructions that hardware can
	execute.
	Assemble: First program that translates a symbolic version of an instruction to binary version
	symbolic version of an instruction to binary
	nersion
,	
	Fisherly &
	ASSEMBLY (ANGUAGE - Symbolic Language ,
	MACHINE LANGUAGE BIRANG PANGUEYE.
	1
	FLOWGIART
	(WAW)
	Compiler Assembler
/	Nigh level Assembly Burary
	Machine Machine
	Language)

. 0	Cobol - Businers data processing. Lisp - Symbol Manipulation
- Y	angres joi dellinge
۵	Computation
	1000 - Business data processing
0	Lisp - Symbol Manipulation
7	
	and the same and the same the
	WHY HIGH-LEVEL PROGRAMMING LAGING?
,	THE TEVEL PROGRAMMING CHAPT
1)	A 11
/	Allow programmers to think in a more
2)	natural language
~?)	Improved programmer productivity
	less true when programs are un Her in
	less true when programs are un'tten in languages that require few lines.
	Conciseness is a big advantage
3)	Allow programs to be independent of the
	Computer on which they were developed
	The state of the s
	a like it and the second of the second of
0	FIVE COMPONENTS OF COMPUTERS
	TIVE COMPOSER &
.)	Input
	Output
3)	Memory
<u> </u>	Processol
	-> Datagath
	- tentral
1	Cuteria

	VON NEUMANN ARCHITECTURE (Stored Memory) Rogram)
7 1	and the same of th
	Data & Program (Set of Instructions) Store in Main numary
	ur Main niemary
	Mains I Mains In the second will !
	Memory
	Charles and American St. Charles and a Ville St.
	A PROTECTION OF THE PROTECTION
	Central Processing Unit
	of a transfer was the
-	Profestare Anthonetic
	and Logic
	Unit Unit
	1
	The state of the s
	Lupert Output System
	Company of the state of the sta
\sim	Register eto store tenporary data, fastes! memory, Si ze og negister is smallest, means har
	Siza or negester is smallest means has
\$-1/4.27	an allast manages to the
	smallest menory, some Cudan have comprine
→	21 is sæquence of buts (when we combine
(200)	There is a speed mis match in the working of the
	There is a africa mus march in the warry of the
	(very fast) and Main memory (relatively slower)
	So to reduce this buden from memory, negisters
	(very fast) and main memory (relatively slower). So to reduce this benden from memory negisters Stone dath temporarily as it is user to ALU.

	POPU Page No.: Onto:
	Control Unit -
	Timing cional - generale timing, every time particular sask is done
	register. Tontrol & Conducate
→	The same of the sa
	These Components are corrected through Buses.