

भारतीय प्रौद्योगिकी संस्थान पटना Indian Institute of Technology Patna



L-T-P CRD GRD

3-0-3 9 DD

3-0-0 | 6 | CC

3-0-0 | 6 | DD

0-0-3 3 DD

3-1-0 | 8 | CD

3-0-0 6 CD

3-1-0 8 DD

Transcript

Roll No: 0401EE33

Name: Verge Ondrasek

Year of Admission: 2004

Programme: Bachelor of Technology

Course: Electrical Engineering

Semester 1

Sub. Code	Subject Name	L-T-P	CRD	GRD
CH101	Chemistry - I	3-1-0	8	DD
CH110	Chemistry Laboratory	0-0-3	3	BB
EE101	Electrical Sciences	3-1-0	8	DD
HS101	English : Language Through Literature	3-0-0	6	CD
MA101	Mathematics - I	3-1-0	8	DD
ME111	Engineering Drawing	2-0-3	7	DD
PH101	Physics - I	2-1-0	6	DD
PH110	Physics Laboratory	0-0-3	3	CC

Sub. Code Subject Name L-T-P | CRD | GRD Chemistry - II CH102 3-0-0 6 DD CS101 Introduction to Computing 3-0-0 6 CD CS110 Computing Laboratory 3 | CD 0-0-3 EE102 **Basic Electronics Laboratory** 4 | CD 0-0-4 MA102 Mathematics - II 3-1-0 8 | DD ME101 **Engineering Mechanics** 8 CC 3-1-0 ME110 Workshop - I 0-0-3 3 BB

Physics - II

MA201

2-1-0

6 CD

Semester 3

Sub. Code

CS201

EE200

EE201

EE202

EE220

HS221

Credits Taken: 46 Credits Cleared: 46 SPI: 4.57 CPI: 4.7

Subject Name

Object Oriented Programming and Data Structures

Semiconductor Devices and Circuits

Digital Circuits and Microprocessors

Digital Circuits Laboratory

Signal, System and Networks

Fundamentals of Linguistics Science

Mathematics - III

Credits Taken: 49 Credits Cleared: 49 SPI: 4.49 CPI: 4.49

Semester 4

Sub. Code	Subject Name	L-T-P	CRD	GRD
EE203	Analog Integrated Circuits	3-0-0	6	CC
EE204	Analog Circuits Laboratory	0-0-3	3	CC
EE230	Principles of Communication	3-1-0	8	CD
EE280	Electrical Machines	3-0-0	6	DD
EE281	Electrical Machines Laboratory	0-0-3	3	CD
HS223	Cognition: Language and Computation	3-0-0	6	CC
MA225	Probability Theory and Random Processes	3-1-0	8	CD
MA251	Optimization Techniques	3-0-0	6	DD

Credits Taken: 46 Credits Cleared: 46 SPI: 5.07 CPI: 4.79

Semester 5

Semester 2

Sub. Code	Subject Name	L-T-P	CRD	GRD
EE310	Introduction to VLSI Design	3-0-0	6	DD
EE311	VLSI laboratory	0-0-3	3	DD
EE320	Digital Signal Processing	3-0-0	6	DD
EE331	Communication Laboratory	0-0-3	3	F
EE350	Control Systems	3-0-0	6	F
EE370	Electronic Instrumentation	3-0-0	6	F
EE371	Instrumentation and Control Lab	0-0-3	3	F
EE380	Electrical Power Systems	3-0-0	6	DD

Credits Taken: 39 Credits Cleared: 21 SPI: 2.15 CPI: 4.33

Credits Taken: 44 Credits Cleared: 44 SPI: 5.07 CPI: 4.76

Semester 6

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Sub. Code	Subject Name	L-T-P	CRD	GRD
EE304	Design Laboratory	0-0-4	4	F
EE321	Digital Signal Processing Laboratory	0-0-3	3	F
EE322	Mathematical Methods in Electrical Engineering	3-0-0	6	F
EE340	Electromagnetic Theory	3-0-0	6	F
EE351	Advanced Control Systems	3-0-0	6	F
EE360	Embedded Systems	3-0-0	6	F
EE361	Embedded Systems Laboratory	0-0-3	3	F
HS331	Sociology of Development	3-0-0	6	F

Credits Taken: 40 Credits Cleared: 0 SPI: 0.0 CPI: 3.67

Semester 7

Sub. Code	Subject Name	L-T-P	CRD	GRD
EE310	Introduction to VLSI Design	3-0-0	6	F
EE311	VLSI laboratory	0-0-3	3	F
EE320	Digital Signal Processing	3-0-0	6	DD
EE331	Communication Laboratory	0-0-3	3	F
EE350	Control Systems	3-0-0	6	F
EE370	Electronic Instrumentation	3-0-0	6	F
EE371	Instrumentation and Control Lab	0-0-3	3	F
EE380	Electrical Power Systems	3-0-0	6	DD

Credits Taken: 39 Credits Cleared: 12 SPI: 1.23 CPI: 3.36



Assitant Registrar (Academic):

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