



CERTIFICATE OF COMPLETION

This is to certify that Mr./Ms	Aditya G	upta a student
of B.TECH from	Shiv Nadar University	
	Uttar Pradesh	has successfully
completed VLSI Design Internship Program from		
During the internship program wi	ith us, he/she workedon	AHBtoAPB Bridge Design
project.		

Date: 12 Aug 2024

Place: Bengaluru

MSUID: MS/23-24/4196

