Q1. COMPLEX CMOS LOGIC OUT= ~(AB+CD)
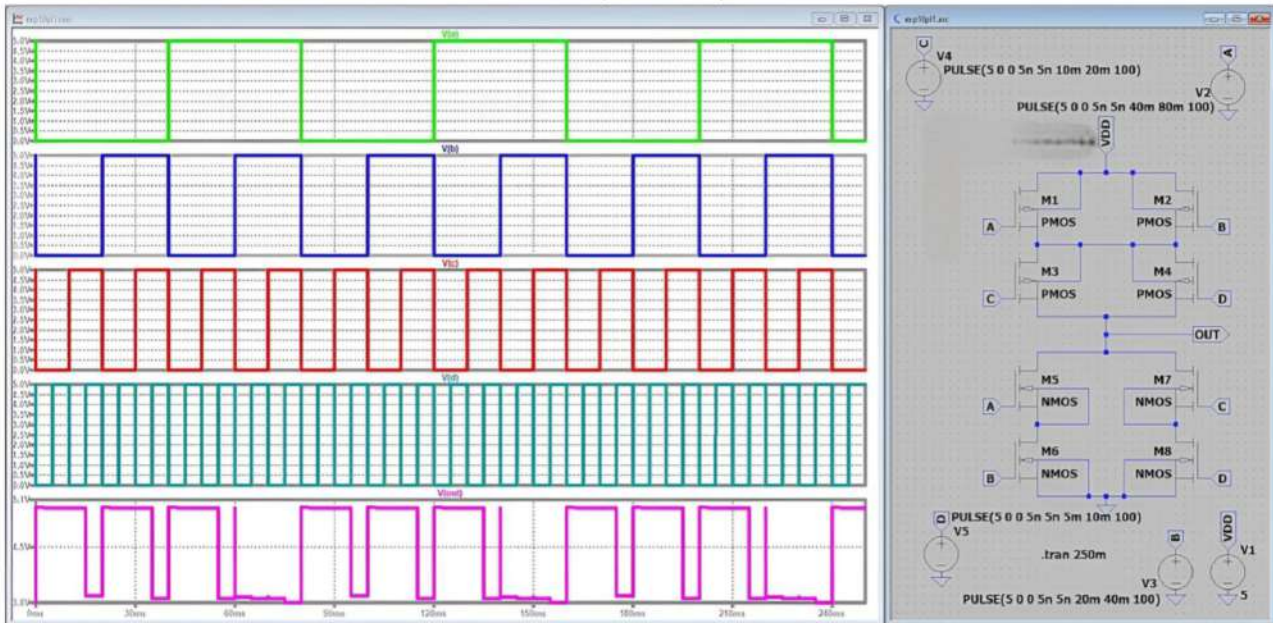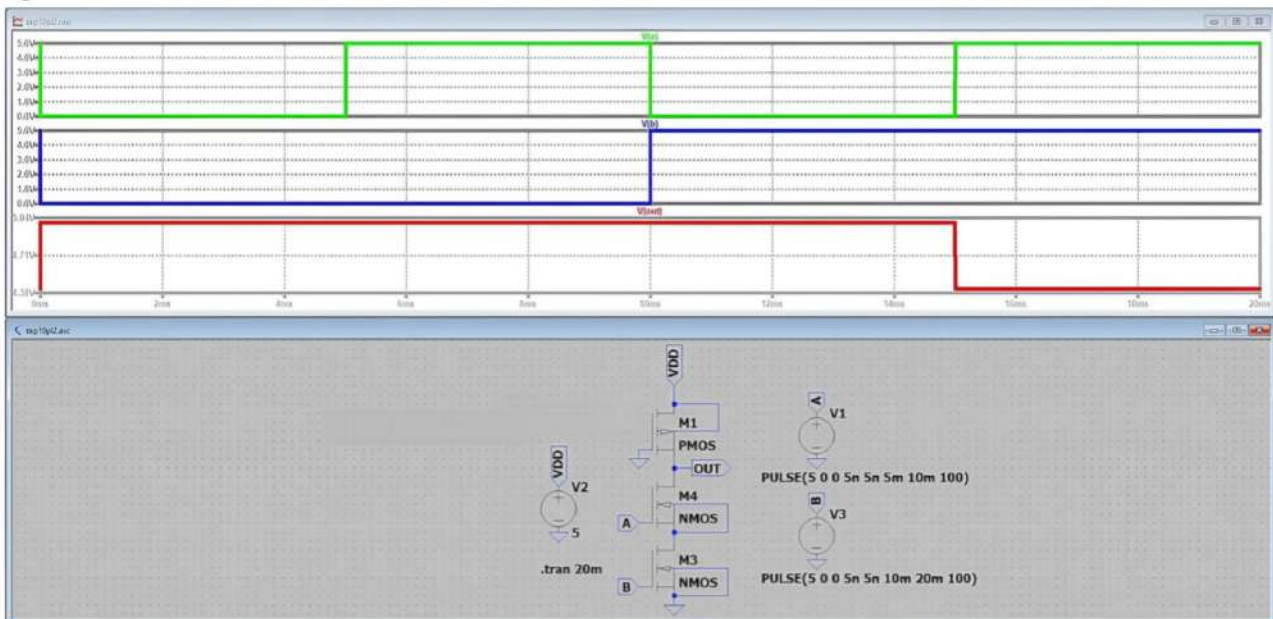

Q2. PSEUDO NMOS NAND GATE

Q3. DC ANALYSIS FOR CMOS INVERTER