# Problem Statement:

Design a 1-bit Half Adder.

# Block Diagram:

|  |
| --- |
| A white rectangular sign with black text |

Input and Outputs are as shown above.

# Truth Table:

|  |  |  |  |
| --- | --- | --- | --- |
| Inputs | | Outputs | |
| A | B | S | C |
| 0 | 0 | 0 | 0 |
| 0 | 1 | 1 | 0 |
| 1 | 0 | 1 | 0 |
| 1 | 1 | 0 | 1 |

# Design Equations:

# Circuit Implementation:

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| --- |
|  |
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|
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# Results:

