ECE 8813A   Spring 2018
Problem Set #11

Assigned: 11-April-18
Due Date: 18-April-18

PROBLEM 11.1:

Given: Working off the design created in HW 10. Implement the Sobel Edge Detection algorithm that is detailed in the publication entitled "Design of Sobel Operator based Image Edge Detection Algorithm on FPGA" by Girish Chaple and R.D. Daruwala.

Find: Create a detailed block diagram and ASM diagrams as needed. Modify your code in HW10_top.v.

Detailed information about the standard can be found at the following link:

http://tinyvga.com/vga-timing/800x600@60Hz