

# Linear Active Networks

- Spence

Ch: 1

## Notation

$v_{AC}$  → instantaneous value.

$V_{AC}$  → mean or dc. value.

$v_{ac}$  → signal component

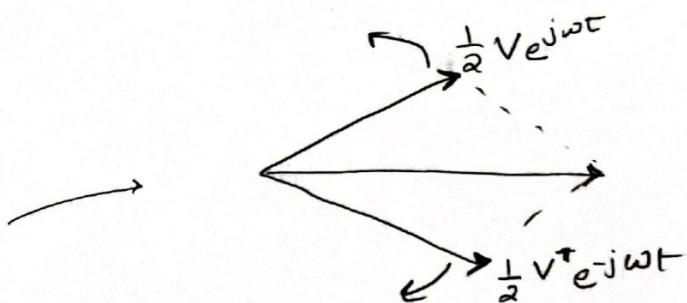
$V_{ac}$  → Complex Voltage.

$\hat{V}_{ac}$  → peak voltage.

$$\rightarrow \text{Let } v = \hat{V} \cos \omega t$$

$$\Rightarrow v = \operatorname{Re} [\hat{V} e^{j\omega t}]$$

$$= \frac{1}{2} [V e^{j\omega t} + V^* e^{-j\omega t}]$$



In the most general case.

$$v = \frac{1}{2} [V e^{j\omega t} + V^* e^{-j\omega t}]$$

$$i = \frac{1}{2} [I e^{j\omega t} + I^* e^{-j\omega t}]$$

where  $V$  and  $I$  are complex quantities since their phases may be non-zero.

$$\Rightarrow V = \hat{V} e^{j(\theta + \phi)}$$

$$I = \hat{I} e^{j\theta} \quad \left. \begin{array}{l} \phi \text{ is relative phase} \\ \theta \text{ is initial phase at } t=0 \end{array} \right\}$$

Sum of these two counterrotating vectors is a sinusoid  $\hat{V} \cos \omega t$ .

Therefore the voltage & current in a network (although scalars) can be fully described by  $V$  &  $I$  (complex).

$\theta$  is usually ignored since it is irrelevant for steady state analysis.

$$V_{rms} = \frac{|V|}{\sqrt{2}} = \hat{V}/\sqrt{2}$$

$$I_{rms} = \frac{|I|}{\sqrt{2}} = \hat{I}/\sqrt{2}$$

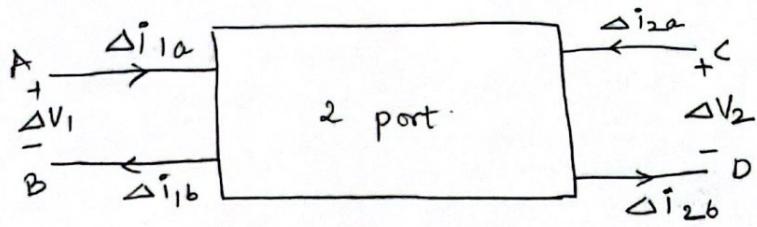
Generalising even further ...

Let,  
 $i = \frac{1}{2} [I e^{j\omega t} + I^* e^{-j\omega t}]$

The exponent need not be purely imaginary.

$$\Rightarrow i = \frac{1}{2} [I e^{(\sigma+j\omega)t} + I^* e^{(\sigma-j\omega)t}] \Rightarrow \text{sinusoids whose amplitudes exponentially.}$$

Two port description.



#### Assumptions.

- > The source & load are such that  $\Delta i_{1a} = \Delta i_{1b}$  &  $\Delta i_{2a} = \Delta i_{2b}$ .
- > We ignore the voltage  $V_{AC}$  &  $V_{BD}$ .

At any given frequency

$$\left. \begin{array}{l} I_1 = Y_{11} V_1 + Y_{12} V_2 \\ I_2 = Y_{21} V_1 + Y_{22} V_2 \end{array} \right\} \begin{array}{l} Y \text{ parameters are a function of frequency.} \\ \text{and also on the bias point since the network is only linear for small signals.} \end{array}$$

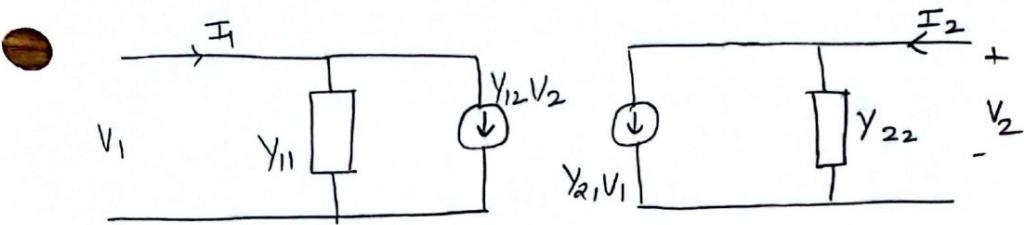
$$Y_{11} = g_{11} + j b_{11} = \frac{I_1}{V_1} \Big|_{V_2=0} \rightarrow \text{short circuit input admittance } (y_i)$$

$$Y_{12} = g_{12} + j b_{12} = \frac{I_1}{V_2} \Big|_{V_1=0} \rightarrow \text{reverse transfer } " \quad (y_r)$$

$$Y_{21} = g_{21} + j b_{21} = \frac{I_2}{V_1} \Big|_{V_2=0} \rightarrow \text{forward } " \quad (y_f)$$

$$Y_{22} = g_{22} + j b_{22} = \frac{I_2}{V_2} \Big|_{V_1=0} \rightarrow \text{s.c op admittance. } (y_o)$$

## Alternative representation (visual)



## Matrix representation.

$$\begin{bmatrix} I_1 \\ I_2 \end{bmatrix} = \begin{bmatrix} Y_{11} & Y_{12} \\ Y_{21} & Y_{22} \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \end{bmatrix}$$

## Three terminal Networks.

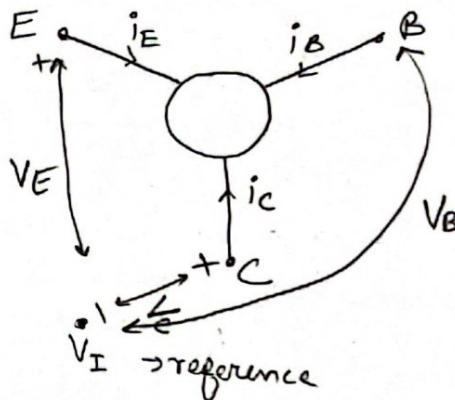
$$I_e = Y_{ee} V_e + Y_{eb} V_b + Y_{ec} V_c$$

$$I_b = Y_{be} V_e + Y_{bb} V_b + Y_{bc} V_c$$

$$I_c = Y_{ce} V_e + Y_{cb} V_b + Y_{cc} V_c.$$

Indefinite admittance matrix

$$\Rightarrow \begin{bmatrix} I_e \\ I_b \\ I_c \end{bmatrix} = \begin{bmatrix} Y_{ee} & Y_{eb} & Y_{ec} \\ Y_{be} & Y_{bb} & Y_{bc} \\ Y_{ce} & Y_{cb} & Y_{cc} \end{bmatrix} \begin{bmatrix} V_e \\ V_b \\ V_c \end{bmatrix}$$



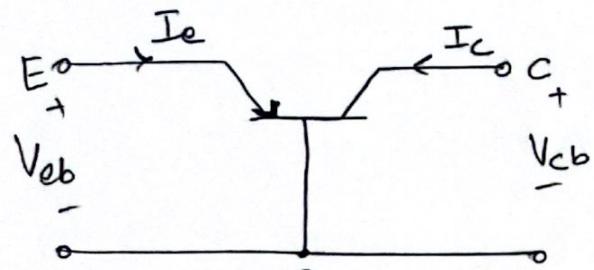
$$Y_{bc} = \frac{I_b}{V_c} \quad V_e = V_b = 0.$$

$$\left. \begin{array}{l} \text{Notice, (1) KCL} \Rightarrow Y_{ee} + Y_{be} + Y_{ce} = 0 \\ Y_{eb} + Y_{bb} + Y_{cb} = 0 \\ Y_{ec} + Y_{bc} + Y_{cc} = 0 \end{array} \right\}$$

Sum of each column = 0

- (2) If only one node is driven by a voltage all the terminal voltages must be equal since no currents flow.  $\Rightarrow$  This can only be satisfied if the sum of each row = 0.

> Two port from 3 terminal.



$$\begin{matrix} E & B \\ \left[ \begin{array}{ccc} Y_{ee} & Y_{eb} & Y_{ec} \\ Y_{be} & Y_{bb} & Y_{bc} \\ Y_{ce} & Y_{cb} & Y_{cc} \end{array} \right] \end{matrix}$$

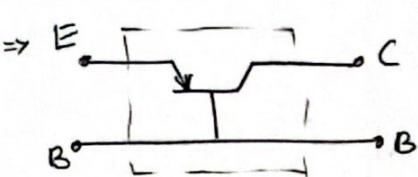
deleted because  
we are not interested  
in  $I_b$

deleted  
because  
 $V_b$  is reference  
 $\Rightarrow V_b = 0$ .

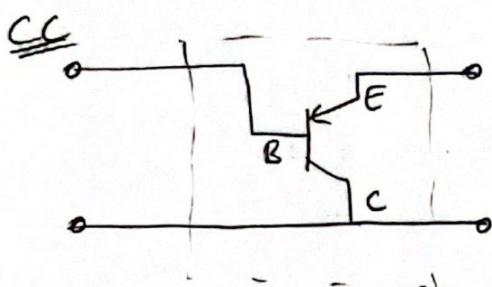
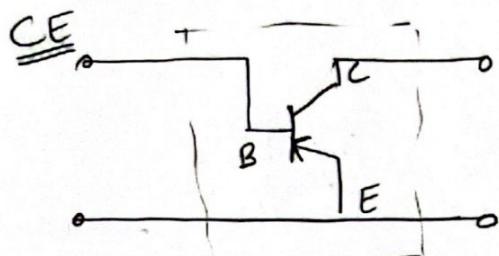
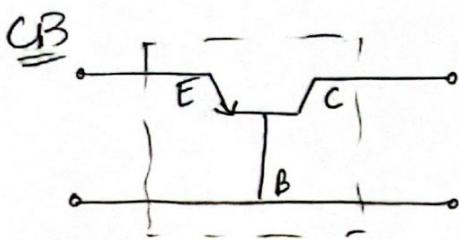
$$I_c = Y_{ee} V_{eb} + Y_{ec} V_{cb}$$

$$I_c = Y_{ce} V_{eb} + Y_{cc} V_{cb}$$

→ Basically remove the B column & row from 3 terminal indefinite admittance matrix to arrive at two port.



$$\begin{bmatrix} Y_{11} & Y_{12} \\ Y_{21} & Y_{22} \end{bmatrix} = \begin{bmatrix} Y_{ee} & Y_{ec} \\ Y_{ce} & Y_{cc} \end{bmatrix}$$



> Since the conditions of  $E_{row}=0$  &  $E_{column}=0$  need to be satisfied, 4 parameters of  $g$  in the indefinite admittance matrix are enough to generate the full matrix.

⇒ By characterizing CB behaviour we can derive CC & CE behaviour.

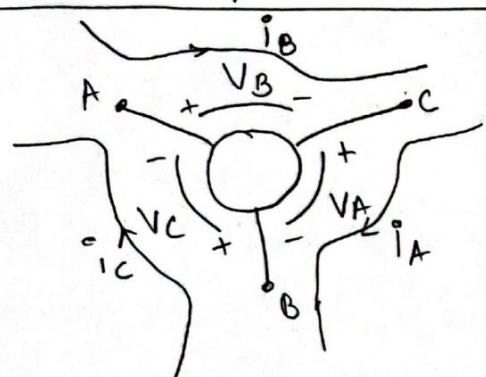
- > Fill in CB parameters '4'
- > Fill in remaining 5 elements since  $E_{row}=0$  &  $E_{column}=0$
- > Eliminate E row & column to get 2 port parameters for CE.

- > A four terminal network has a  $4 \times 4$  indefinite admittance matrix and requires at least 9 out of the 16 parameters to fully describe it. However redrawing it as a two port only gives 4 parameters. Therefore a 2 port model is a partial description but sufficient for practical situations.
- > Impedance matrix

$$Z = Y^T = \begin{bmatrix} Z_{11} & Z_{12} \\ Z_{21} & Z_{22} \end{bmatrix}$$

3 terminal impedance description.

Indefinite impedance matrix



$$\begin{bmatrix} V_a \\ V_b \\ V_c \end{bmatrix} = \begin{bmatrix} z_{aa} & z_{ab} & z_{ac} \\ z_{ba} & z_{bb} & z_{bc} \\ z_{ca} & z_{cb} & z_{cc} \end{bmatrix} \begin{bmatrix} I_a \\ I_b \\ I_c \end{bmatrix}$$

$\sum \text{row} = 0 \quad \& \quad \sum \text{column} = 0$

Common base  $\Rightarrow$  2 port  $\begin{bmatrix} V_a \\ V_c \end{bmatrix} = \begin{bmatrix} z_{aa} & z_{ac} \\ z_{ca} & z_{cc} \end{bmatrix} \begin{bmatrix} I_a \\ I_c \end{bmatrix}$

Let  $-V_c = V_{in}$ ,  $-I_c = I_{in}$ ,  $V_a = V_{out}$ ,  $I_a = I_{out}$ .

$$\Rightarrow \begin{bmatrix} Z_{11} & Z_{12} \\ Z_{21} & Z_{22} \end{bmatrix} = \begin{bmatrix} +z_{cc} & -z_{ca} \\ -z_{ac} & +z_{aa} \end{bmatrix}$$

## Hybrid parameters

$$\begin{bmatrix} V_1 \\ I_2 \end{bmatrix} = \begin{bmatrix} h_{11} & h_{12} \\ h_{21} & h_{22} \end{bmatrix} \begin{bmatrix} I_1 \\ V_2 \end{bmatrix}$$

$$H = K^T$$

$$\&$$

$$K = H^T$$

$$\begin{bmatrix} I_1 \\ V_2 \end{bmatrix} = \begin{bmatrix} K_{11} & K_{12} \\ K_{21} & K_{22} \end{bmatrix} \begin{bmatrix} V_1 \\ I_2 \end{bmatrix}$$

→ Indefinite matrix description does not exist since units of  $h \& K$  vary &  $\sum_{\text{row}} \& \sum_{\text{column}} \neq 0$ .

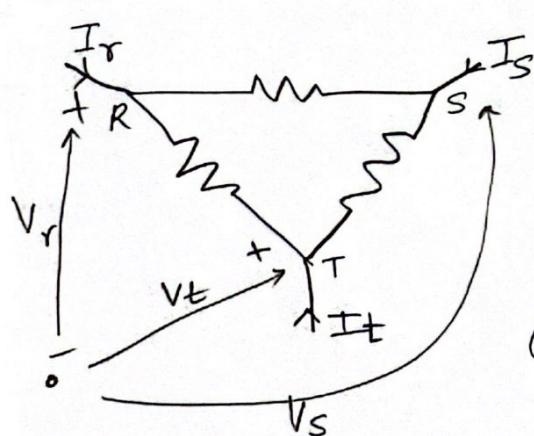
## ABCO or Transmission parameters

$$\begin{bmatrix} V_1 \\ I_1 \end{bmatrix} = \begin{bmatrix} A & B \\ C & D \end{bmatrix} \begin{bmatrix} V_2 \\ -I_2 \end{bmatrix}$$

↳ Cascaded 2 port.  
→ No indefinite matrix.

→ Z parameters use mesh descriptions of  $V \& I$  and becomes complex for larger networks & therefore Y is preferred.

## Networks with 2 terminal elements



$$Y = \begin{bmatrix} R & S & T \\ S & g_1 + g_3 & -g_1 & -g_3 \\ T & -g_1 & g_1 + g_2 & -g_2 \\ S & g_3 & -g_2 & g_2 + g_3 \end{bmatrix}$$

### Observations

① Diagonal elements = sum of conductances incident on the terminal.

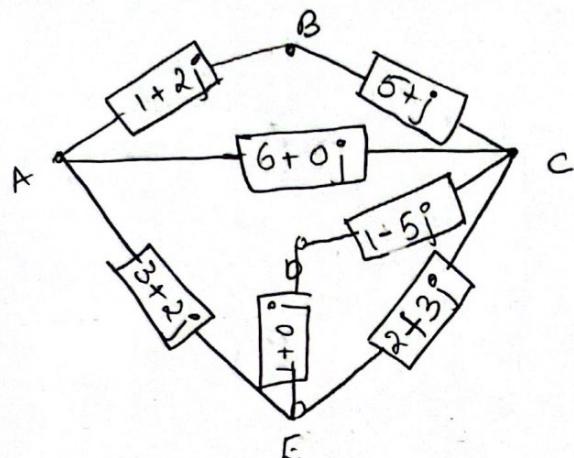
② Off diagonal elements = -ve of conductance between the two terminals involved.

## 11

### Rules for constructing indefinite admittance matrix

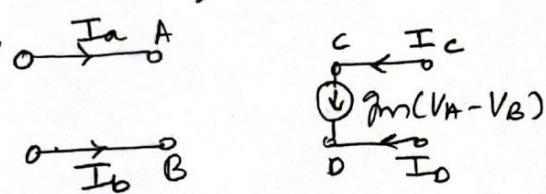
- 1) No. of rows & columns = no. of network terminals.
- 2) Main diagonal term  $y_{pp}$  = sum. of all admittances incident on P
- 3) Off diagonal term  $y_{pq}$  = -ve of all admittances connected b/w p & q.

Eg:



$$\begin{array}{c|ccccc|c} & A & B & C & D & E \\ \hline A & [10+4j-(1+2j)-(6+0j)] & 0 & & & -(3+2j) \\ B & -(1+2j) & 6+3j-(5+j) & 0 & & 0 \\ C & -(6+0j)-(5+j) & 14-j & -(1-5j)(2+3j) & & \\ D & 0 & 0 & -(1-5j) & 2-5j & -(1+0j) \\ E & -(3+2j) & 0 & -(2+3j)(1+0j) & 6+5j & \end{array}$$

### Indefinite admittance matrix for controlled source



$$KCL \Rightarrow I_a = I_b = 0$$

$$I_c = -I_d = gm(V_A - V_B)$$

$$\Rightarrow Y = \begin{bmatrix} a & b & c & d \\ b & 0 & 0 & 0 \\ c & 0 & 0 & 0 \\ d & 0 & 0 & 0 \end{bmatrix}$$

$$\begin{bmatrix} a & b & c & d \\ 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 \\ gm & -gm & 0 & 0 \end{bmatrix}$$

$$\begin{bmatrix} a & b & c & d \\ 0 & 0 & 0 & 0 \\ -gm & gm & 0 & 0 \end{bmatrix}$$

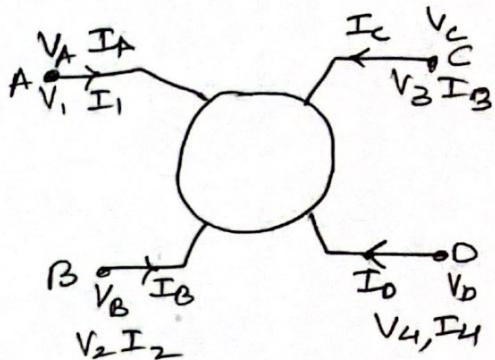
### Rules for constructing matrix

- 1) Non zero elements occur at columns defined by controlling voltages & rows defined by controlled current source.
- 2) Column of the reference voltage & row of the reference current nodes gives an element with the sign.  $+gm$ . Rest of elements' signs are assigned by  $\Sigma_{row=0}$  &  $\Sigma_{column=0}$  rules.

> For N port network we have  $2N$  terminals. To use one N port description we must ensure the currents entering & leaving each port are equal & that we don't care about voltages b/w 2 port terminals. If these two conditions are not satisfied we must use the  $2N$  terminal description.

Q1.5 Given a 4 terminal network & its indefinite admittance matrix

$$\begin{matrix} & A & B & C & D \\ A & 4 & 7 & -15 & 4 \\ B & -11 & -3 & 19 & -5 \\ C & 8 & -13 & 7 & -2 \\ D & -1 & 9 & -11 & 3 \end{matrix}$$



Find admittance matrix if port 1 is  $\textcircled{A}\textcircled{B}$

& port 2 is  $\textcircled{C}\textcircled{D}$

Sol:

$$\begin{bmatrix} I_1 \\ I_2 \\ I_3 \\ I_4 \end{bmatrix} = \begin{bmatrix} 4 & 7 & -15 & 4 \\ -11 & -3 & 19 & -5 \\ 8 & -13 & 7 & -2 \\ -1 & 9 & -11 & 3 \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \\ V_3 \\ V_4 \end{bmatrix}$$

$I_1 = -I_2$  and  $I_3 = -I_4$  for 2 port to be valid.

$$\Rightarrow 7V_1 - 4V_2 = 4V_3 - V_4 \Rightarrow 7V_{12} = 4V_{24} + 4V_{34} \quad \left. \right\} \textcircled{1}$$

$$\qquad \qquad \qquad \Rightarrow 7V_{12} = 4V_{32} - V_{42}$$

$$\begin{bmatrix} I_1 \\ I_2 \\ I_3 \end{bmatrix} = \begin{bmatrix} 4 & 7 & -15 \\ -11 & -3 & 19 \\ 8 & -13 & 7 \end{bmatrix} \begin{bmatrix} V_{14} \\ V_{24} \\ V_{34} \end{bmatrix} \quad \times \quad \begin{bmatrix} I_1 \\ I_3 \\ I_4 \end{bmatrix} = \begin{bmatrix} 4 & -15 & 4 \\ 8 & 7 & -2 \\ -1 & -11 & 3 \end{bmatrix} \begin{bmatrix} V_{12} \\ V_{32} \\ V_{42} \end{bmatrix}$$

We want  $I_1$  &  $I_3$  in terms of  $V_{12}$  &  $V_{24}$

$$\Rightarrow \begin{aligned} I_1 &= 4V_{14} + 7V_{24} - 15V_{34} \\ I_3 &= 8V_{14} - 13V_{24} + 7V_{34} \end{aligned} \quad \left| \begin{aligned} I_1 &= 4V_{12} - 15V_{32} + 4V_{42} \\ I_3 &= 8V_{12} + 7V_{32} - 2V_{42} \end{aligned} \right.$$

From ① subs values of  $V_{32}$  &  $V_{14}$

$$\Rightarrow I_1 = \frac{4}{7} [4V_{24} + 4V_{34}] + 7V_{24} - 15V_{34}$$

$$I_3 = \frac{8}{7} [4V_{24} + 4V_{34}] - 13V_{24} + 7V_{34}$$

$$I_1 = 4V_{12} - \frac{15}{7} [7V_{12} + V_{42}] + 4V_{42}$$

$$I_3 = 8V_{12} + \frac{8}{7} [7V_{12} + V_{42}] - 2V_{42}$$

$$\Rightarrow 7I_1 = 65V_{24} - 89V_{34} \quad \left| \begin{array}{l} 4I_1 = -89V_{12} - V_{24} \\ 4I_3 = +81V_{12} + V_{24} \end{array} \right.$$

$$7I_3 = -59V_{24} + 81V_{34}$$

$$\Rightarrow 7I_1 = 65[-89V_{12} - 4I_1] - 89V_{34}$$

$$7I_3 = -59[-81V_{12} + 4I_3] + 81V_{34}$$

$$\Rightarrow 7I_1 = -5785V_{12} - 260I_1 - 89V_{34} \quad \left. \begin{array}{l} 26I_1 = -5785V_{12} - 89V_{34} \\ 243I_3 = 4779V_{12} + 81V_{34} \end{array} \right\} \Rightarrow$$

$$7I_3 = 4779V_{12} - 236I_3 + 81V_{34}$$

$$\Rightarrow I_1 = -21.67V_{12} - 0.33V_{34}$$

$$I_3 = 19.67V_{12} + 0.33V_{34}$$

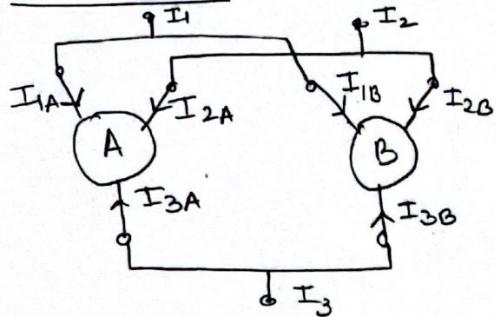
$$\Rightarrow \begin{bmatrix} I_1 \\ I_3 \end{bmatrix} = \begin{bmatrix} -21.67 & -0.33 \\ 19.67 & +0.33 \end{bmatrix} \begin{bmatrix} V_{12} \\ V_{34} \end{bmatrix}$$

verified to be correct

## Ch 2 :

Parallel connection of terminals and ports.

### ① Terminals.

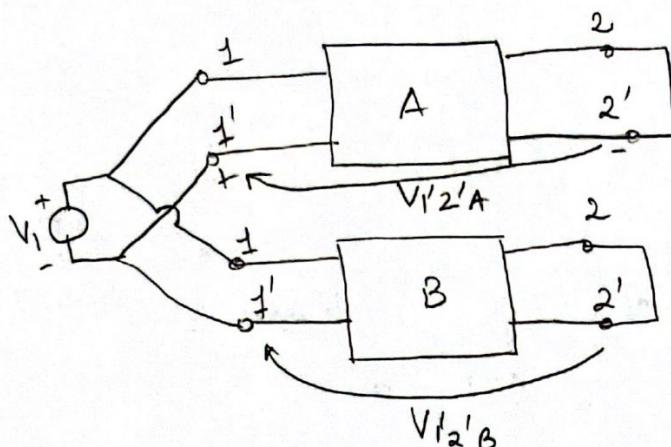


> In this case the indefinite admittance matrices add.

$$Y = Y_A + Y_B$$

### ② Ports

> In order to add the two matrices the ports must satisfy the "validity test"



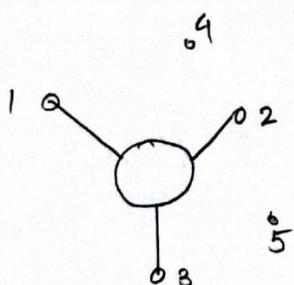
Validity test

$$V_{1'2'A} = V_{1'2'B}$$

This ensures  $I_1 = -I_1'$

> If the condition is not satisfied, the two port must be treated as 4 terminal & additional parameters would be needed to fully describe the system.

Unconnected terminals

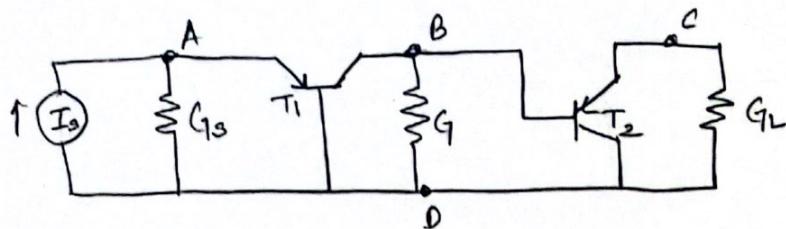


	1	2	3	4	5
1	$y_{11}$	$y_{12}$	$y_{13}$	0	0
2	$y_{21}$	$y_{22}$	$y_{23}$	0	0
3	$y_{31}$	$y_{32}$	$y_{33}$	0	0
4	0	0	0	0	0
5	0	0	0	0	0

> Add zero row & zero column to the original matrix for each new unconnected terminal.

- Now we have two powerful tools to describe complex networks.
- Parallel combination & unconnected terminals.

Eg:

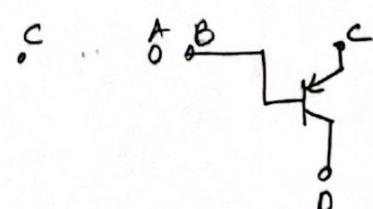
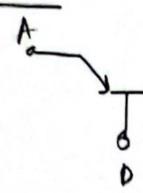
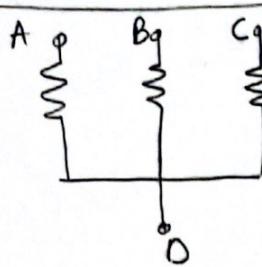


T<sub>1</sub>, Common base parameters:  $y_{11} = 80$     $y_{12} = -1$     $y_{21} = -180$     $y_{22} = 2$

T<sub>2</sub>  $\Rightarrow Y_{ee} = 60$ ;  $y_{bb} = 10$ ;  $y_{cc} = 1$ ;  $y_{bc} = 1$ .

$G_s = 2$ ;  $G_L = 5$ ;  $G = 0.5$

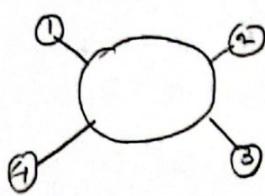
Sol: Divide into 3 circuits.  $\rightarrow$  Notice we can add them all in parallel.



$$\begin{bmatrix} 2 & 0 & 0 & -2 \\ 0 & 0.5 & 0 & -0.5 \\ 0 & 0 & 5 & -5 \\ -2 & -0.5 & -5 & 7.5 \end{bmatrix} + \begin{bmatrix} 80 & -1 & 0 & -79 \\ -180 & 2 & 0 & 179 \\ 0 & 0 & 0 & 0 \\ 100 & -1 & 0 & -99 \end{bmatrix} + \begin{bmatrix} 0 & 0 & 0 & 0 \\ 0 & 10 & -11 & 1 \\ 0 & -58 & 60 & -2 \\ 0 & 48 & -119 & 1 \end{bmatrix} = \begin{bmatrix} 82 & -1 & 0 & -88 \\ -180 & 12.5 & -111 & 0 \\ 0 & -58 & 65 & -7 \\ 98 & 46.5 & -54 & 90 \end{bmatrix}$$

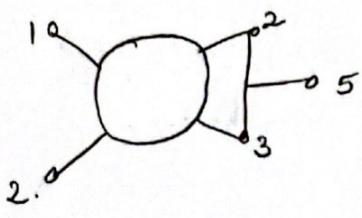
Fill in unknown values  
using  $\sum_{\text{row}} = \sum_{\text{column}} = 0$

Short circuit of 2 terminals.



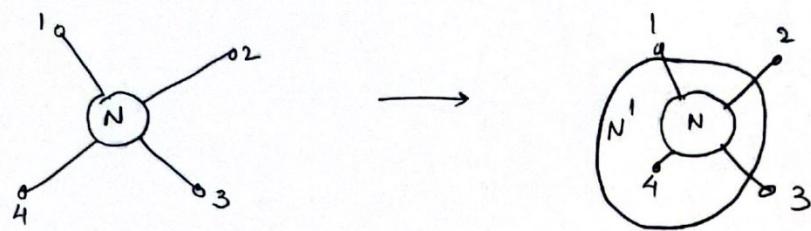
$$\begin{bmatrix} y_{11} & y_{12} & y_{13} & y_{14} \\ y_{21} & y_{22} & y_{23} & y_{24} \\ y_{31} & y_{32} & y_{33} & y_{34} \\ y_{41} & y_{42} & y_{43} & y_{44} \end{bmatrix}$$

↓ add rows 2 & 3 and also columns 2 & 3 because new  $I_5 = I_2 + I_3$   
 $\Delta V_2 = V_3 = V_5$



$$\begin{bmatrix} y_{11} & y_{12} + y_{13} & y_{14} \\ y_{21} + y_{31} & y_{22} + y_{23} + y_{32} + y_{33} & y_{24} + y_{34} \\ y_{41} & y_{42} + y_{43} & y_{44} \end{bmatrix}$$

Pivotal Condensation. → Suppressing a terminal that is of no interest.



$$\begin{bmatrix} Y_{11} & Y_{12} & Y_{13} & Y_{14} \\ Y_{21} & Y_{22} & Y_{23} & Y_{24} \\ Y_{31} & Y_{32} & Y_{33} & Y_{34} \\ Y_{41} & Y_{42} & Y_{43} & Y_{44} \end{bmatrix} \rightarrow \frac{1}{Y_{44}} \begin{bmatrix} Y_{11}Y_{44} - Y_{14}Y_{41} & Y_{12}Y_{44} - Y_{14}Y_{42} & Y_{13}Y_{44} - Y_{14}Y_{43} \\ Y_{21}Y_{44} - Y_{24}Y_{41} & Y_{22}Y_{44} - Y_{24}Y_{42} & Y_{23}Y_{44} - Y_{24}Y_{43} \\ Y_{31}Y_{44} - Y_{34}Y_{41} & Y_{32}Y_{44} - Y_{34}Y_{42} & Y_{33}Y_{44} - Y_{34}Y_{43} \end{bmatrix}$$

> The whole matrix is divided by  $Y_{mm}$  if  $m^{\text{th}}$  element is suppressed.

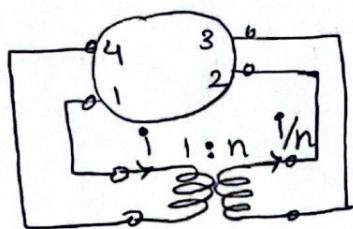
> Replace  $Y_{jk}' = Y_{jk} Y_{mm} - Y_{jm} Y_{mk}$

>  $Y_{mm}$  is called the pivot. Main diagonal elements are preferred as pivots because they are rarely = 0.

> Some procedure can be followed for port admittance matrix to suppress an open circuited port.

> If a port is short circuit in the port admittance matrix then a reduced port admittance matrix can be obtained by simply eliminating the associated row & column.

## Introducing an ideal transformer.



Constraints

$$\rightarrow (V_2 - V_3) = n(V_1 - V_4)$$

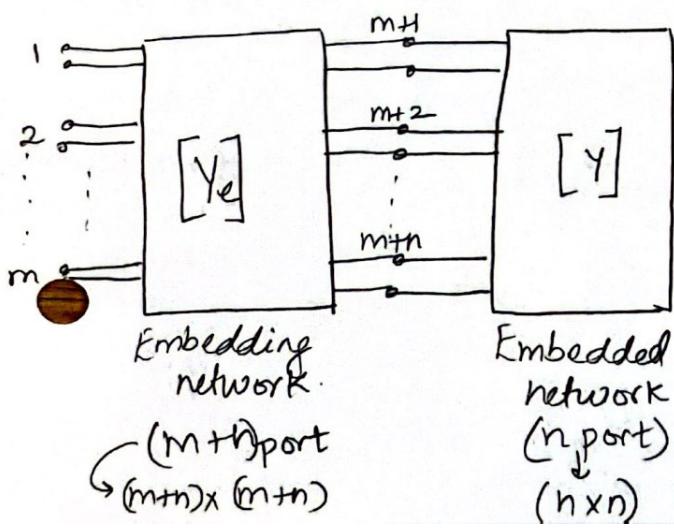
$\rightarrow i \text{ & } i/n$  must be included in KCL.

$$\Rightarrow \begin{bmatrix} I_1 - i \\ I_2 + i/n \\ I_3 - i/n \\ I_4 + i \end{bmatrix} = \begin{bmatrix} Y_{11} + ny_{12} & Y_{13} + Y_{12} & Y_{14} - ny_{12} \\ Y_{21} + ny_{22} & Y_{23} + Y_{22} & Y_{24} - ny_{22} \\ Y_{31} + ny_{32} & Y_{33} + Y_{32} & Y_{34} - ny_{32} \\ Y_{41} + ny_{42} & Y_{43} + Y_{42} & Y_{44} - ny_{42} \end{bmatrix} \begin{bmatrix} V_1 \\ V_3 \\ V_4 \end{bmatrix}$$

$\rightarrow$  We have eliminated  $V_2$  (arbitrarily). Since we don't care about  $i$  we can eliminate it.

$$\begin{bmatrix} I_1 + nI_2 \\ I_3 + I_2 \\ I_4 - nI_2 \end{bmatrix} = \begin{bmatrix} Y_{11} + ny_{12} & Y_{13} + Y_{12} & Y_{14} - ny_{12} \\ +ny_{21} + n^2y_{22} & +ny_{23} + ny_{22} & ny_{24} - n^2y_{22} \\ Y_{31} + ny_{32} & Y_{33} + Y_{32} & Y_{34} - ny_{32} \\ +y_{21} + ny_{22} & +y_{23} + y_{22} & +y_{24} - ny_{22} \\ Y_{41} + ny_{42} & Y_{43} + Y_{42} & Y_{44} - ny_{42} \\ -ny_{21} - n^2y_{22} & -ny_{23} - ny_{22} & -ny_{24} + n^2y_{22} \end{bmatrix}$$

## General port embedding:



$$Y_e = \begin{bmatrix} Y_{11} & \dots & Y_{1m} & | & Y_{1(m+1)} & \dots & Y_{1(m+n)} \\ \vdots & & \vdots & | & \vdots & & \vdots \\ Y_{m1} & \dots & Y_{mm} & | & Y_{m(m+1)} & \dots & Y_{m(m+n)} \\ Y_{(m+1)1} & \dots & Y_{(m+1)m} & | & Y_{(m+1)(m+1)} & \dots & Y_{(m+1)(m+n)} \\ \vdots & & \vdots & | & \vdots & & \vdots \\ Y_{(m+n)1} & \dots & Y_{(m+n)m} & | & Y_{(m+n)(m+1)} & \dots & Y_{(m+n)(m+n)} \end{bmatrix}$$

$$\Rightarrow Y_e = \begin{bmatrix} Y_{mm} & | & Y_{mn} \\ \hline Y_{nm} & | & Y_{nn} \end{bmatrix}$$

→ gives  $m \times m$  port

$$\Rightarrow Y' = Y_{mm} - Y_{mn} [Y_{nn} + Y]^T Y_{nm}$$

→ The form is similar to pivotal condensation since here we are suppressing a matrix and not just a part.

Ch 3 → only exists for square matrices.

### Determinant of a matrix

#### ① Minors

Given  $A = \begin{bmatrix} Y_{11} & Y_{12} & Y_{13} & Y_{14} \\ Y_{21} & Y_{22} & Y_{23} & Y_{24} \\ Y_{31} & Y_{32} & Y_{33} & Y_{34} \\ Y_{41} & Y_{42} & Y_{43} & Y_{44} \end{bmatrix}$

Minor  $M_{32} = \begin{vmatrix} Y_{11} & Y_{13} & Y_{14} \\ Y_{21} & Y_{23} & Y_{24} \\ Y_{41} & Y_{43} & Y_{44} \end{vmatrix}$  → Remove corresponding row & column & take determinant.

$$M_{12,3} = \begin{vmatrix} Y_{21} & Y_{24} \\ Y_{31} & Y_{34} \end{vmatrix}$$

- ② Second minor ⇒ Remove two rows & columns & take det.
- ③ Principal minor ⇒ Remove identical row & column ⇒  $M_{11}, M_{22}, M_3$
- ④ Cofactor is a signed minor.

Cofactor of element  $Y_{32}$  is  $Y_{32} = (-1)^{3+2} \cdot M_{32} = - M_{32}$

#### ⑤ Determinant

Laplace {Take any row or column & form corresponding cofactors development for each element. Multiply corresponding elements & of determinant cofactors & add the products.}

#### Rules

- ① The algebraic sign is changed for the det if any two rows or columns are interchanged.
- ② If any 2 rows or columns are identical,  $\det = 0$ .
- ③ Det is unchanged if to any row, a linear combination of any of its other rows is added. Same applies to columns.

## Inverse.

$$A^{-1} = \frac{(\text{cofactor of } A)^T}{\det(A)}$$

$\det(A) = 0 \Rightarrow$  Singular matrix  $\Rightarrow$  no inverse!

$$I = YV$$

$$\& V = ZI \text{ where } Z = Y^T.$$

## Tramer's rule.

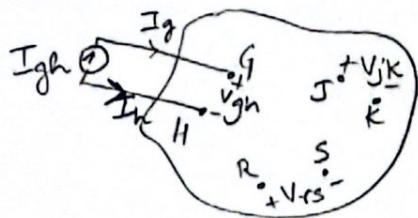
$$\begin{bmatrix} y_1 \\ y_2 \\ \vdots \\ y_n \end{bmatrix} = \begin{bmatrix} a_{11} & a_{12} & \dots & a_{1n} \\ a_{21} & a_{22} & \dots & a_{2n} \\ \vdots & \vdots & \ddots & \vdots \\ a_{n1} & a_{n2} & \dots & a_{nn} \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \\ \vdots \\ x_n \end{bmatrix}$$

$$\Rightarrow x_1 = \frac{\begin{vmatrix} y_1 & a_{12} & a_{13} & \dots & a_{1n} \\ y_2 & a_{22} & a_2 & \dots & a_{2n} \\ \vdots & \vdots & \ddots & \ddots & \vdots \\ y_n & a_{n2} & \dots & \dots & a_{nn} \end{vmatrix}}{|A|}$$

$$x_k = \frac{\begin{vmatrix} a_{11} & \dots & y_1 & \dots & a_{1n} \\ a_{12} & \dots & y_2 & \dots & a_{2n} \\ \vdots & \ddots & \vdots & \ddots & \vdots \\ a_{1m} & \dots & y_n & \dots & a_{nn} \end{vmatrix}}{|A|}$$

→ replace column k by column y & take det.

## \* General Solutions of $n$ terminal networks.



- > Assume an arbitrary network that is driven by current source at terminals G, H.
- > If more current sources exist, use superposition.

- > If all terminals except G & H are left open, then the transfer admittance b/w G,H & J,K is  $\frac{I_{gh}}{V_{jk}} = \text{sgn}(k-j)\text{sgn}(h-g) \cdot \frac{Y_{uv}}{Y_{jk, hk}}$
- >  $Y_{uv}$  is any cofactor chosen arbitrarily. A smart choice would be such that rows u & column v do not have any zeros.
- > The 1<sup>st</sup> cofactors of all elements of an indefinite admittance matrix are equal.
- >  $\text{sgn}(k-j) = \text{sign of } (k-j) \cdot \text{if } k=3, j=1 \Rightarrow \text{sgn}(k-j) = \text{true.}$
- >  $Y_{jk, hk}$  is the cofactor formed by deleting the ~~rows g,h~~  
~~current excitation~~  
~~voltage observed.~~
- > Note here the indefinite admittance matrix is not the same as the port admittance matrix since all other terminals are short & not open  $\rightarrow$  indefinite.

port.

$$\begin{bmatrix} 0 \\ 0 \\ 0 \\ \vdots \\ I_g \\ I_h \\ 0 \\ 0 \end{bmatrix} = \begin{bmatrix} Y_{11} & Y_{12} & Y_{13} & \dots & Y_{1n} \\ \vdots & & & & \\ Y_{g1} & Y_{g2} & \dots & & Y_{gn} \\ Y_{h1} & Y_{h2} & \dots & & Y_{hn} \\ Y_{n1} & Y_{n2} & \dots & & Y_{nn} \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \\ \vdots \\ V_n \end{bmatrix}$$

> Other network properties are similarly derived.

① Self admittance at input =  $\frac{I_{gh}}{V_{gh}} = \frac{Y_{uv}}{Y_{gg, hh}}$

Take (-1)<sup>g+g+h+h</sup> x det after  
delete row g, column a  
row h, column h

② Voltage gain b/w J,K & R,S when current drive is at G<sub>2H</sub>.

$$G_{jk,rs}^{gh} = \left. \frac{V_{jk}}{V_{rs}} \right|_{I_{gh}} = \text{sgn}(s-r) \text{sgn}(k-j) \frac{Y_{gi,hk}}{Y_{gr, hs}}$$

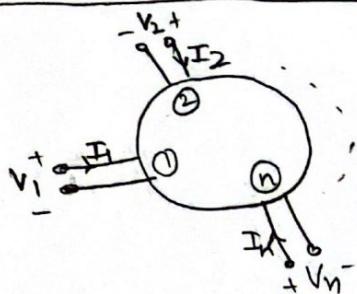
③ Driving point voltage gain.

$$G_{jk, gh}^{gh} = \left. \frac{V_{jk}}{V_{gh}} \right|_{I_{gh}} = \text{sgn}(h-g) \text{sgn}(k-j) \frac{Y_{gi, hk}}{Y_{gg, hh}}$$

→ See examples on Pg 23.

→ All the <sup>first</sup> cofactors of the indefinite admittance matrix are equal & hence it is aka Equi cofactor matrix. This is because each row & column sum to zero. Proof is complex. Need to manipulate rows & columns s.t. det Y remains unchanged.

### n-Port Network Solution.



$$\mathbf{I} = \mathbf{Y} \mathbf{V} \rightarrow \begin{bmatrix} I_1 \\ I_2 \\ \vdots \\ I_n \end{bmatrix} = [\mathbf{Y}] \begin{bmatrix} V_1 \\ V_2 \\ \vdots \\ V_n \end{bmatrix}$$

> Remember Y parameters are defined by voltage excitations & short circuit currents measured. Unlike how indefinite admittance matrices are defined (open).

> We want open circuit voltages w.r.t current excitations which comes from Z parameters.  $\mathbf{V} = \mathbf{Z} \mathbf{I}$ . Where  $\mathbf{Z} = \mathbf{Y}^T$ .

> The general sol. of  $Z = Y^{-1}$  is what we derived earlier said that the same could be applied to ports.

> Let  $I_r$  be a current excitation at port R &  $V_s$  be open circuit voltage at port S. (all other ports are open).

From Gramme's rule  $V_s = \frac{I_r \cdot \Delta_{rs}}{\Delta} \rightarrow$  cofactor of element  $y_{rs}$

$$\Rightarrow Z_{sr} = \frac{V_s}{I_r} = \frac{\Delta_{rs}}{\Delta} \rightarrow \boxed{y_{sr} = \frac{\Delta}{\Delta_{rs}}} \text{ Transfer admittance}$$

$\Delta$  is det  $Y$  &  $\Delta_{rs}$  is cofactor of element  $y_{rs}$ .  
 $S \rightarrow$  response  $V$ .

defined for open circuit transfer conditions unlike  $Y$  parameters.

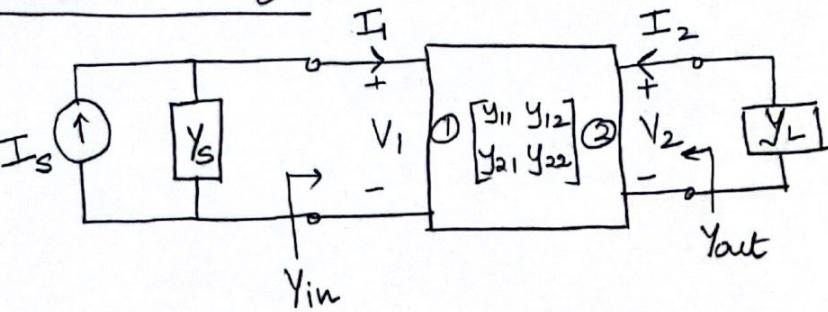
> However  $\Delta_{rs}$  &  $\Delta$  are derived from short circuit port admittance matrix  $Y$ .  $\Delta$  is det  $Y$  &  $\Delta_{rs}$  is cofactor of element  $y_{rs}$ .

>  $y_{sr}$  is the reciprocal of the open circuited port impedance  $Z_{sr}$ .

> Similarly if a network is described by the port impedance matrix  $Z$  (which is defined for open circuit configuration), but if the short circuit port current response  $I_q$  to an impressed voltage  $V_p$  is required when all other ports are shorted, the corresponding  $Z_{pq} = \frac{V_p}{I_q} = \frac{\Delta}{\Delta_{pq}} \rightarrow$  det  $Z$  } of the matrix  $\Delta_{pq} \rightarrow$  cofactor of  $z_{pq}$

$$> Z_{pq} = \frac{1}{y_{pq}} .$$

## Two Port



$$I_1 = I_s - V_1 Y_s$$

$$I_2 = Y_{21} V_1 + Y_{22} V_2$$

$$I_1 = Y_{11} V_1 + Y_{12} V_2$$

$$I_2 = -V_2 Y_L$$

From earlier discussion of self & transfer admittance we can derive.

$$Y_{in} = Y_{11} - \frac{Y_{12} Y_{21}}{Y_{22} + Y_L}$$

$$Y_{out} = Y_{22} - \frac{Y_{21} Y_{12}}{Y_{11} + Y_s}$$

$$\frac{V_2}{V_1} = -\frac{Y_{21}}{Y_{22} + Y_L}$$

→ Impedance → impedance + admittance.

Transfer impedance  $\Rightarrow Y_p \& O/p$  are different.

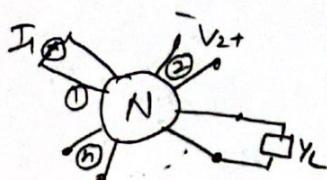
Driving port impedance  $\Rightarrow Y_p \& O/p$  are same.

In general, input impedance can be expressed with any parameter

$$Y_{in} = Y_{11} - \frac{Y_{12} Y_{21}}{Y_{22} + Y_L}; \quad Y_{in} = K_{11} - \frac{K_{12} K_{21}}{K_{22} + Z_L}$$

$$Z_{in} = Z_{11} - \frac{Z_{12} Z_{21}}{Z_{22} + Z_L} \quad Z_{in} = h_{11} - \frac{h_{12} h_{21}}{h_{22} + Y_L}$$

→ To get more insight let us see how changing an arbitrary admittance in a network affects the transfer or driving port impedance elsewhere in the network.



$Y_L \rightarrow$  arbitrary impedance.

$$[I] = [Y] [V]$$

Let us see how  $Y_L$  affects  $Y_{21}$ . We know,

$$Y_{21} = \frac{I_1}{V_2} = \frac{\Delta}{\Delta_{12}} \quad \begin{array}{l} \text{↑ def. of port admittance matrix.} \\ \text{from earlier.} \end{array}$$

$\downarrow$  cofactor of  $Y_{12}$  element.

> In terms of  $Y_L$  :

$$\Delta_{12} = \Delta^o + Y_L \cdot \Delta_{33,12}$$

$$\Delta = \Delta^o + Y_L \Delta_{33}$$

$\left\{ \begin{array}{l} \Delta_{12}^o \text{ & } \Delta^o \text{ are} \\ \Delta_{12} \text{ & } \Delta \text{ when } Y_L = 0 \end{array} \right.$

$$\Rightarrow Y_{21} = \frac{\Delta^o + Y_L \Delta_{33}}{\Delta_{12}^o + Y_L \Delta_{33,12}} \quad (3.58)$$

$$y = \frac{a+by}{c+dy}$$

Let  $k_1+k_2 = b/d$ ;  $k_2-k_1 = a/c$ ;  $m = c/d$ ;

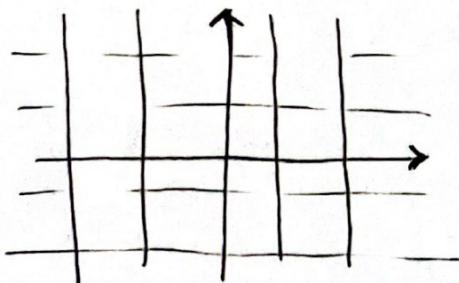
Any transfer or deriving port impedance is related to any single element by the bilinear transformation.  $y = \frac{a+by}{c+dy}$

$$\Rightarrow Y = k_1 \lambda + k_2; \text{ where } \lambda = \frac{y-m}{y+m}. \quad (3.61)$$

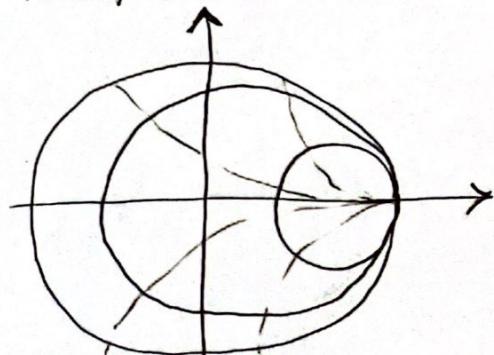
$$\text{Let } Y_N = Y/m$$

$$\Rightarrow \lambda = \frac{Y_N - 1}{Y_N + 1} \quad \text{where } \begin{array}{l} a = \Delta^o; b = \Delta_{33} \\ c = \Delta_{12}^o; d = \Delta_{33,12} \\ y = Y_L \end{array}$$

$Y_N$  plane.



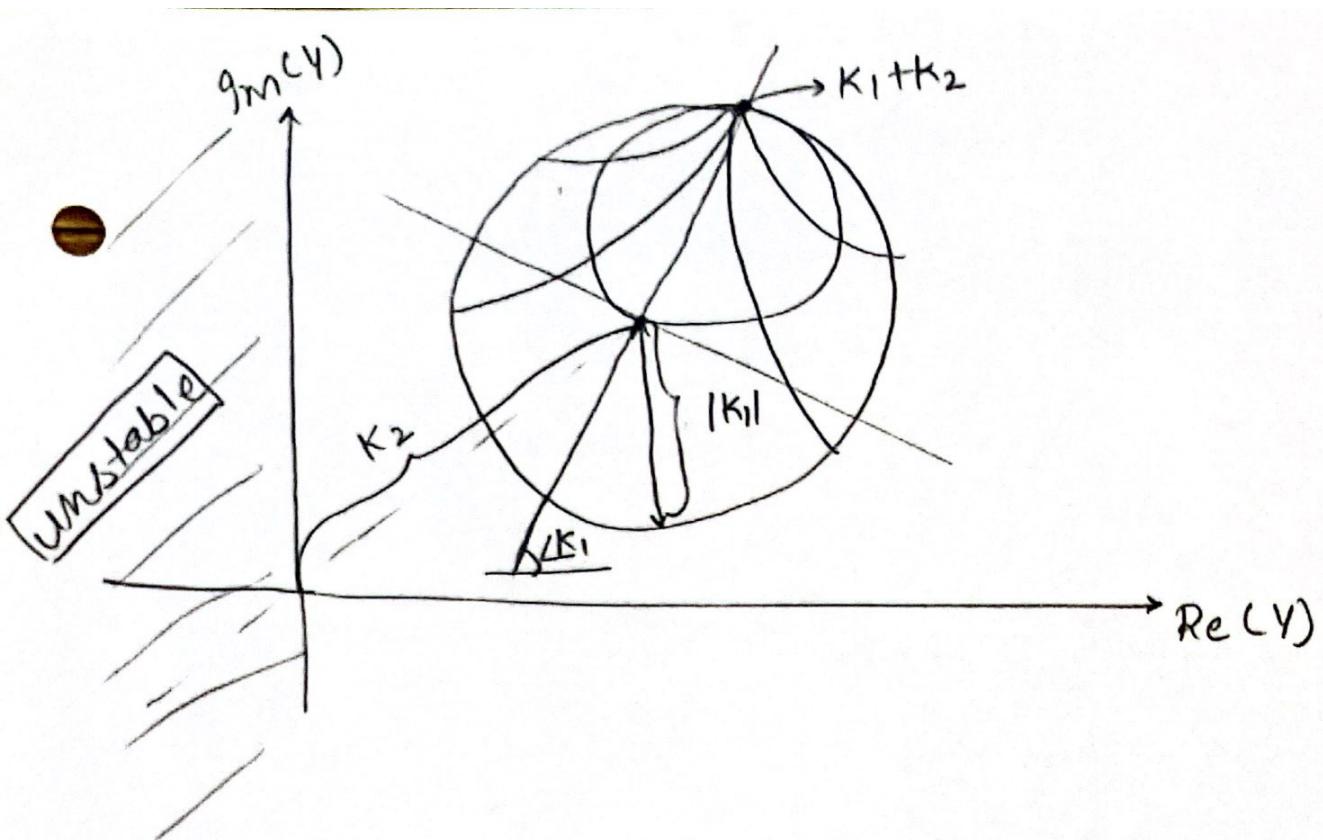
$\lambda$  plane.



$y$  to  $\lambda$  is a pure bilinear transform as shown above?

$\lambda$  to  $Y_N$  is a scaling & shifting as shown by 3.61.

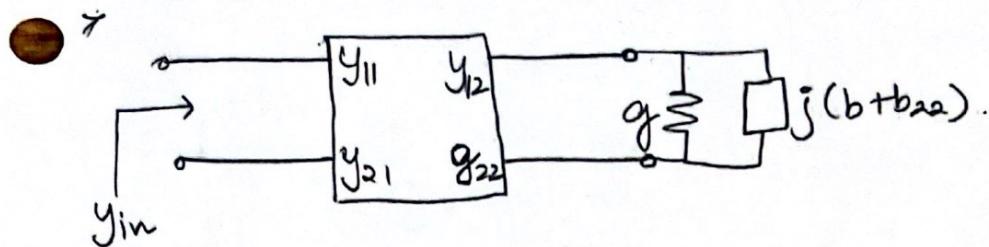
The unit circle is scaled by  $|k_1|$ , rotated by  $\angle k_1$  & shifted or translated by  $k_2$ .



## Example: Alignability of amplifier

75

> Changing output reactance to change input impedance.



> \$g+jb\$ is load.

> \$g\_{22}+jb\_{22}\$ is part of circuit

> Here we have lumped \$b+b\_{22}\$.

> We would like to see the effect of \$j(b+b\_{22})\$ on \$y\_{in}\$.

$$\Rightarrow Y = \begin{bmatrix} y_{11} & y_{12} \\ y_{21} & g_{22} + g + j(b_{22} + b) \end{bmatrix}$$

$$\Rightarrow \Delta^o = \Delta / b + b_{22} = 0 = y_{11}(g_{22} + g) - y_{12}y_{21}$$

$$\Delta_{22} = y_{11}$$

$$\Delta_{11} = g_{22} + g.$$

$$\Delta_{22,11} = \text{unity } (\because \text{matrix is empty})$$

$$\Rightarrow Y_{IN} = Y_{11} = \frac{y_{11}(g_{22} + g) - y_{12}y_{21} + y_{11}j(b + b_{22})}{(g_{22} + g) + j(b + b_{22})}$$

$$\text{Here, } m = g_{22} + g$$

$$\Rightarrow \text{Transforming from } \lambda \text{ plane to } Y_{IN} \text{ plane} \Rightarrow k_1 + k_2 = y_{11}$$

$$k_1 = -\frac{y_{12}y_{21}}{2(g + g_{22})}$$

$$\Rightarrow b + b_{22} \rightarrow \pm \infty \Rightarrow Y_{IN} = Y_{11} = k_1 + k_2$$

> which should be intuitive since this implies no load reactance.  $\Rightarrow$  independant of \$g\$!

>  $k_1 = -\frac{1}{y_{12}y_{21}}$  is also independant of \$g\$.

> Stability depends on \$|k\_1| > g\$, on \$-\frac{1}{y\_{12}y\_{21}}\$ and on real part of \$y\_{11}\$  
 $\Rightarrow g_{11}$ . ...  $\approx -1/\alpha \Rightarrow$  unstable when there is no load.

\$Y\_{IN}\$

\$\uparrow\$



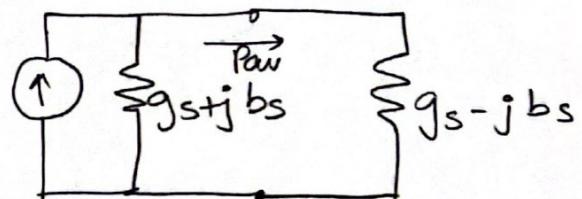
\$G\_{IN}\$

# Power Gain

## Available power

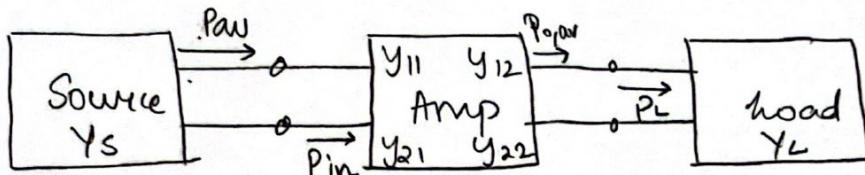
"Power delivered to a matched load".

$$P_{av} = \frac{|I_s|^2}{4g_s}$$



> This is also the maximum power delivered.

> If we need higher power delivered we need power gain.  
> A matched load is used to calculate  $P_{av}$ .



## (I) Transducer power gain $G$ → (most meaningful)

$$G = \frac{P_L}{P_{av}} = \frac{\text{Power delivered to load}}{\text{Max. power the source can deliver}}$$

>  $G < 1 \Rightarrow$  passive.

>  $G = 1$  for matched load or ideal transformer matching.

$$G = \frac{P_L}{P_{av}} = 4g_s g_L \left| \frac{V_L}{I_s} \right|^2$$

$$= \frac{4 |Y_{21}|^2 g_s g_L}{|(Y_{11} + g_s)(Y_{22} + g_L) - Y_{12} Y_{21}|^2}$$

$$\therefore \frac{V_L}{I_s} = \frac{Y_{21}}{(Y_{11} + g_s)(Y_{22} + g_L) - Y_{12} Y_{21}}$$

## (II) Power gain ( $G_p$ ):

$$G_p = \frac{P_L}{P_{in}} = \frac{\text{Power delivered to load}}{\text{power supplied to amp i/p by the source}}$$

>  $P_{in} \leq P_{av}$  always.  $P_{in} = P_{av}$  if Amp  $\gamma_p$  is matched to source

$\therefore G_p \geq G \Rightarrow$  can be deceptive.

Q7

>  $G_p$  also is  $\leq 1$  for passive networks.

### (III) Available power gain ( $G_A$ )

> If Amp. output is not matched, the power at the output  $\neq P_L$

$\therefore G_A = \frac{P_{o,av}}{P_{av}} = \frac{\text{Power delivered to load matched at } \Omega_p \text{ of Amp}}{\text{power delivered by source matched at } Y_p \text{ of Amp}}$

> It is also deceptive.

>  $G_A = G \Rightarrow$  output is conjugate matched.

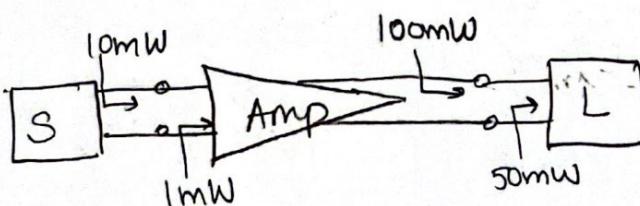
$\Rightarrow G_A \geq G$ .

### (IV) Conjugate matched power gain ( $G_C$ )

>  $G_C = G_A = \frac{P_{o,av}}{P_{av}}$  = power gain when both  $Y_p$  &  $\Omega_p$  are matched

> Conjugate matching may lead to instability at very high freq.

Eg:



$\Rightarrow$

$$G = 5; G_p = 50; G_A = 10$$

## Ch:4

### Signal Power

$$\text{If } v(t) = \hat{V} \cos \omega t \quad \left. \begin{array}{l} \\ \end{array} \right\} \text{Delivered to a one port-} \\ i(t) = \hat{I} \cos(\omega t + \phi) \quad \left. \begin{array}{l} \\ \end{array} \right\}$$

① Instantaneous power:  $p(t) = v(t)i(t)$

$$= \frac{\hat{V}\hat{I}}{2} \cos \phi + \frac{\hat{V}\hat{I}}{2} \cos(2\omega t + \phi)$$

constant & max  
when  $\phi = 0$

- Varies with time.
- Even when load is purely resistive it varies since  $v(t)$  &  $i(t)$  vary. Varies at 2x freq.

② Average power ( $P_{av}$ )

$$P_{av} = \frac{1}{T} \int_{t_0}^{t_0+T} p(t) dt = \frac{\hat{V}\hat{I}}{2} \cos \phi \quad \text{avg. power is constant.}$$

Decomposing  $i(t)$  into inphase & quadrature components:

$$p(t) = \frac{\hat{V}\hat{I}}{2} \cos \phi + \frac{\hat{V}\hat{I}}{2} \cos 2\omega t \cos \phi - \frac{\hat{V}\hat{I}}{2} \sin 2\omega t \sin \phi$$

(Inphase I) → (out of phase II)

$$= \underbrace{\frac{\hat{V}}{2} (1 + \cos 2\omega t)}_{V \& I \text{ are in phase \& this power is dissipated in the circuit}} \hat{I} \cos \phi - \underbrace{\frac{\hat{V}}{2} \sin 2\omega t \cdot \hat{I} \sin \phi}_{V \& I \text{ are out of phase \& power is stored in reactive components.}}$$

↓  
Active Signal power  
avg value is  $\frac{1}{2} \hat{V}\hat{I} \cos \phi$

Reactive signal power.  
avg value is zero  
Amplitude is  $\frac{1}{2} \hat{V}\hat{I} \sin \phi$

In phasors.

$$V = \hat{V} e^{j\theta}$$

$$I = \hat{I} e^{j(\theta+\phi)}$$

$$\Rightarrow V^* I = \hat{V} e^{-j\theta} \hat{I} e^{j(\theta+\phi)}$$

$$V^* I = \hat{V} \hat{I} \cos \phi + j \hat{V} \hat{I} \sin \phi$$

Complex signal power ( $P$ )

Real signal power ( $P_R$ )

Imaginary signal power ( $P_I$ )

> Notice complex conjugate product gives what we got earlier.

But  $P_R = 2 P_{active}$ . &  $P_I = 2 \times \text{amplitude of } P_{reactive}$ .

To avoid carrying this  $\frac{1}{2}$  everywhere we assume  $V$  &  $I$  in phasors are rms values.  $V_{rms} = \frac{V}{\sqrt{2}}$ .

$$P = V^* I = P_R + j P_I$$

Avg. power dissipated

peak value of reactive power oscillation.

> We care about real power. If real power delivered "to" a device is negative  $\Rightarrow$  device is active since  $P_{out} > P_{in}$

$\Rightarrow P_{in} - P_{out}$  is -ve.

> Active device  $\Rightarrow$  Real signal power supplied "to" the element is "capable" of being negative.

> Given a 3 terminal device: The total signal power (real) entering it is:

$$\frac{P_R}{|V_1| |V_2|} = \alpha g_{11} + \bar{\alpha} g_{22} + \operatorname{Re} [(y_{21} + y_{12}^*) e^{j\alpha}]$$

$$\Rightarrow \frac{P_R}{|V_1||V_2|} = (\alpha g_{11} + \alpha^* g_{22}) + |y_{21} + y_{12}^*| \cos[\angle y_{21} + y_{12}^* + \alpha]$$

- > If  $g_{11} < 0$  and/or  $g_{22} < 0$ ,  $P_R$  would be negative. This type of activity is rarely used.
- > If  $g_{11}$  &  $g_{22}$  are positive,  $\frac{P_R}{|V_1||V_2|}$  minimum must be negative.

$$\Rightarrow \left( \frac{P_R}{|V_1||V_2|} \right)_{\min} = +\sqrt{g_{11} g_{22}} \cdot 2 - |y_{21} + y_{12}^*|$$

$$\Rightarrow \boxed{|y_{21} + y_{12}^*|^2 > 4 g_{11} g_{22}}$$

Subtracting  $g_{12} g_{21}$  on both sides  $\Rightarrow$

$$|y_{21} - y_{12}|^2 > 4(g_{11} g_{22} - g_{12} g_{21})$$

$$\Rightarrow \boxed{U = \frac{|y_{21} - y_{12}|^2}{4(g_{11} g_{22} - g_{12} g_{21})}}$$

Mason's invariant.

- >  $U > 1 \Rightarrow$  active!
- >  $U < 0 \Rightarrow$  also active but this type is not used.
- > Why use  $U$ ? It has interesting properties!

- >  $U$  is invariant under lossless, reciprocal embedding. (3)
- >  $U$  is invariant w.r.t CE, CC or CB ( $C_D, C_S$  or  $C_G$ ) .
- > If a device is unilateral ( $\Rightarrow y_{12}=0$ ) and is matched at  $1/P_2 \rightarrow 0/P$  the power gain is equal to the value of  $U$ . A device can be made unilateral with lossless reciprocal embedding.

### Hermitian matrix

$$A^t = A^* \rightarrow \text{Hermitian!} \rightarrow A_H.$$

Transpose      Conjugate

### Skew hermitian matrix

$$A^t = -A^* \rightarrow A_{SH}$$

- >  $j \cdot A_{SH} \rightarrow$  gives a Hermitian matrix.
- > Any matrix can be written as a sum of  $A_H$  &  $A_{SH}$ .  
 $\Rightarrow A = A_H + A_{SH}$ .

$$A_H = \frac{1}{2}(A + A^{*t})$$

$$A_{SH} = \frac{1}{2}(A - A^{*t})$$

### Matrix description of activity.

$$P = V_1^* I_1 + V_2^* I_2 = [V_1^* \ V_2^*] \begin{bmatrix} I_1 \\ I_2 \end{bmatrix} = V^* t I = V^* Y V.$$

$$\Rightarrow P = \underbrace{V^* t Y_H V}_{\text{Hermitian quadratic form}} + \underbrace{V^* t Y_{SH} V}_{\text{purely imaginary}}$$

purely imaginary } imaginary power.

Purely real  $\Rightarrow$  real power

$$\therefore P_R = V^* Y_H V$$

$$j P_I = V^* Y_{SH} V$$

$V^* Y_H V \geq 0 \Rightarrow$  positive semidefinite

$V^* Y_H V > 0 \Rightarrow$  positive definite.

Since device activity is independent of  $V$ , we only test to see if  $Y_H \geq 0$  or not

\* Necessary and sufficient condition that  $Y_H$  is a positive matrix is that all its principal minors are nonnegative.

Eg:

$$Y = \begin{bmatrix} y_{11} & y_{12} \\ y_{21} & y_{22} \end{bmatrix} \text{ Find activity condition for 2 port.}$$

Set:

$$Y_H = \begin{bmatrix} g_{11} & \frac{1}{2}(y_{12} + y_{21}^*) \\ \frac{1}{2}(y_{21} + y_{12}^*) & g_{22} \end{bmatrix}$$

Principal minor of order 0 is  $g_{11}g_{22} - \frac{1}{4}(y_{12} + y_{21}^*)(y_{21} + y_{12}^*)$   
 " " " 1<sup>st</sup> order are  $g_{11}$  &  $g_{22}$ .

$\Rightarrow$  Passivity conditions:

$$\left. \begin{array}{l} g_{11}g_{22} - \frac{1}{4}|y_{12} + y_{21}^*|^2 \geq 0 \\ \text{& either } g_{11} \geq 0 \text{ or } g_{22} \geq 0. \end{array} \right\} \text{These are identical to the ones proved earlier.}$$

(because  $① \otimes ② \Rightarrow ③$ )

$\Rightarrow$  matrix description of activity is more general to n-port.

## Isoclinic power element

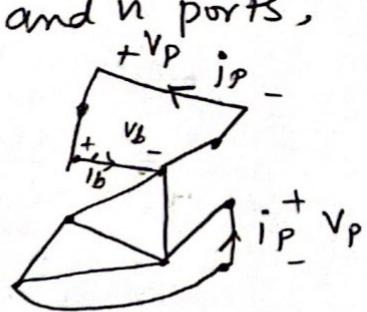
132

- >  $\frac{P_R}{P_I}$  is fixed  $\Rightarrow$  independent of terminal voltages.
- >  $Y = e^{j\theta} Y_{term}$   $\rightarrow$  general form of isoclinic power element

## Ch:5 Actual power theorem

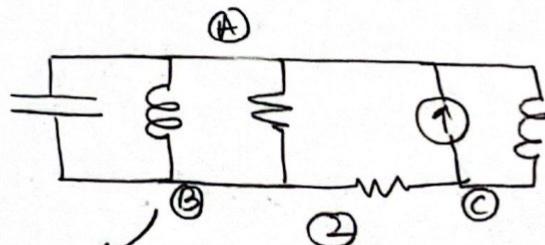
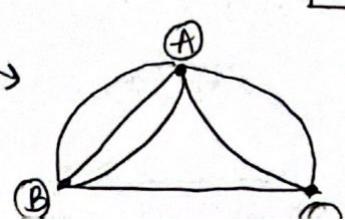
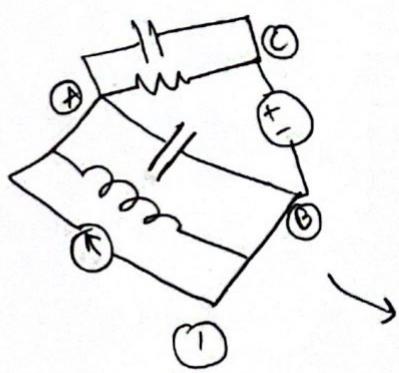
- > Given a certain graph with  $m$  branches and  $n$  ports,

$$\sum_{b=1}^m i_b v_b = \sum_{p=1}^n i_p v_p$$



- > Notice directions of  $i$  in port & branch are opposite.
- > This suggests that total <sup>actual</sup> instantaneous power in a network is zero.
- > Here  $i$  &  $v$  are actual  $i$  &  $v$  and they are found by solving "both" KVL & KCL.
- > However KVL & KCL can individually give an infinite set of voltages & currents respectively. These voltages which satisfy only KVL & currents which satisfy only KCL are called VIRTUAL voltages & current.
- > Two different circuits can have identical topologies. Actual V&I values of ① are virtual values for ② & vice versa.

Eg:



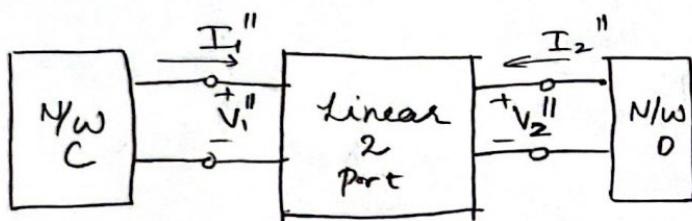
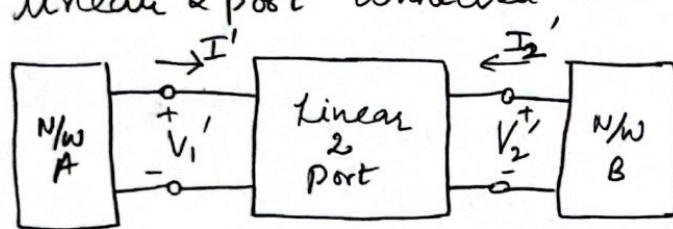
- Tellegen's theorem (aka virtual power theorem).
- $\sum_b i'_b V_b'' = \sum_p i'_p V_p''$  → where  $i'$  are virtual currents from state 1 &  $V''$  are virtual voltages from state 2.
- A much more general form of actual power theorem and depends only on topology.
- Valid for nonlinear, time variant, passive/active etc. as long as Kirchoff's laws are satisfied.

For linear networks

$$\sum_b I'_b V_b'' = \sum_p I'_p V_p'' \rightarrow (\text{aka strong Tellegen's theorem})$$

Reciprocity

Given a linear 2 port connected to 2 networks.



If  $\sum_b (I'_b V_b'' - I_b'' V_b') = 0$ , (5.15)

Then,  $[I_1' V_1'' + I_2' V_2'' = I_1'' V_1' + I_2'' V_2']$  is satisfied.

- Where  $I_b$  &  $V_b$  are branch currents & voltages inside the 2port.
- Eq. 5.15 is the most general statement of 2port reciprocity.

- > If  $V_2' = 0 \neq V_2'' = 0 \Rightarrow Y_{21} = Y_{12}$
  - > Similarly,  $h_{21} = -h_{12}$
- } Special tests of reciprocity -

### Condition for reciprocity

$\sum_b (I_b' V_b'' - I_b'' V_b') = 0$ . This is true if each branch also satisfies this condition.  $\Rightarrow \sum_{b=1}^N (I_b' V_b'' - I_b'' V_b') = 0$ .

- > Any resistor, capacitor, inductor or transformer satisfies  $I_b' V_b'' - I_b'' V_b'$ .  $\Rightarrow$  they are reciprocal.
- ~~⇒~~ Any 2 port with reciprocal elements is also reciprocal.
- An n-port is reciprocal if it satisfies the condition:  
 $\sum_p (I_p' V_p'' - I_p'' V_p') = 0$       → derived from the weak Tellegen's theorem.
- A reciprocal element has a symmetric admittance or impedance matrix.
- A reciprocal network also has a symmetric admittance or impedance matrix

Note: Negative resistances are also reciprocal.  
 $\Rightarrow$  Passivity  $\neq$  Reciprocity

### Weak Tellegen's theorem

$$\sum_b (I_b' V_b'' - I_b'' V_b') = \sum_p (I_p' V_p'' - I_p'' V_p').$$

Gyrator → (A passive nonreciprocal element)

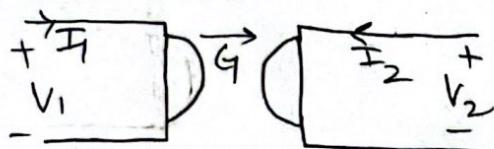
> Antireciprocal element.

$$Y = \begin{bmatrix} 0 & G \\ -G & 0 \end{bmatrix} \quad G \rightarrow \text{gyration conduction.}$$

> R, L, C, mutual inductors & gyrators can model any nonreciprocal network.

$$\begin{bmatrix} g_{11} & g_{12} \\ g_{21} & g_{22} \end{bmatrix} = \underbrace{\begin{bmatrix} g_{11} & \frac{1}{2}(g_{12}+g_{21}) \\ \frac{1}{2}(g_{12}+g_{21}) & g_{22} \end{bmatrix}}_{\text{reciprocal}} + \underbrace{\begin{bmatrix} 0 & \frac{1}{2}(g_{12}-g_{21}) \\ -\frac{1}{2}(g_{12}-g_{21}) & 0 \end{bmatrix}}_{\text{antireciprocal.}}$$

> Circuit symbol.



> Recall input admittance of a 2port is  $y_{IN} = y_{11} - \frac{y_{12}y_{21}}{y_{22}+y_L}$

if a gyrator is used as the 2port  $\Rightarrow$

$$y_{IN} = \frac{G^2}{Y_L}$$

> which is an impedance inverter.

⇒ Cap  $\leftrightarrow$  inductor for LCs.

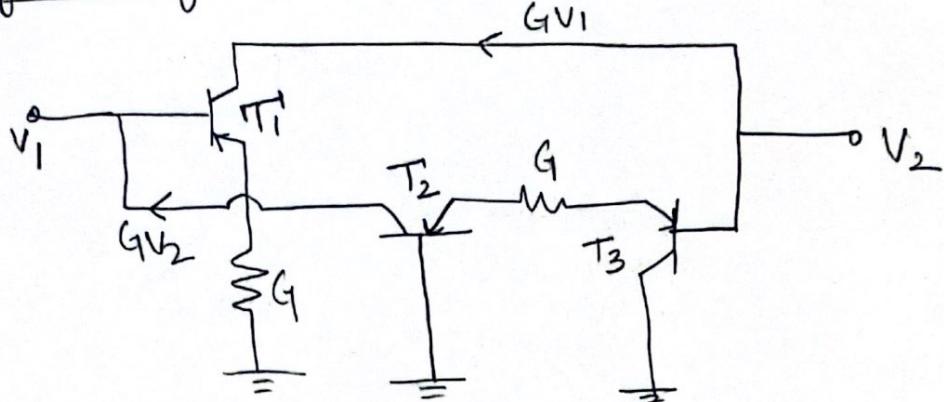
> H matrix of gyrator is  $\begin{bmatrix} 0 & Y_G \\ G & 0 \end{bmatrix}$ .

⇒ Cascaded gyrators can act as ideal transformers.

$$\begin{bmatrix} 0 & Y_{G_1} \\ G_1 & 0 \end{bmatrix} \cdot \begin{bmatrix} 0 & Y_{G_2} \\ G_2 & 0 \end{bmatrix} = \begin{bmatrix} \frac{G_2}{G_1} & 0 \\ 0 & \frac{G_2}{G_1} \end{bmatrix}$$

$$\rightarrow \text{Turns ratio } n = \frac{G_1}{G_2}$$

## Gyrorator from transistors



## Antireciprocality

> An N branch element is antireciprocational if :

$$\sum_{b=1}^N (I_b' V_b'' + I_b'' V_b') = 0 \quad \xrightarrow{\text{Same as reciprocal except for sign change.}}$$

> An N port network is antireciprocational if :

$$\sum_{p=1}^M (I_p' V_p'' + I_p'' V_p') = 0$$

> A network of antireciprocational elements is antireciprocational.

Complex gyrorator:  $Y = \begin{bmatrix} 0 & Y_0 \\ -Y_0 & 0 \end{bmatrix}$  where  $Y_0$  is complex.

- > Ideal transformer, short & open are both reciprocal & antireciprocational.
- > Skew symmetric admittance matrix  $\Rightarrow$  antireciprocational.
- > splitting indefinite admittance matrix into reciprocal and antireciprocational components makes oscillator model easier.
- > Splitting port admittance matrix makes amplifier model easier.

### 3 terminal symmetrical model

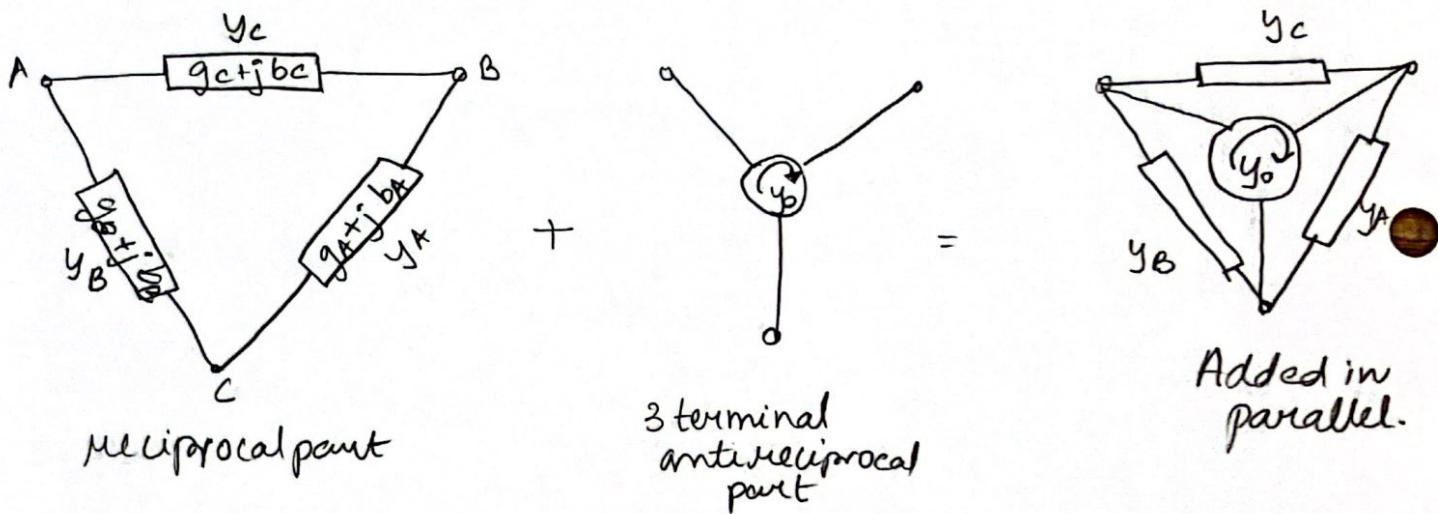
$$\begin{bmatrix} Y_{aa} & Y_{ab} & Y_{ac} \\ Y_{ba} & Y_{bb} & Y_{bc} \\ Y_{ca} & Y_{cb} & Y_{cc} \end{bmatrix} = \underbrace{\begin{bmatrix} Y_B + Y_C & -Y_C & -Y_B \\ -Y_C & Y_C + Y_A & -Y_A \\ -Y_B & -Y_A & Y_A + Y_B \end{bmatrix}}_{\text{reciprocal part}} + \underbrace{\begin{bmatrix} 0 & Y_0 & -Y_0 \\ -Y_0 & 0 & Y_0 \\ Y_0 & -Y_0 & 0 \end{bmatrix}}_{\text{3 terminal complex gyrator part.}}$$

where,  $Y_A = g_A + j b_A = -\frac{1}{2}(Y_{bc} + Y_{cb}) = \frac{1}{2}(Y_{bb} + Y_{cc} - Y_{aa})$

$$Y_B = g_B + j b_B = -\frac{1}{2}(Y_{ac} + Y_{ca}) = \frac{1}{2}(Y_{cc} + Y_{aa} - Y_{bb})$$

$$Y_C = g_C + j b_C = -\frac{1}{2}(Y_{ab} + Y_{ba}) = \frac{1}{2}(Y_{aa} + Y_{bb} - Y_{cc})$$

$$Y_0 = \frac{1}{2}(Y_{ab} - Y_{ba}) = \frac{1}{2}(Y_{bc} - Y_{cb}) = \frac{1}{2}(Y_{ca} - Y_{ac})$$



> Some can be done with  $\pi$  parameters.

### Two port model using antisymmetric transitions.

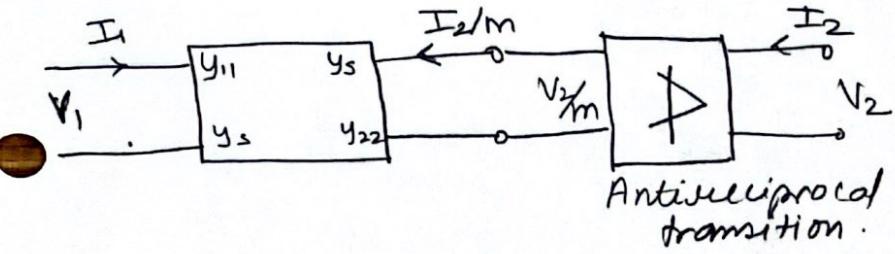
> Above we saw how 3 terminal elements can be modelled.

> Now let's look at 2 port. It uses a cascaded model.

$$[I] = [Y][V]$$

Let us define.  $Y_s \triangleq (Y_{12} Y_{21})^{\frac{1}{2}}$   
 $m \triangleq (Y_{21}/Y_{12})^{\frac{1}{2}}$

$$\Rightarrow \begin{bmatrix} I_1 \\ I_2/m \end{bmatrix} = \underbrace{\begin{bmatrix} Y_{11} & Y_s \\ Y_s & Y_{22} \end{bmatrix}}_{=} \begin{bmatrix} V_1 \\ V_2/m \end{bmatrix}$$



- > The antireciprocal transition introduces a voltage gain of ' $m$ ' and current gain of  $-m$ .  $\Rightarrow$  power gain of  $1/m^2$ .

## Nonreciprocity and Activity.

- > Disassemble the given matrix into
  - Symmetric  $\rightarrow$  Hermitian
  - Skewsymm.  $\rightarrow$  SkewHermitian.
  - Skewsymm.  $\rightarrow$  Hermitian
  - Skewsymm.  $\rightarrow$  SkewHermitian.

$$\begin{bmatrix} Y_{aa} & Y_{ab} & Y_{ac} \\ Y_{ba} & Y_{bb} & Y_{bc} \\ Y_{ca} & Y_{cb} & Y_{cc} \end{bmatrix} = \begin{bmatrix} g_B + g_C & -g_C & -g_B \\ -g_C & g_C + g_A & -g_A \\ -g_B & -g_A & g_A + g_C \end{bmatrix} + j \begin{bmatrix} b_B + b_C & -b_C & -b_B \\ -b_C & b_C + b_A & -b_A \\ -b_B & -b_A & b_A + b_B \end{bmatrix}$$

nonconservative reciprocal Resistor

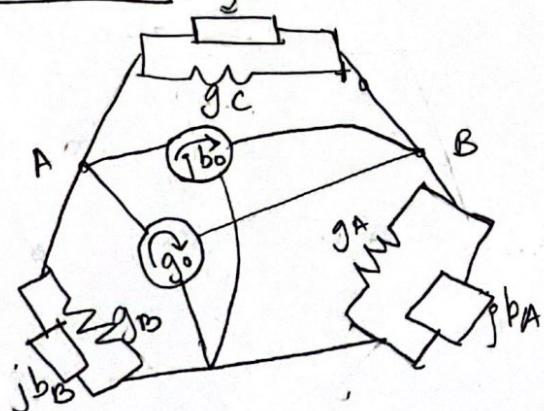
conservative L, C reciprocal transform

$$+ \begin{bmatrix} 0 & g_0 & -g_0 \\ -g_0 & 0 & g_0 \\ g_0 & -g_0 & 0 \end{bmatrix} + \begin{bmatrix} 0 & jb_0 & -jb_0 \\ -jb_0 & 0 & jb_0 \\ jb_0 & -jb_0 & 0 \end{bmatrix}$$

conservative nonreciprocal (real gyrator)

nonconservative nonreciprocal (imaginary gyrator).

## Circuit model. $jbo$



## Conditions for activity

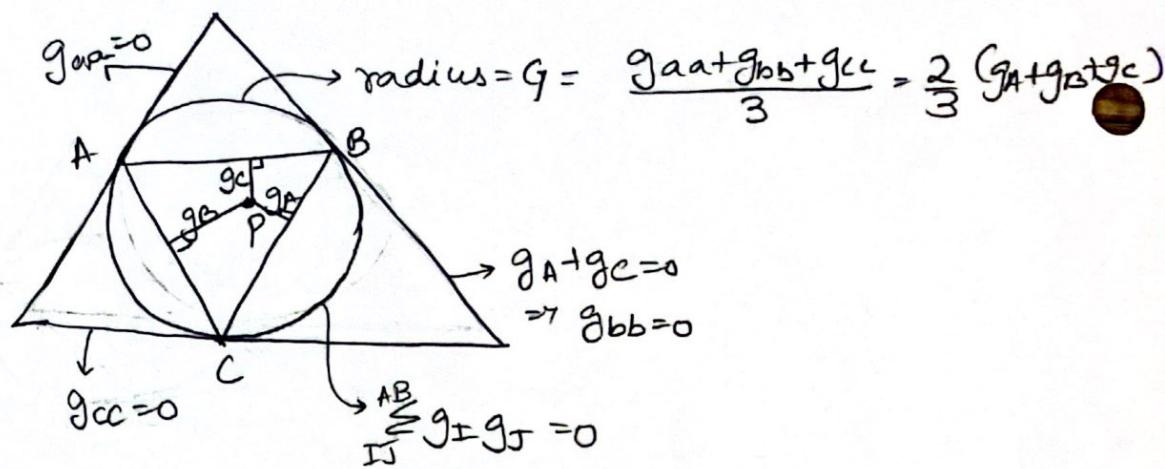
$$g_B + g_C < 0 \quad \textcircled{1}$$

$$g_C + g_A < 0 \quad \textcircled{2}$$

$$g_A + g_C < 0 \quad \textcircled{3}$$

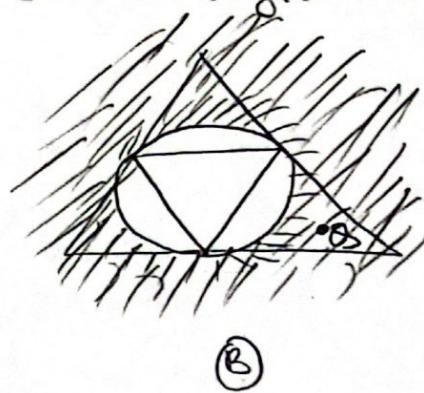
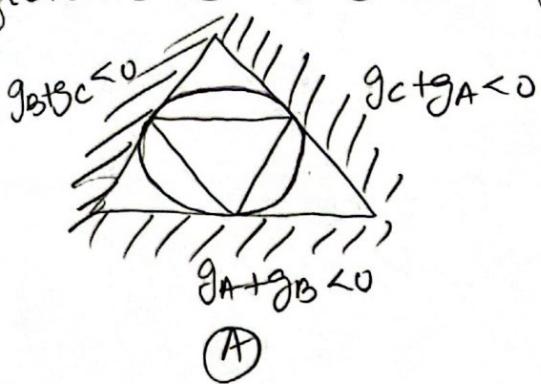
$$\sum_{I,J}^{A,B} g_I g_J - b_0^2 < 0 \quad \textcircled{4}$$

Can be represented by the Mason's Conductance diagram.



> 'P' represents the 3 terminal device.

- >  $g_A, g_B, g_C$  are arm conductances in the model. They could be negative.
- > If  $b_0 = 0$ ; activity conditions. ①-③ correspond to shaded region in ② & ④ corresponds to shaded region in ③.



> A device at pt. Q is reciprocal & has no negative self conductance but it is active due to negative arm conductance.

> If  $b_0 \neq 0$ , the circle of passivity has a radius =  $\sqrt{G^2 - \frac{4}{3}b_0^2}$ .

## Chapter 6

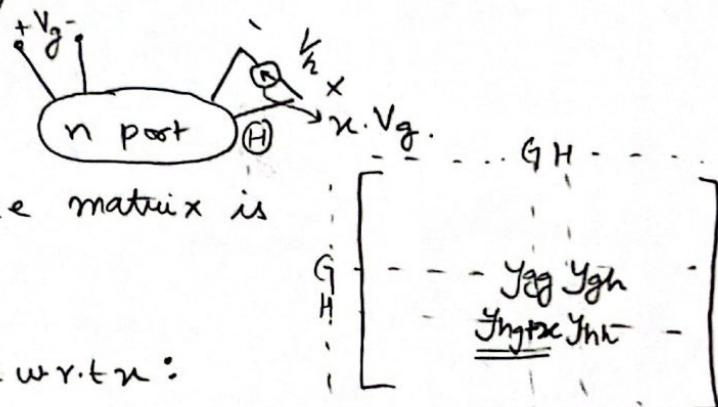
### Stability.

#### Return difference.

> R.D F(x) of a network w.r.t an element x is <sup>the</sup> ratio of the network determinant ( $\Delta$ ) and the det when  $x=0$ .  $\Delta^0$

$$\Rightarrow F(x) = \frac{\Delta}{\Delta^0}$$

> Given a network with 2 accessible ports G & H, where H is driven by a VCCS.



R.D of this network w.r.t n:

$$F(x) = 1 + x \frac{\Delta_{hg}}{\Delta^0} \quad \text{where } \Delta_{hg} \text{ is the cofactor of } (Y_{hg}+x) \text{ &} \\ \Delta^0 \text{ is the det of the matrix when } x=0$$

Here  $-x \frac{\Delta_{hg}}{\Delta^0}$  is a voltage ratio & called the "loop transmission"

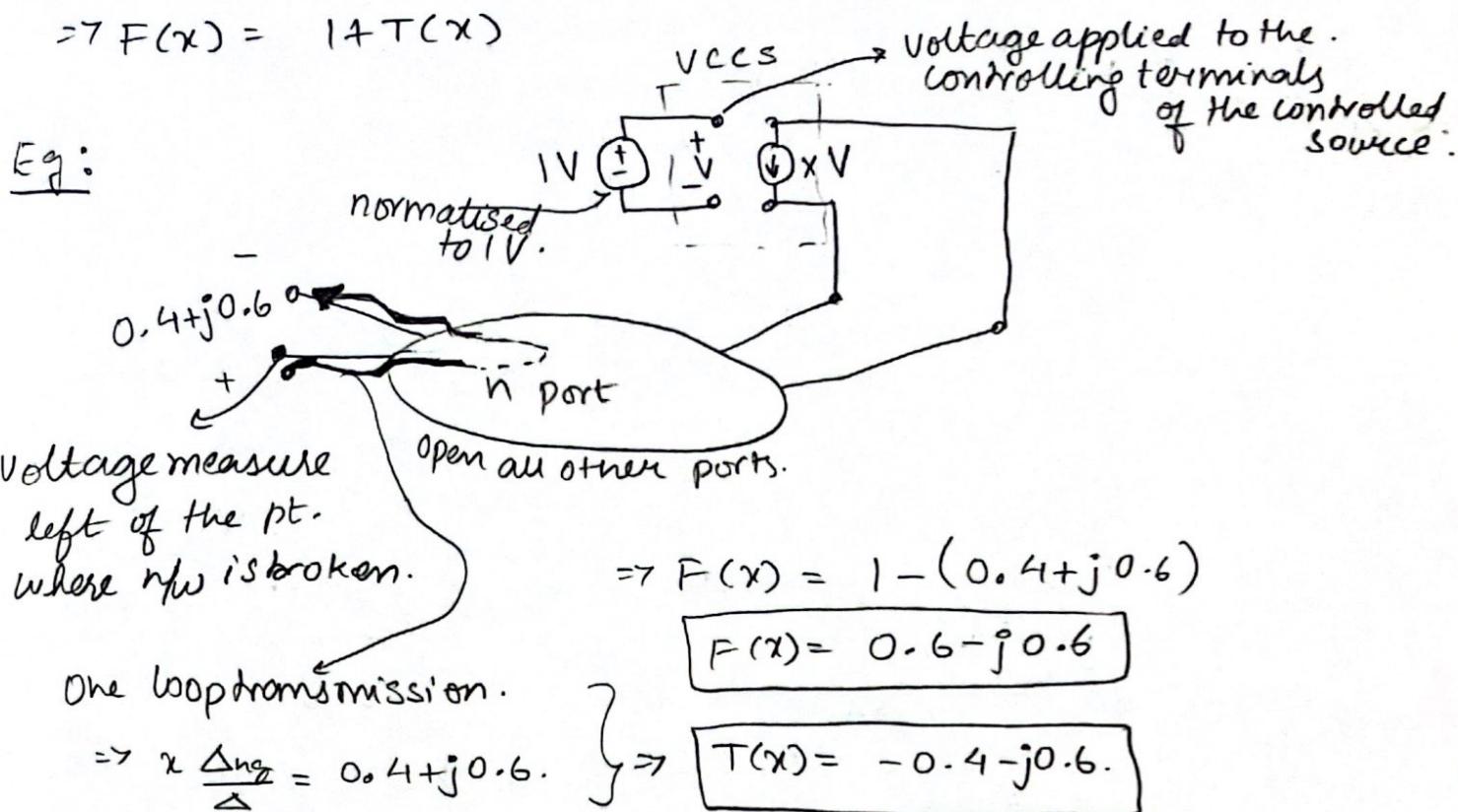
"Return difference is the difference, normalized to one volt, b/w the voltage applied to the controlling terminals of the controlled source and the voltage measured, left of the point at which the network is broken for the purpose of defining the measurement".

Return ratio  $\rightarrow$  "Fraction of the applied voltage returned to the controlling terminals".

$$T(x) = x \frac{\Delta_{\text{ng}}}{\Delta}$$

$$\Rightarrow F(x) = 1 + T(x)$$

Eg:



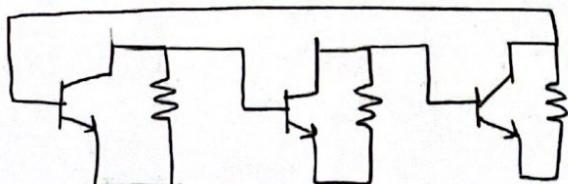
## Nyquist Criterion

Prerequisite: Single loop feedback  $\Rightarrow$  nulling any one active element must stabilize the network.

## Consequences of single loop amplifier feedback

①  $\Delta = abc + d \Rightarrow$  parameters of the circuit appear in simple form in the determinant.

Eg:



$$\Rightarrow \Delta = Y_1 Y_2 Y_3 - g_{m1} g_{m2} g_{m3}$$

② Return difference is same for each active element (Since  $\Delta^0$  is same).

- > Criterion: If the locus of the return ratio encloses  $(-1, 0)$  on the  $\text{Re}(\tau), \text{Im}(\tau)$  plane, the network is unstable.

## 2 Port Stability - Lewellyn's Conditions.



- > An active two port terminated in passive one ports is considered absolutely (or inherently) stable if it satisfies :

$$\begin{aligned} g_{11} &\geq 0 \\ g_{22} &\geq 0 \\ 2g_{11}g_{22} - M &\geq L \end{aligned}$$

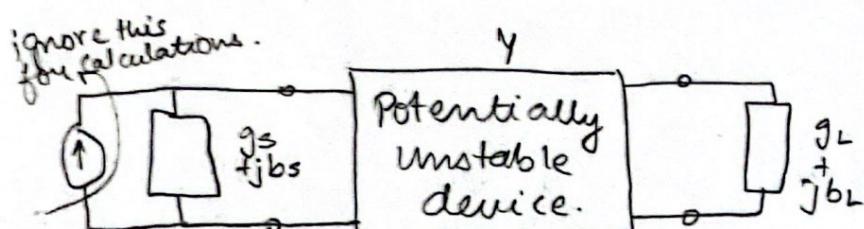
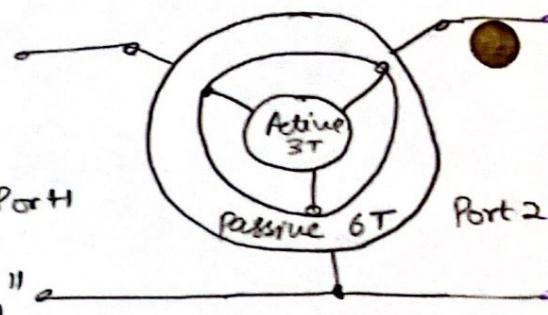
where,  $M + jN = Y_{12} Y_{21}$  &  
 $L^2 = M^2 + N^2$

- > Absolutely stable at  $f \Rightarrow$  the network is stable for all possible  $Y_s$  &  $Y_L$ . Which should be clear since the conditions only depend on the 2 port parameters.
- > Potentially unstable  $\Rightarrow$  conditions are not satisfied & some values of  $Y_s$  &  $Y_L$  could lead to instability.
- > A network can be absolutely stable at one frequency and potentially unstable elsewhere.
- > Switching the terminals (of a transistor) of the 2 port may cause instability  $\Rightarrow$  conditions must be rechecked.

- > In general  $g_{11} > 0$  &  $g_{22} > 0$  so we define  $\eta = \frac{2g_{11}g_{22} - M}{L}$
- > If  $\eta \geq 1 \Rightarrow$  inherently stable.  
 $\eta < 1 \Rightarrow$  potentially unstable.
- >  $\eta$  is a real number.
- >  $\eta$  is invariant to impedance transformation. [W.r.t  $Z, h \& K$ ]
- >  $\Rightarrow \eta = \frac{2g_{11}g_{22} - M_Z}{L_Z}$  where  $M_Z = \text{Re}(Z_1Z_2)$   
 $L_Z = |Z_1Z_2|$
- >  $\eta$  is not invariant with terminal transformation.

### Stabilization.

- > We care about 3rd condition  $2g_{11}g_{22} - M \geq L$ .
- > Add a 6-terminal passive reciprocal embedding to make it stable.
- > Special case is "unilateralization".
- > Make  $y_{12} = 0 \Rightarrow M = L = 0 \Rightarrow$  Stable!
- However more widely used is a technique called "mismatch stabilization".
- Mismatch Stabilization.



- > The values of  $Y_s$  &  $Y_L$  are so chosen that the network is stable over a narrow band. The values of  $Y$  must remain relatively constant over this band!

> For stability,

$$\frac{2(g_{11} + g_s)(g_{22} + g_L)}{M+L} > 1$$

$$L = \text{Re}(Y_{12}Y_{21})$$

$$M = |Y_{12}Y_{21}|$$

> We define  $K \triangleq \frac{2(g_{11} + g_s)(g_{22} + g_L)}{M+L}$  as the Stern's stability factor.

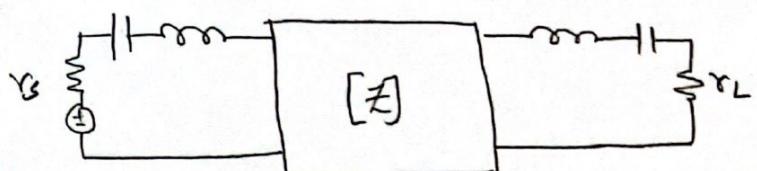
> Recall that  $\frac{2g_{11}g_{22} - M}{L} < 1 \Rightarrow$  potential instability. This can be rewritten as  $\frac{2g_{11}g_{22}}{M+L} < 1$  & we define  $k_i$  as the inherent stability factor.  $k_i = \frac{2g_{11}g_{22}}{M+L}$

- > Both  $k_i < 1$  &  $\eta < 1$  are equivalent expressions of inherent instability but  $k_i \neq \eta$  and is not immittance invariant.
- > Note that potential instability is independent of  $Y_s$  &  $Y_L$  but here we are dealing with instability in general.
- > A potentially unstable 2 port device is made stable over a small bandwidth using  $Y_s$  &  $Y_L$  to satisfy  $K > 1$ .

In case of series tuning:

$$\frac{2(\gamma_{11} + \gamma_s)(\gamma_{22} + \gamma_L)}{L_z + M_z} > 1$$

$K_Z$

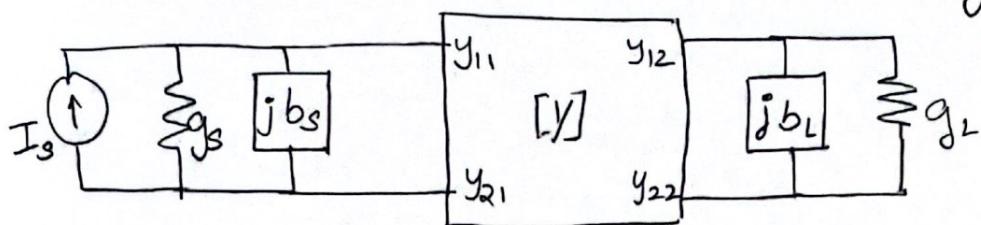


> Activity is a necessary condition for instability but not a sufficient condition.

## W. 1 Power gain I

Conjugate matching:

Conjugate matching is only possible at the frequencies where the 2 port is "inherently" stable. This can be changed if a suitable embedding is used. In which case it can always be matched.



Conjugate match in terms of Y

$$g_s = R g_{11}, \quad b_s = -b_{11} + \frac{N}{2g_{22}}$$

$$g_L = R g_{22}, \quad b_L = -b_{22} + \frac{N}{2g_{11}}$$

where  $M+jN = y_{12}y_{21}$ ;  $R = \sqrt{1 - \frac{M}{g_{11}g_{22}} - \frac{N^2}{4g_{11}^2g_{22}^2}}$

Unilateral  $\Rightarrow y_{12}=0 \Rightarrow M=N=0; R=1 \Rightarrow g_s=g_{11}; b_s=-b_{11}$  } obvious.  
 $g_L=g_{22}; b_L=-b_{22}$

> It only makes sense when R is real since  $g_s, g_L$  can only be real. Therefore a conjugate match is not always possible

### Power Gain

> Recall,  $G = \frac{4g_s g_L |y_{21}|^2}{|(y_{11}+y_s)(y_{22}+y_L)-y_{12}y_{21}|^2}$

When conjugate matched,

$$\text{max-available power gain. } G_c = \frac{|y_{21}|^2}{2g_{11}g_{22}(1+R)-M}.$$

also the transducer power gain.

What happens if R is not real?

$R = \frac{L}{2g_{11}g_{22}} \sqrt{\eta^2 - 1}$  where,  $\eta = \frac{2g_{11}g_{22} - M}{L}$ .

- $\eta$  must be  $> 1$  for stability  $\Rightarrow$  conjugate matching is only possible for a stable 2-port (inherently stable).

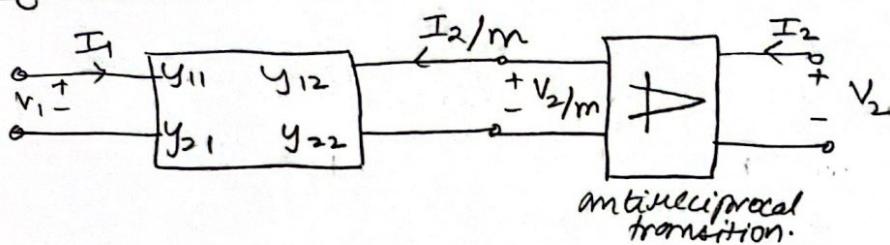
### Upper bound on $G_c$

Rewriting,  $G_c = \left| \frac{Y_{21}}{Y_{12}} \right| \frac{1}{\eta + \sqrt{\eta^2 - 1}}$ , let  $A = \frac{Y_{21}}{Y_{12}}$

$$\Rightarrow G_c = \frac{|A|}{\eta + \sqrt{\eta^2 - 1}} \quad (7.14)$$

- $G_c$  is not invariant to terminals unlike  $U$ . However,  $G_c$  of all configurations is  $= 1$  at  $f_{max}$  (like  $U$ ).
- $G_c < 1$  at  $f > f_{max}$ .

### Using the cascade model



We know  $m^2 = \frac{y_{21}}{y_{12}} = A$ , where  $|m^2|$  is the power gain of  $\triangleright$ .

Power gain of  $[Y]$  is  $\frac{1}{\eta + \sqrt{\eta^2 - 1}}$

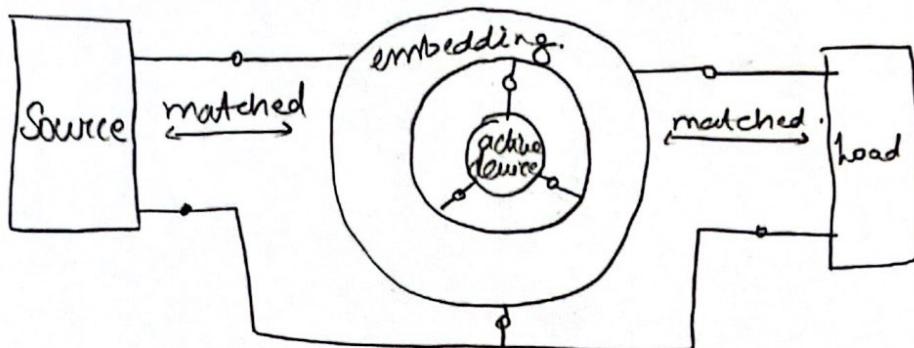
The  $\triangleright$  is impedance transparent  $\Rightarrow$  can be ignored during stability analysis.

In equation 7.14  $\eta$  &  $G_c$  are invariant to admittance & hence  $|A|$  must be so too.

$$A = \frac{y_{21}}{y_{12}} = -\frac{h_{21}}{h_{12}} = -\frac{k_{21}}{k_{12}} = \frac{z_{21}}{z_{12}}$$

> Similarly  $G = \frac{4|y_{21}|^2 g_s g_L}{|(y_{11}+y_s)(y_{22}+y_L) - y_{12}y_{21}|^2}$  can also be expressed in terms of  $\pi$ ,  $h$  &  $K$  parameters with care.

### Power gain of Embedded device.



- > Assume source and load are passive.
- > The embedding ensures conjugate matched  $\Rightarrow$  inherently stable.

$$U = \frac{|y_{21} - y_{12}|^2}{4[g_{11}g_{22} - g_{12}g_{21}]}$$

Rewriting & substituting  $G_c = \frac{|A|}{n + \sqrt{n^2 - 1}}$  — (7.11)

We get,

$$\frac{U}{G_c} = \left| \frac{A - 1}{A - G_c} \right|^2$$

>  $U, G_c$  are real numbers.  
>  $A$  is complex.

$$\sqrt{\frac{G_c}{U}} = \left| \frac{A - G_c}{A - 1} \right| \quad (7.19)$$

Assuming  $|A| \gg 1$  which is usually the case.

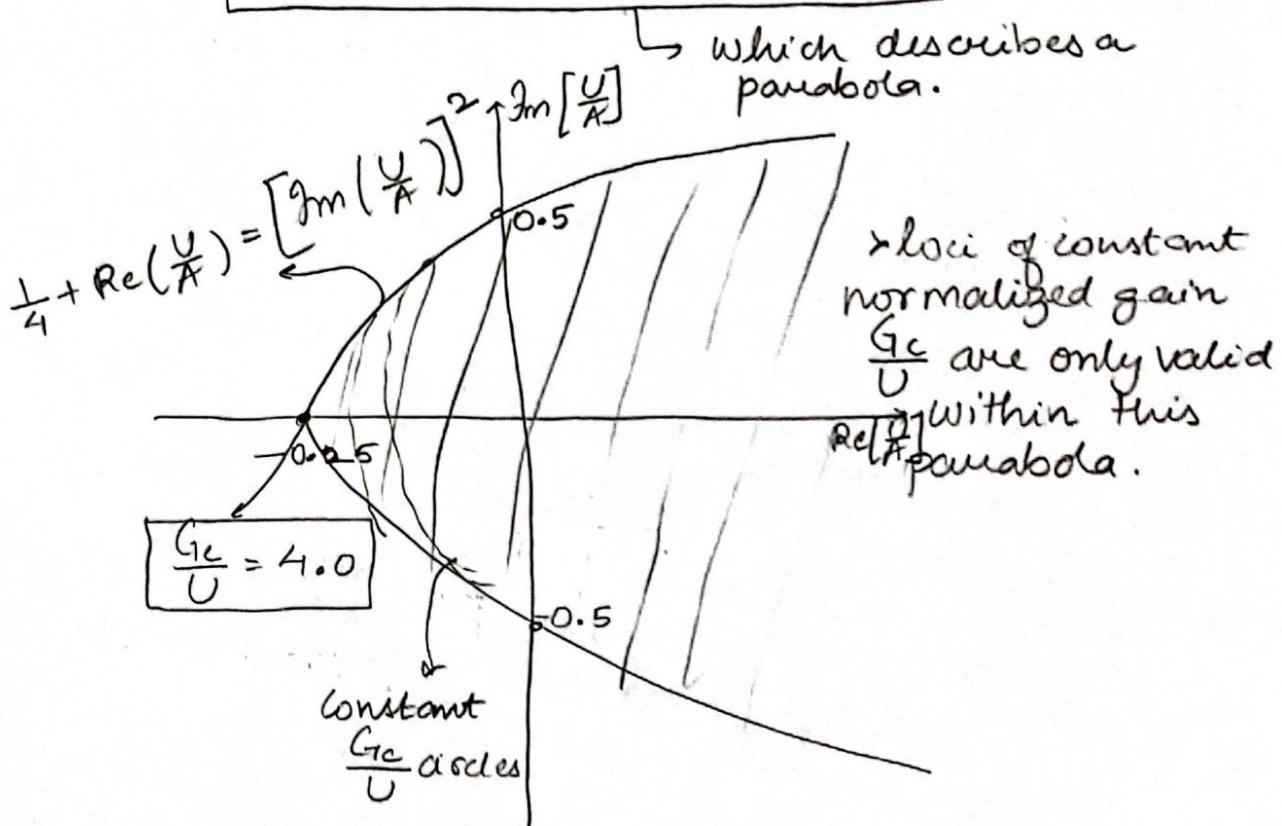
$$\sqrt{\frac{G_c}{U}} = \left| 1 - \frac{U}{A} \cdot \frac{G_c}{U} \right| \xrightarrow{\text{Complex value -}} \quad (7.22)$$

normalised  
gain.  
(real)

$\downarrow$  basis for gain plane.

- > We plot loci of constant  $\frac{G_c}{U}$  on the  $\frac{U}{A}$  plane. 49  
 To do that we must find the bounds of  $G_c \Rightarrow$  when conjugate matching is possible.  $\Rightarrow$  area where device is inherently stable.
- > Inherent stability  $\Rightarrow \eta \geq 1 \Rightarrow G_c \leq |A|$  {from (7.11)} & Substituting in (7.22) gives:

$$\left[ \operatorname{Im}\left(\frac{U}{A}\right) \right]^2 < \operatorname{Re}\left(\frac{U}{A}\right) + \frac{1}{4}$$



Rewriting eq.(7.22)

$$\frac{G_c}{U} = \left| 1 - \frac{G_c}{U} \operatorname{Re}\left(\frac{U}{A}\right) - j \frac{G_c}{U} \operatorname{Im}\left(\frac{U}{A}\right) \right|^2$$

$$\frac{U}{G_c} = \left[ \operatorname{Re}\left(\frac{U}{A}\right) - \frac{U}{G_c} \right]^2 + \left[ \operatorname{Im}\left(\frac{U}{A}\right) \right]^2$$

$\hookrightarrow$  circles of radius  $\sqrt{\frac{U}{G_c}}$  and centre at  $(\frac{U}{G_c}, 0)$

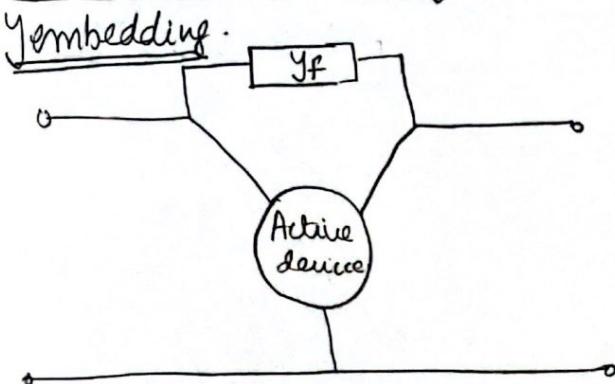
> Constant gain circles are incomplete since they don't exist outside

- > Embedding can change the position on the gain plane.
- >  $G_{C_{\max}} = (2U-1) + 2\sqrt{U(U-1)}$  ————— (7-27)

When  $U \gg 1 \Rightarrow G_{C_{\max}} = 4U$ .

At frequencies close to  $f_{\max}$   $U \approx 1$  & we use Eq 7-27

### Movement in gain plane

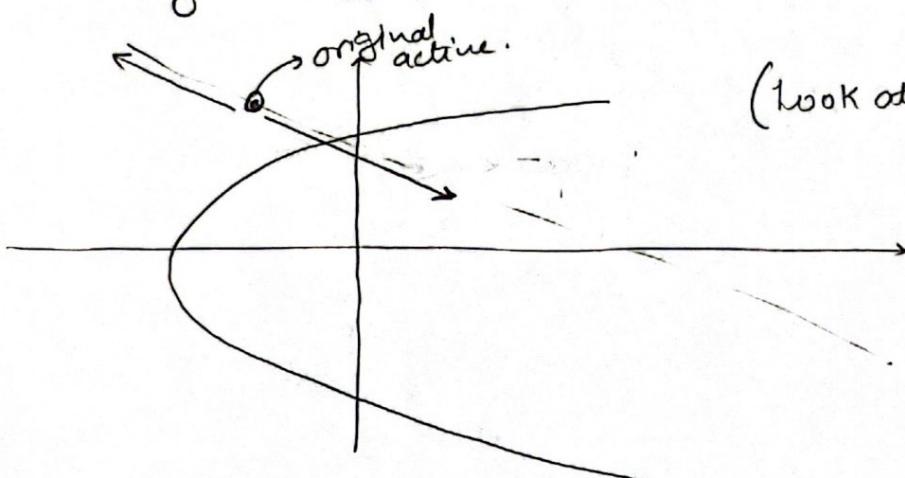


Assumption:  $|Y_{f1}| \ll |Y_{21}|$   
+  $Y_f$  is lossless.

$$\Rightarrow \frac{U'}{A'} = \frac{U}{A} - \frac{U|Y_{f1}|}{|Y_{21}|} \boxed{\pm \frac{\pi}{2} - \angle Y_{21}}$$

-ve  $\Rightarrow$  capacitor.  
+  $\Rightarrow$  inductor

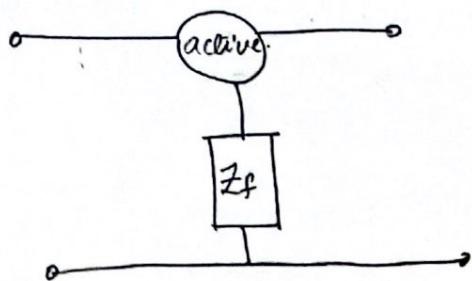
- > Draw a straight line through the point (original active 2port) at an angle  $\mp \frac{\pi}{2} - \angle Y_{21}$  w.r.t  $\text{Re}(U/A)$ . Then Yembedding moves along this line.



(look at attached example)

## Z embedding.

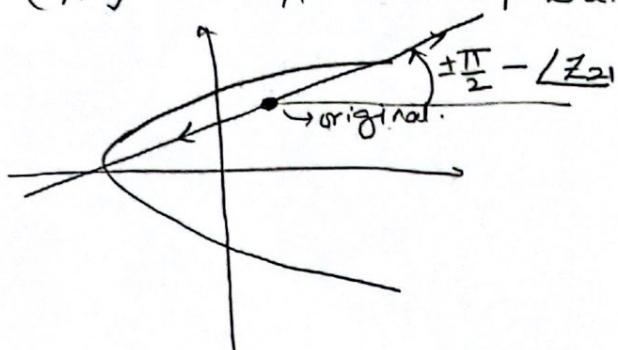
51



$$\Rightarrow \begin{bmatrix} Z_{11} + Z_s & Z_{12} + Z_f \\ Z_{21} + Z_f & Z_{22} + Z_s \end{bmatrix}$$

$$\left(\frac{U}{A}\right)' = \frac{U}{A} + U \left| \frac{Z_f}{Z_{21}} \right| \left( \pm \frac{\pi}{2} - \angle Z_{21} \right)$$

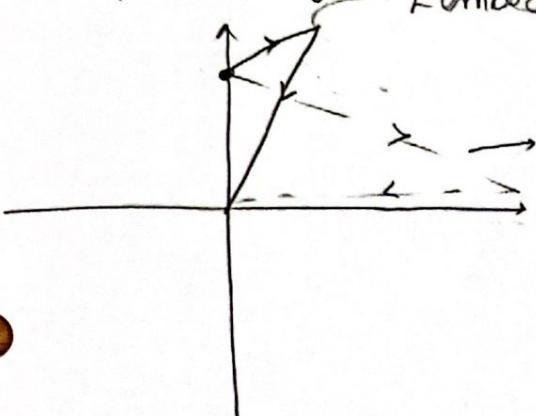
+  $\Rightarrow$  inductive.  
-  $\Rightarrow$  capacitive.



- > Note that unilateralization  $\Rightarrow Y_{12} = 0 \Rightarrow \frac{1}{A} = 0 \Rightarrow$  the origin! Therefore at the origin the normalized gain  $\frac{G_c}{U} = 1$  {since  $\sqrt{\frac{G_c}{U}} = \left| \frac{1 - \frac{G_c}{U} \cdot \frac{U}{A}}{1 - \frac{1}{U} \cdot \frac{U}{A}} \right| \right\}$ .

- > Since Y embedding changes  $Z_2$ , it also changes the slope of the Z embedding. Therefore the two are coupled & cannot be independently designed to reach the origin.

→ Z embedding followed by Y embedding.



→ Y embedding followed by Z embedding.

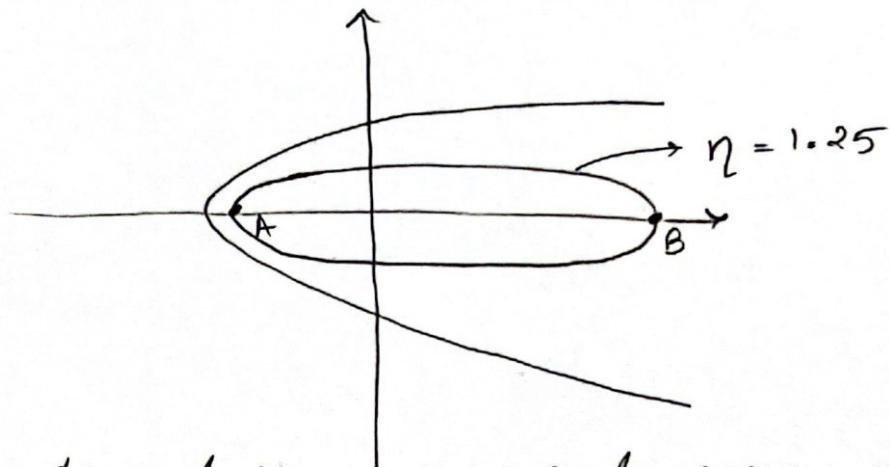
## Stability of conjugate matched amplifiers.

$\gamma = \frac{2g_{11}g_{22} - M}{L}$  is a measure of stability.

We know,  $G_c = \frac{1+A}{n + \sqrt{n^2 - 1}}$

$$\Rightarrow \frac{G_c}{U} = \frac{1+A}{U} \cdot \frac{1}{n + \sqrt{n^2 - 1}}$$

$\Rightarrow$  Constant stability loci form ellipses in the gain plane.



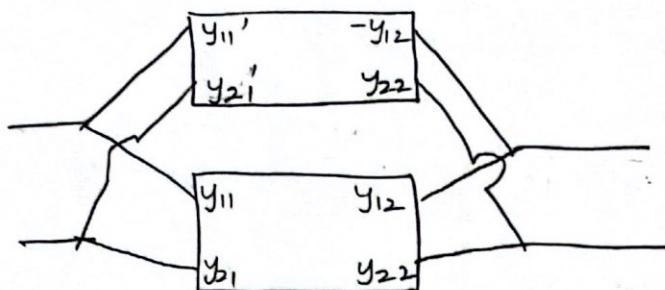
- > But  $\gamma$  does not serve as a good measure of stability. It is only an indicator of stability.
- > Also note that information is lost in the gain plane  $\Rightarrow$  location in gain plane is insufficient to derive  $\gamma$  params.
- $\Rightarrow$  Two identical points on the gain plane may respond differently to identical embedding since their  $U, Iy_2, I_2, I_{y_1}$  may differ.
- > Did not understand section 7.7.

## Lossy Embedding.

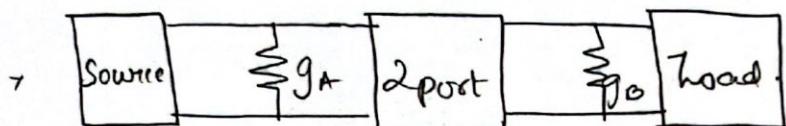
153

- ① > For unilateralization of a 2 port a lossy parallel connection is a trivial solution.

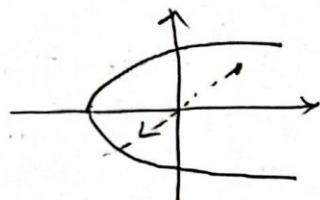
(See example)



- ② Resistive port padding.



- > Since  $g_A$  &  $g_B$  do not change  $A$  & only change  $U$ , we see that the locus is a straight line through the origin.

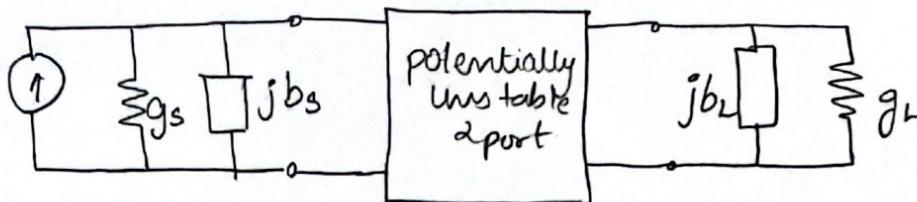


(See example).

- > Note that numerator of  $U$  is invariant to port padding.
- > This form of embedding can improve stability and also is used for cascaded stages.

## Ch: 8 : Power Gain II

Mismatch stabilization.



- > Choose  $y_s$  &  $y_L$  to satisfy  $k = \frac{2(g_{11}+g_s)(g_{22}+g_L)}{L+M} > 1$  to ensure stability. But it will not be conjugate matched.
- > mismatch  $\Rightarrow$  use transducer gain.

$$G = \frac{4|y_{21}|^2 (g_1 - g_{11})(g_2 - g_{22})}{(b_1 b_2 - g_1 g_2 + M)^2 + (b_1 g_2 + b_2 g_1 - N)^2}$$

where,  
 $M+jN = y_{12}y_{21}; g_1 = g_s + g_{11}; g_2 = g_L + g_{22}; b_1 = b_s + b_{11}; b_2 = b_L + b_{22}$

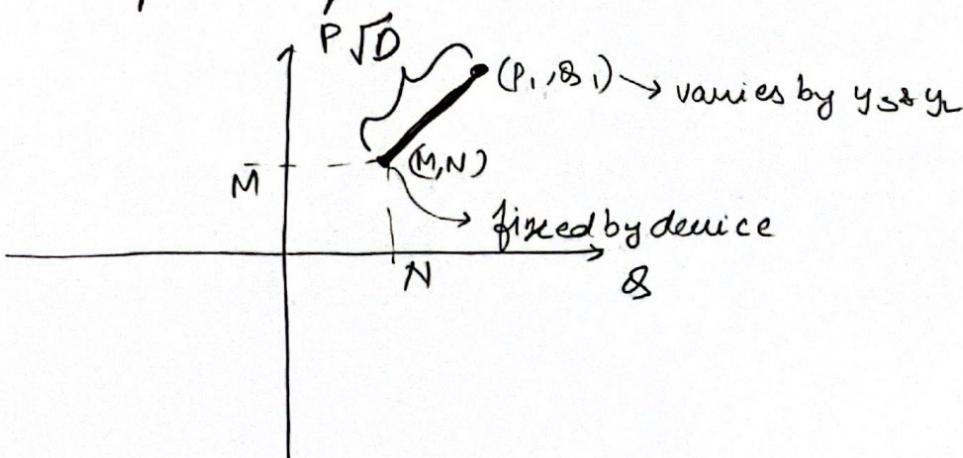
Let denominator of  $G = D = (P-M)^2 + (\Omega-N)^2$

where,

$$P = -b_1 b_2 + g_1 g_2 \quad \& \quad \Omega = b_1 g_2 + b_2 g_1$$

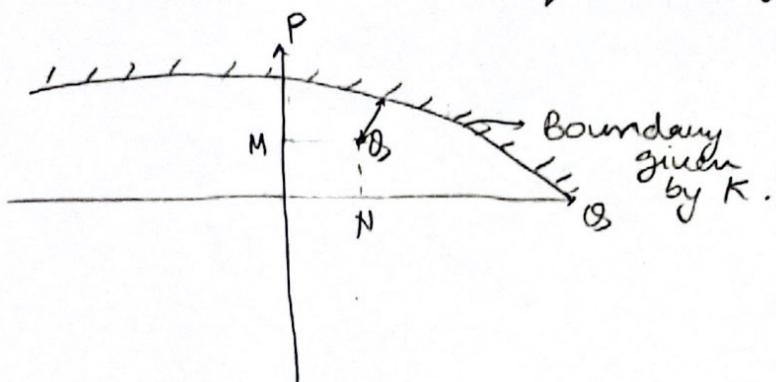
>  $M, N$  are device specific  $\Rightarrow$  constant.  $P, \Omega$  vary with source & load.

> Find optimum of  $g_1 g_2 b_1 b_2$  to minimize  $D$  & maximize  $G$ .



> Find  $P, \Omega$  to minimize  $D$ . However, the values of  $P, \Omega$  are constrained by  $\Omega^2 - 4g_1^2 g_2^2 + 4g_1 g_2 P \geq 0$  to keep it stable.

- > The parabola defining  $P_1, \theta_3$  to keep the amplifier stable, gives us an optimum of  $P_1, \theta_3$ , on it that lies closest to M,N.
- > Parabola position is defined solely by  $K \Rightarrow$  the device itself.



> An analytical solution is possible.

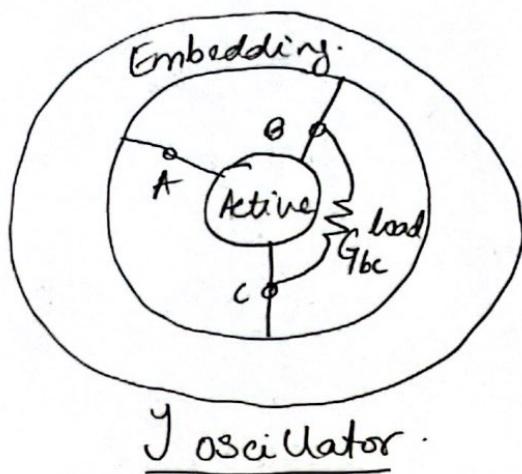
$$g_{Sopt} = g_{11} \left[ \sqrt{\frac{K}{K_i}} - 1 \right] \quad ; \quad g_{Lopt} = g_{22} \left[ \sqrt{\frac{K}{K_i}} - 1 \right]$$

where  $K_i = \frac{2g_{11}g_{22}}{L+M}$  & K is the desired stability factor.

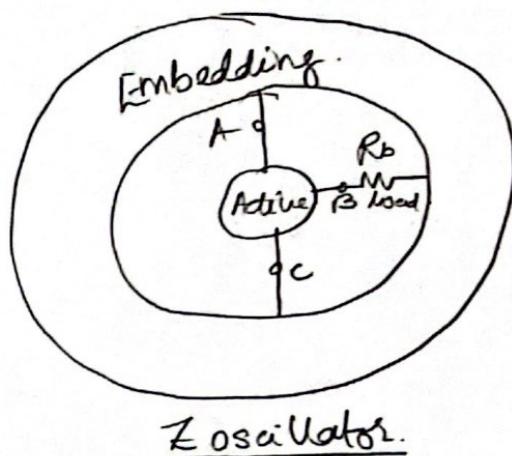
## Oscillators

Maximum loading: load resistance is increased until the oscillations die out. For practical cases a fixed amplitude is selected and the load conductance is increased until the desired amplitude is reached.

## Oscillator models



Y oscillator



Z oscillator

→ We use terminal description. Assuming  $g_A, g_B, g_C$  are  $> 0$ .

Oscillation condition:  $g_A g_B + g_B g_C + g_C g_A - b_0^2 < 0$ .

→  $G_{bc}$  is parallel to  $g_A$  (don't see why).  $\Rightarrow (g_A + G_{bc}) g_B + g_B g_C + g_C (g_A + G_{bc}) - b_0^2 = 0$

is the maximal loading condition.

→ let  $G_{bc} = \max(G_{bc})$

$$\Rightarrow G_{bc} g_{aa} = G_A g_{bb} = G_B g_{cc} = \boxed{G^2} \rightarrow \text{invariant to permutations!}$$

→  $G_{bc}, G_A, G_B$  are the maximum load conductances that can be tolerated.

# Network Sensitivity.

57

- > Let  $w(x)$  be a transfer function within the network w.r.t  $x$ .

## Sensitivity

$$S(x) \triangleq \left. \frac{\frac{\delta w}{w}}{\frac{\delta x}{x}} \right|_{\delta x \rightarrow 0} = \frac{x}{w} \cdot \frac{dw}{dx} = \frac{d(\ln(w))}{dx} \cdot x$$

$$S(x) = -x \cdot \frac{\Delta_{13} \Delta_{42}}{\Delta_{12} \cdot \Delta}$$

$$S(x) = \frac{1}{F(x)} \left[ 1 - \frac{w(0)}{w(x)} \right]$$

↑  
return diff.

