

Figure 11.6 The design of the simplest protocol with no flow or error control

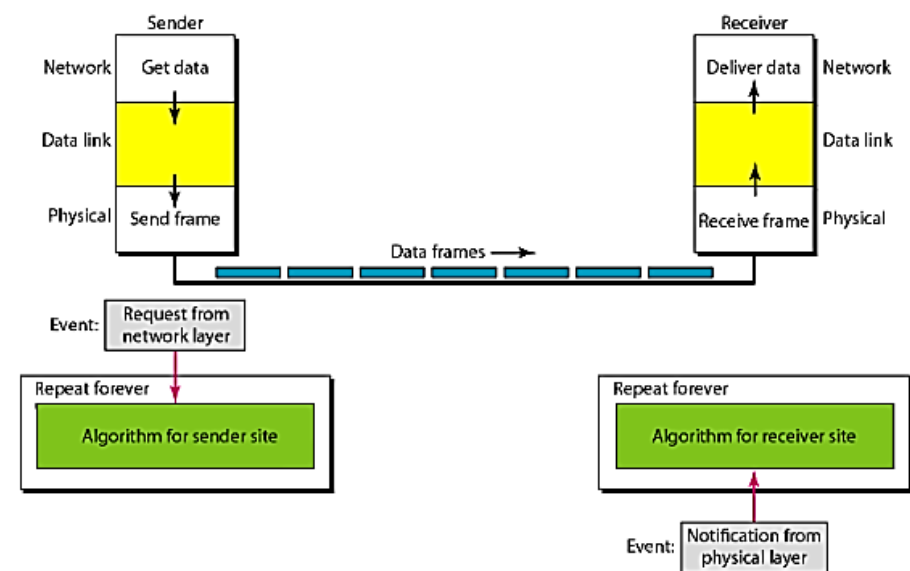


Figure 11.8 Design of Stop-and-Wait Protocol

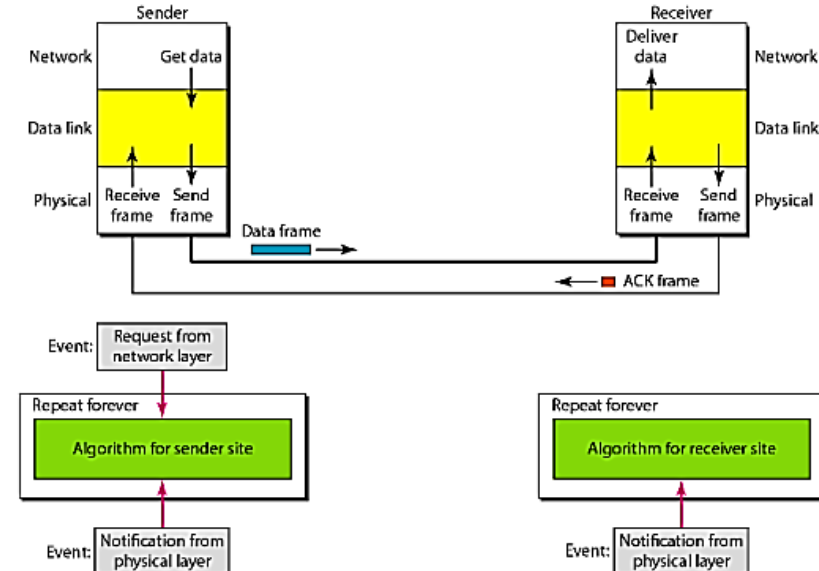


Figure 11.10 Design of the Stop-and-Wait ARQ Protocol

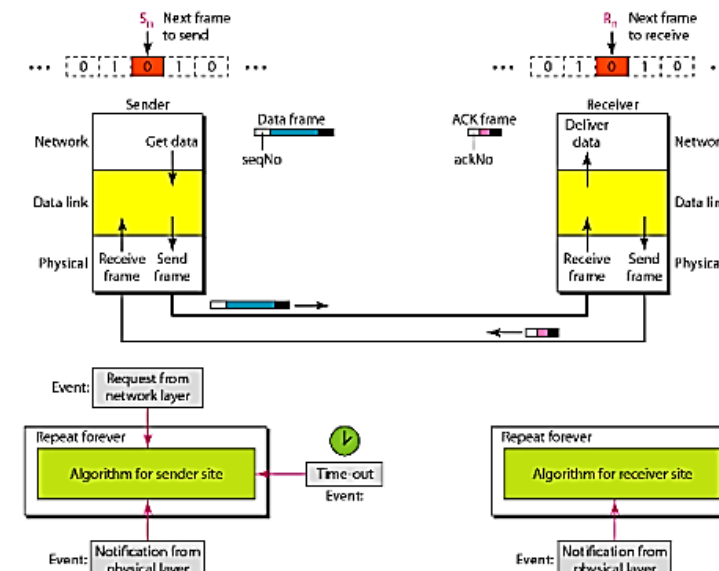


Figure 11.14 Design of Go-Back-N ARQ

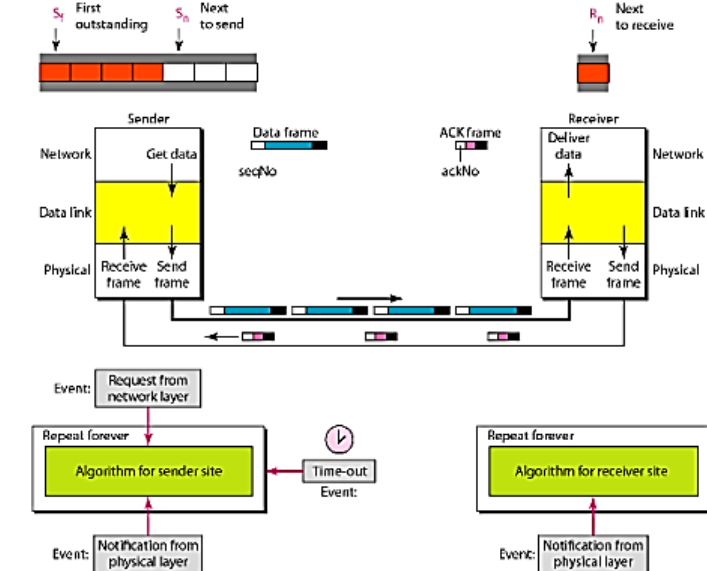


Figure 11.7 Flow diagram for Example 1

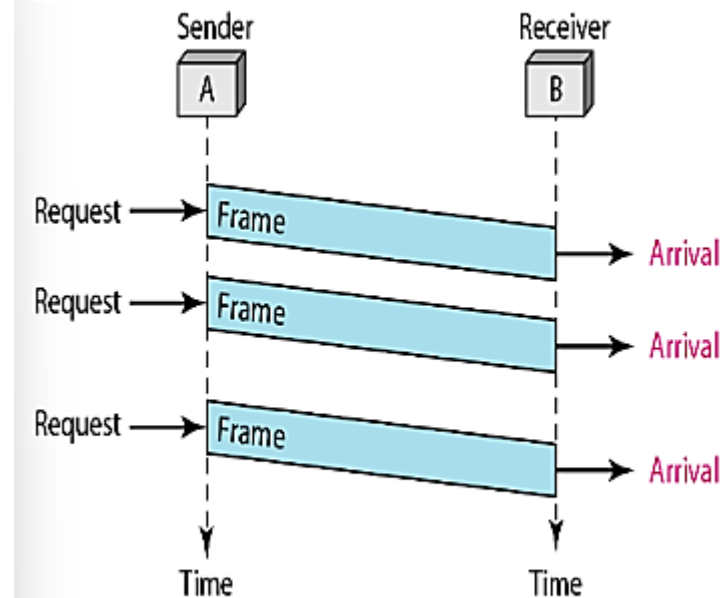


Figure 11.9 Flow diagram for Example 11.2

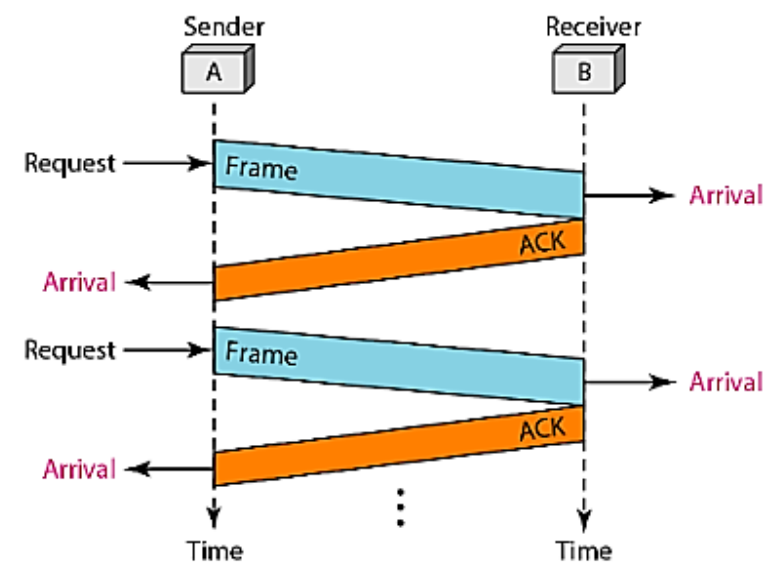


Figure 11.11 Flow diagram for Example 11.3

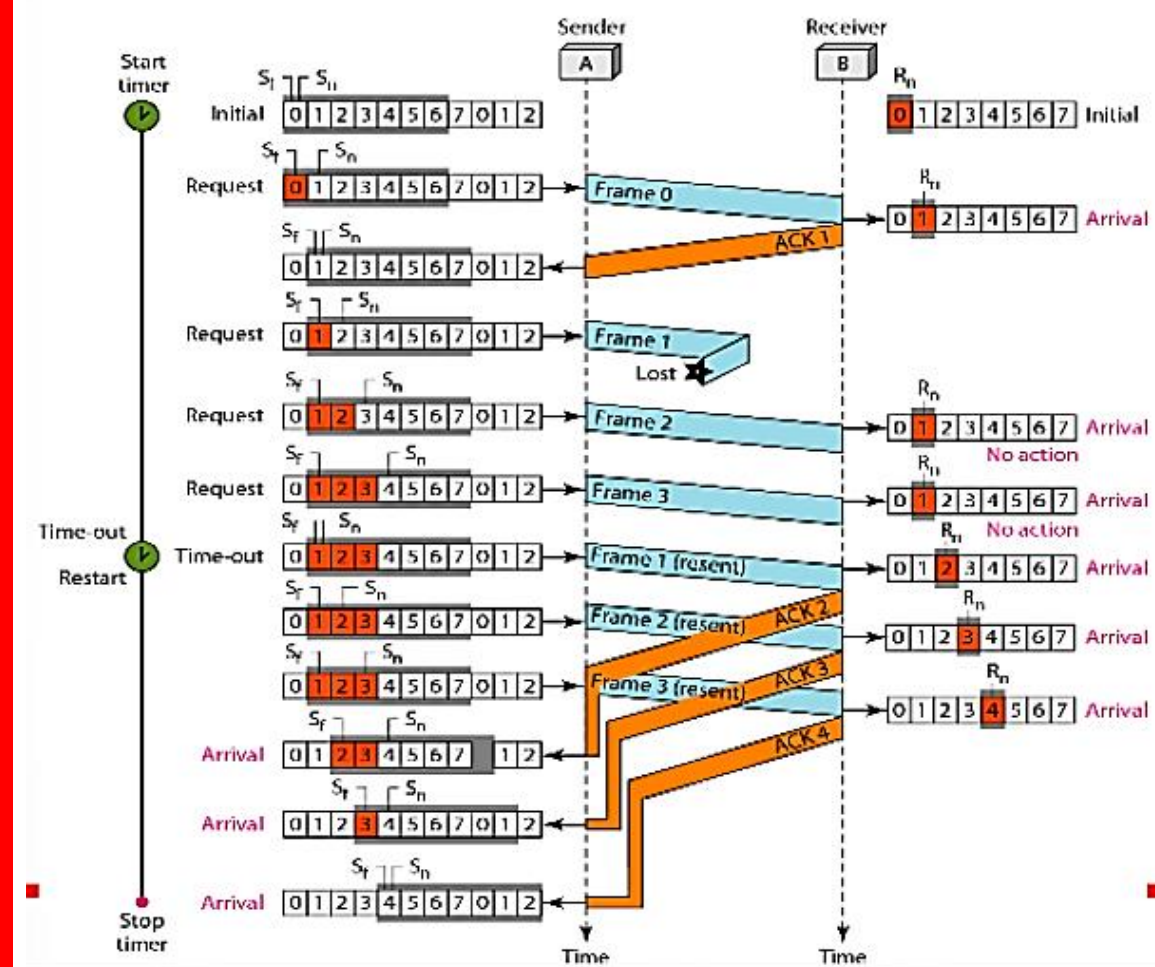
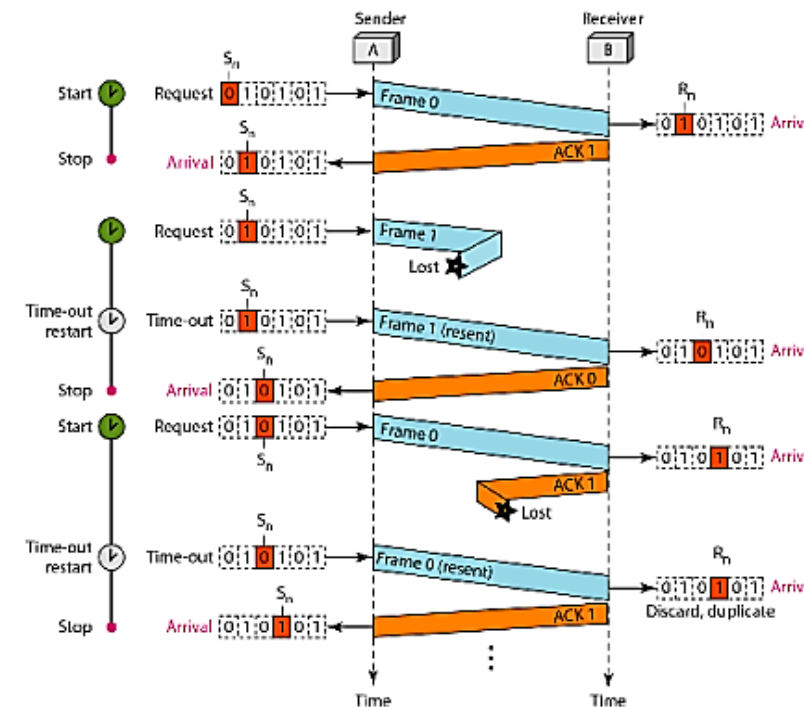


Figure 11.20 Design of Selective Repeat ARQ

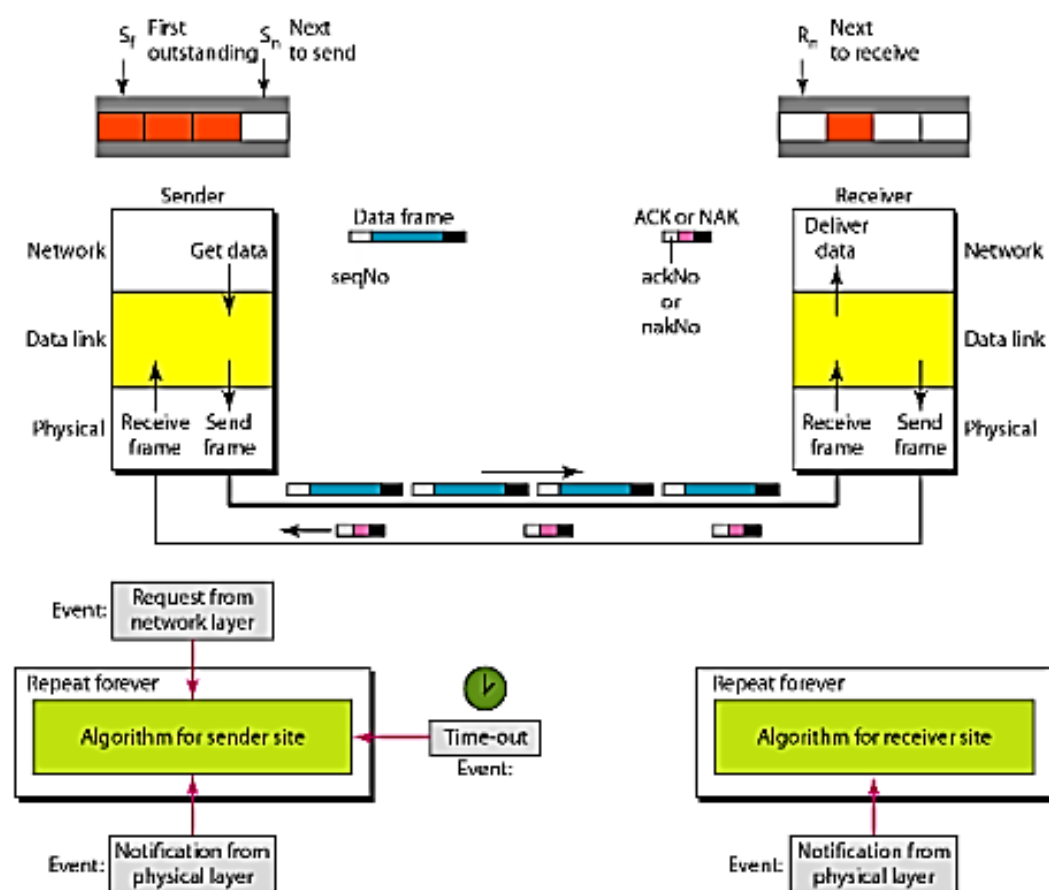
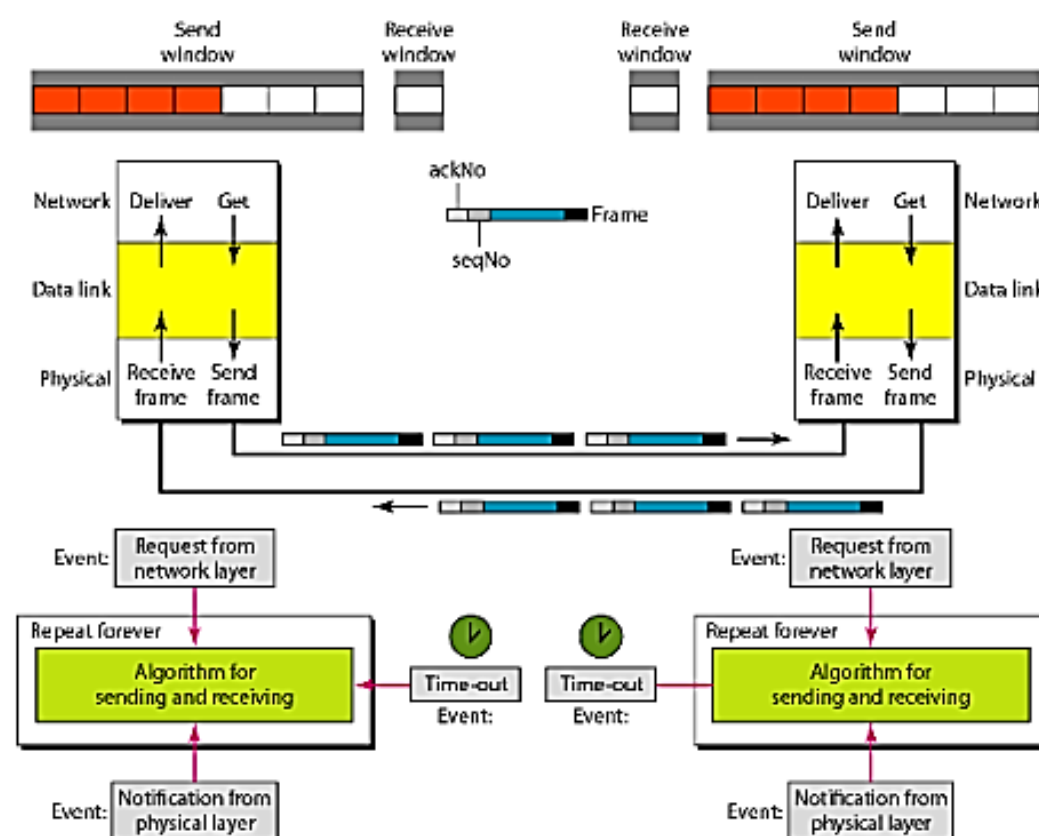
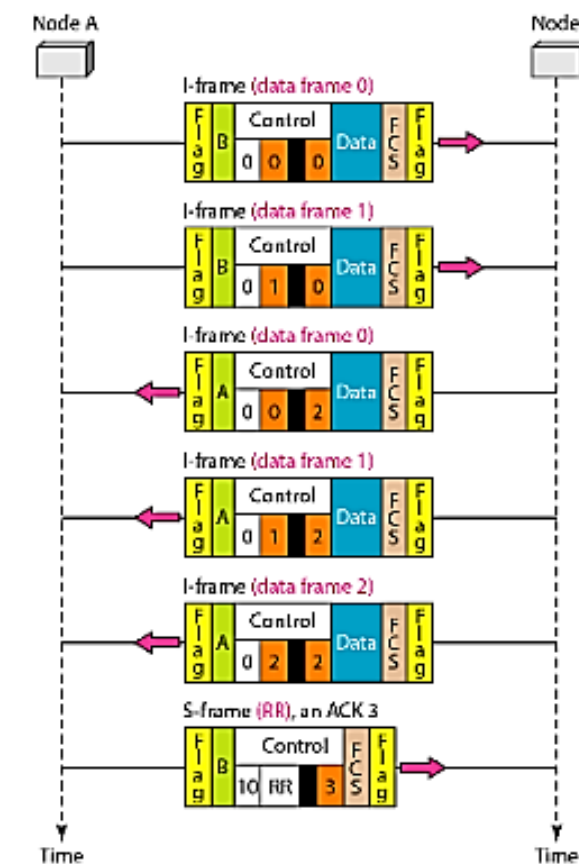


Figure 11.24 Design of piggybacking in Go-Back-N ARQ



Example of piggybacking without error



Example of piggybacking with error

