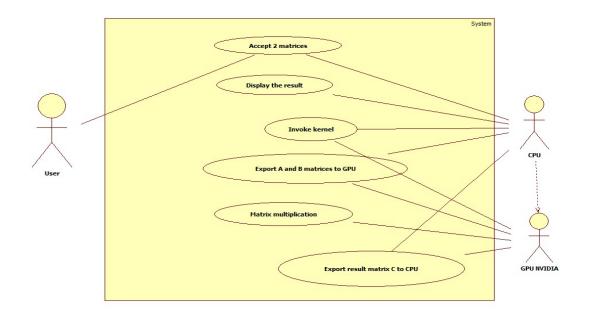
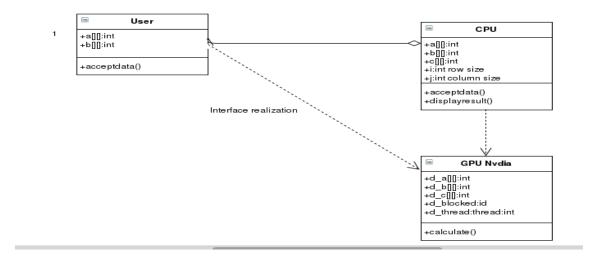
### UML OF nxn matrix in GPU

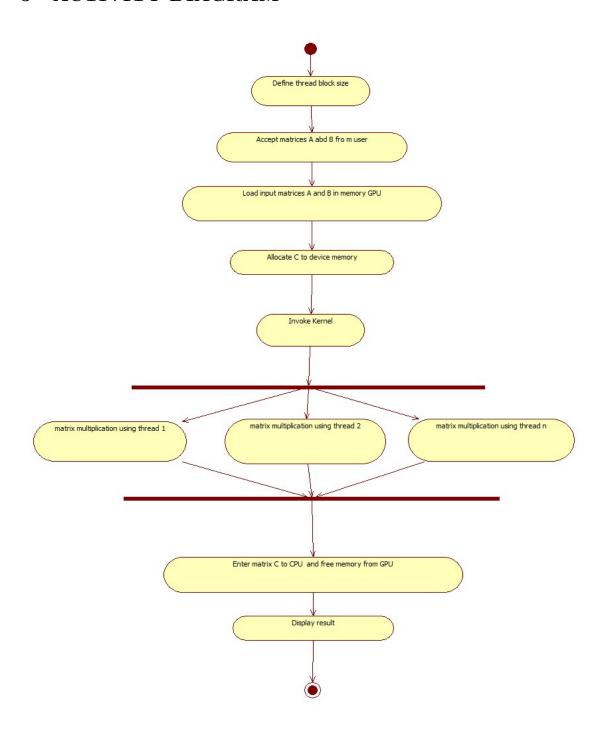
### 1 USECASE



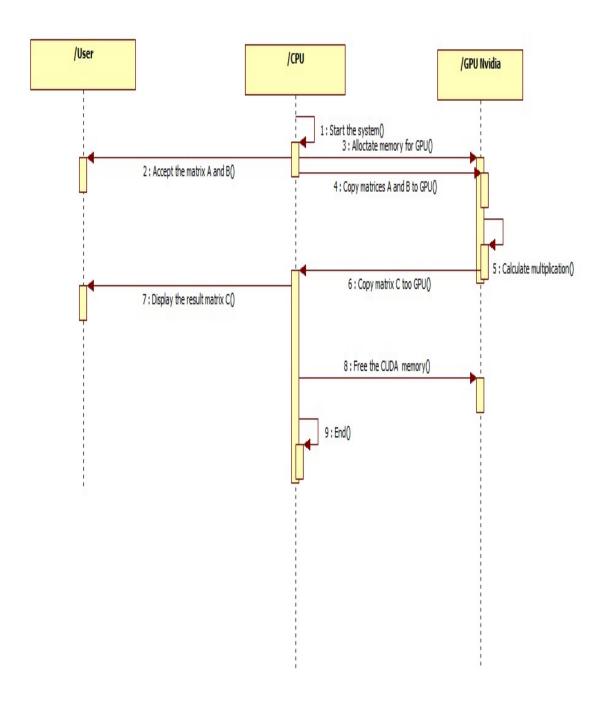
## 2 CLASS DIAGRAM



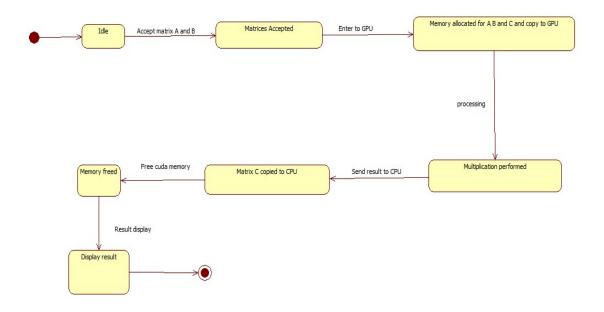
# 3 ACTIVITY DIAGRAM



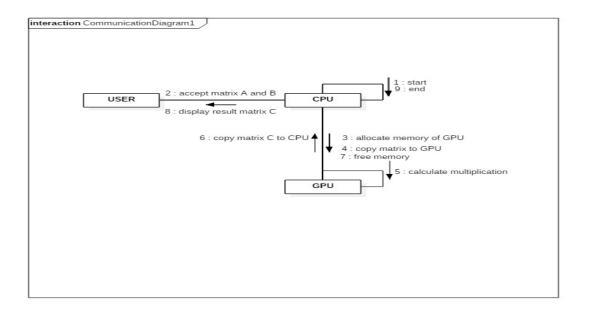
# 4 SEQUENCE DIAGRAM



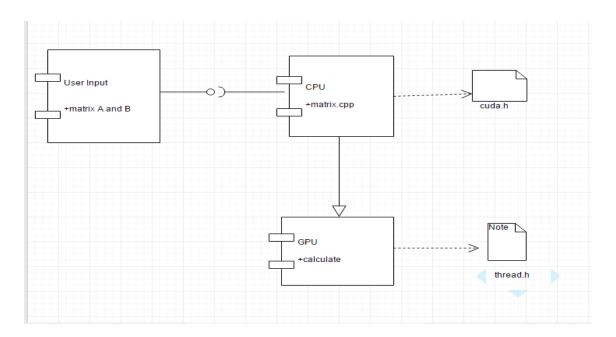
### 5 STATE CHART DIAGRAM



## 6 COMMUNICATION DIAGRAM



# 7 COMPONENT DIAGRAM



## 8 DEPLOYMENT DIAGRAM

