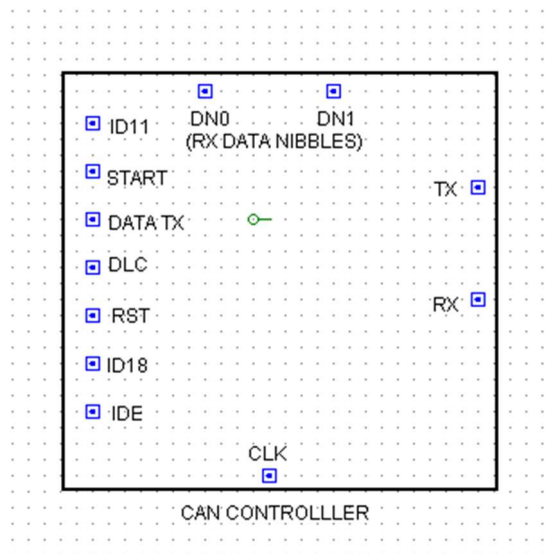


STEPS TO OPERATE CAN CONTROLLER

- In final.circ, there is a circuit “main” that contains the CAN controller



- ID11 is a 11-bit pin to which the 11 bit identifier has to be given
- ID18 is an 18 bit pin to which the remaining 18 bits of the identifier can be given in case of a 29-bit identifier of the CAN Extended Version
- DATA TX pin must be provided with 8 bit data. This must change every 8 clock cycles in case of multiple bytes
- DLC is a 4 bit pin to which length of required transmitted data is inputted in 4 bit form
- RST pin resets the controller if pulled high
- IDE pin sets the CAN mode to Standard if set to dominant (0) and Extended if set to recessive (1)
- DN0 and DN1 are the data nibbles of the received data (4 bit each)
- CLK is the pin to which clock signal must be provided for synchronization
- TX and RX have the transmitted output and received input respectively