RISCV-32I CPU

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# ARCHITECTURE OVERVIEW

# REGISTER FILE

The Register File, often referred to as the *RegFile* is a block a quick-access memory. It holds 32, 32-bit words, each of which support reading and writing.

# ARITHMETIC & LOGIC UNIT

The Arithmetic & Logic Unit (ALU) is the section of the CPU designed for mathematical and logical operations. This simple ALU can perform two math operations, addition and subtraction, as well as three logic operations, AND, OR, and XOR.