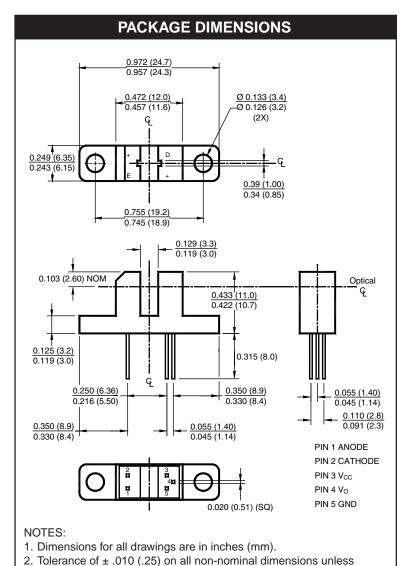
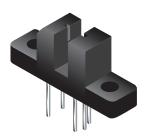


H21LTB H21LTI H21LOB H21LOI





Features

- · Low cost
- 0.035" apertures
- · Black plastic opaque housing
- Mounting tabs on housing
- Choice of inverter or buffer output functions
- Choice of open-collector or totem-pole output configuration
- TTL/CMOS compatible output functions

Description

otherwise specified.

The H21L series are slotted optical switches designed for multipurpose non contact sensing. They consist of a GaAs LED and a silicon OPTOLOGIC[®] sensor packaged in an injection molded housing and facing each other across a .124" (3.15 mm) gap. The output is either inverting or non-inverting, with a choice of totem-pole or open-collector configuration for TTL/CMOS compatibility



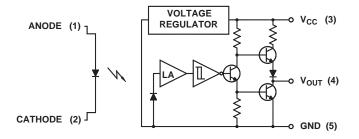
Part Number Definitions			
H21LTB	Totem-pole, buffer output		
H21LTI	Totem-pole, inverter output		
H21LOB	Open-collector, buffer output		
H21LOI	Open-collector, inverter output		

Input/Output Table						
Part Number	LED	Output				
H21LTB	On	High				
H21LTB	Off	Low				
H21LTI	On	Low				
H21LTI	Off	High				
H21LOB	On	High				
H21LOB	Off	Low				
H21LOI	On	Low				
H21LOI	Off	High				



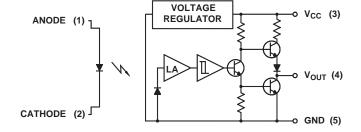
H21LTB H21LTI H21LOB H21LOI

Schematics



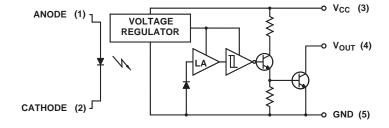
H21LTB

Totem-Pole Output Buffer



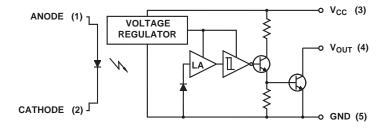
H21LTI

Totem-Pole Output inverter



H21LOB

Open-Collector Output Buffer



H21LOI

Open-Collector Output Inverter



Absolute Maximum Ratings (T _A = 25°C unless otherwise specified)							
Parameter	Symbol	Rating	Units				
Operating Temperature	T _{OPR}	-40 to +85	°C				
Storage Temperature	T _{STG}	-40 to +85	°C				
Soldering Temperature (Iron) ^(3,4,5,6)	T _{SOL-I}	240 for 5 sec	°C				
Soldering Temperature (Flow) ^(3,4,6)	T _{SOL-F}	260 for 10 sec	°C				
Input (Emitter)	1		•				
Continuous Forward Current	I _F	50	mA				
Reverse Voltage	V _R	6	V				
Power Dissipation ⁽¹⁾	P _D	100	mW				
Output (Sensor)			•				
Output Current	Io	50	mA				
Supply Voltage	V _{CC}	4.0 to 16	V				
Output Voltage	Vo	30	V				
Power Dissipation ⁽²⁾	P _D	150	mW				



H21LTB H21LTI H21LOB H21LOI

Electrical/Optical Characteristics (T _A =25°C)								
Parameter	Test Conditions	Symbol	Min.	Тур	Max.	Units		
Input (Emitter)								
Forward Voltage	I _F = 20 mA	V _F	_		1.5	V		
Reverse Leakage Current	V _R = 5 V	I _R	_		10	μΑ		
Output (Sensor)	·	•		•				
Supply Current	V _{CC} = 5 V	I _{CC}	_		5	mA		
Coupled				•		•		
Low Level Output Voltage H21LTB, H21LOB	$I_F = 0$ mA, $V_{CC} = 5$ V, $R_L = 100 \Omega$	V _{OL}	_		0.4	V		
Low Level Output Voltage H21LTI, H21LOI	I_F = 15 mA, V_{CC} = 5 V, R_L = 360 Ω	V _{OL}	_		0.4	٧		
High Level Output Voltage H21LTB	$I_F = 15 \text{ mA}, V_{CC} = 5 \text{ V}, I_{OH} = -800 \mu\text{A}$	V _{OH}	2.4		_	V		
High Level Output Voltage H21LTI	$I_F = 0$ mA, $V_{CC} = 5$ V, $I_{OH} = -800 \mu A$	V _{OH}	2.4		_	V		
High Level Output Current H21LOB	$I_F = 0 \text{ mA}, V_{CC} = 5 \text{ V}, I_{OH} = -800 \mu\text{A}$	I _{OH}			100	μA		
High Level Output Current H21LOI	$I_F = 0 \text{ mA}, V_{CC} = 5 \text{ V}, V_{OH} = 30 \text{ V}$	I _{OH}	_		100	μA		
Turn on Threshold Current	$V_{CC} = 5 \text{ V}, R_L = 360 \Omega$	l _F (+)	_		15	mA		
Turn off Threshold Current	$V_{CC} = 5 \text{ V}, R_L = 360 \Omega$	I _F (-)	0.50		-	mA		
Hysteresis Ratio		I _F (+) / I _F (-)		1.2				
Propagation Delay	$V_{CC} = 5 \text{ V}, R_L = 360 \Omega \text{ (See Fig, 9)}$	t _{PLH} , t _{PHL}		5		μs		
Output Rise and Fall Time	$V_{CC} = 5 \text{ V}, R_L = 360 \Omega \text{ (See Fig, 9)}$	t _r , t _f		70		ns		

Notes: (Applies to Max Ratings and Characteristics Tables.)

- 1. Derate power dissipation linearly 1.67 mW/°C above 25°C.
- 2. Derate power dissipation linearly 2.50 mW/°C above 25°C.
- 3. RMA flux is recommended.
- 4. Methanol or isopropyl alcohols are recommended as cleaning agents.
- 5. Soldering iron 1/16" (1.6mm) from housing.
- $\ensuremath{\text{6.}}$ As long as leads are not under any stress or spring tension.



Figure 1. Output Voltage vs. Input Current (Inverters)

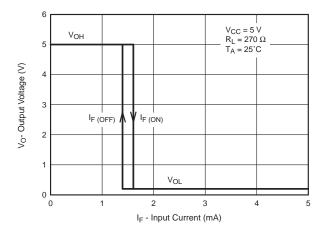


Figure 2. Output Voltage vs. Input Current (Buffers)

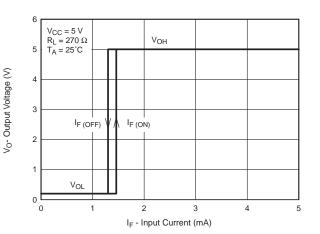


Figure 3. Normalized Threshold Current vs. Shield Distance

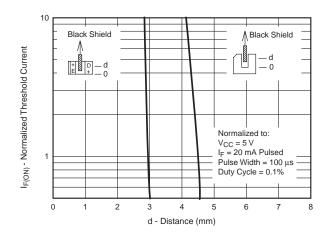


Figure 4. Normalized Threshold Current vs. Supply Voltage

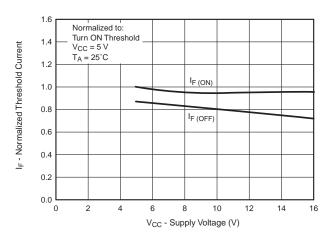




Figure 5. Normalized Threshold Current vs. Ambient Temperature

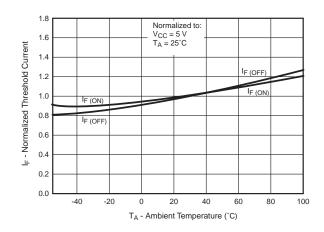


Figure 6. Forward Current vs. Forward Voltage

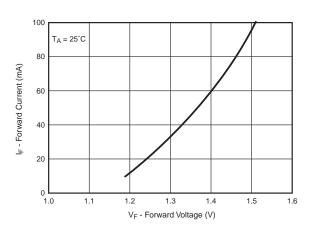


Figure 7. Low Output Voltage vs. Output Current

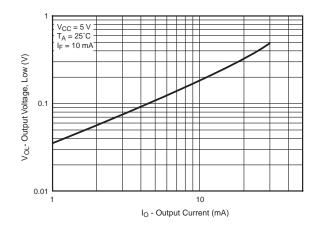


Figure 8. Response Time vs. Forward Current

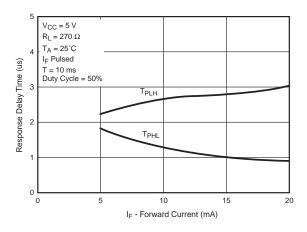




Figure 9. Switching Speed Test Circuit

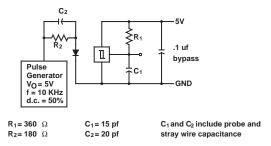


Figure 10. Typical Operating Circuit

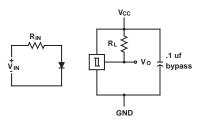


Figure 11. Switching Times Definition for Buffers

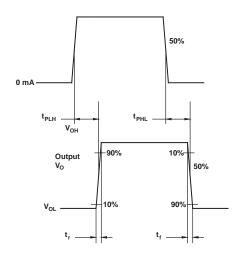
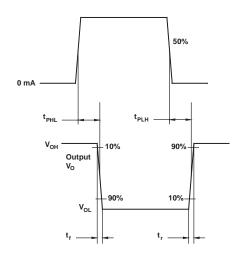


Figure 12. Switching Times Definition for Inverters





H21LTB H21LTI H21LOB H21LOI

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