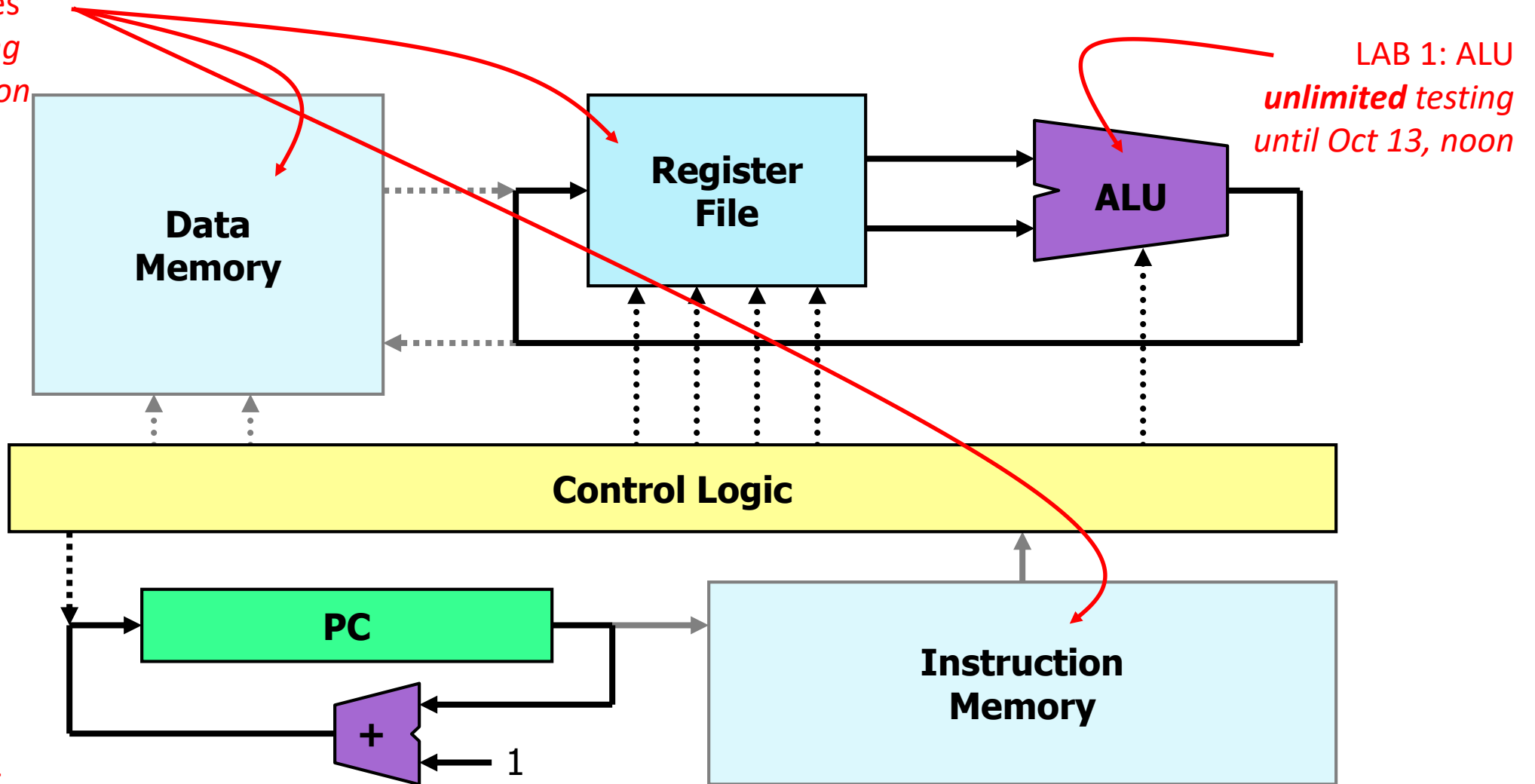


# First Graded Project: A Simple Multicycle Processor

LAB 2: Memories  
*unlimited* testing  
until Oct 13, noon



LAB 3: Processor

Adding control logic and specialized registers. Putting everything together (including ALU and memories).  
The best score among the first **three** team submissions is the **final** project score. Deadline: Nov 3, noon.