6/20/2016 Address Map for hps

Address Map for hps

Interface	Name	Start Address	End Address
		- Cart / Gail Oct	Elia / (dal 000
<u>stm</u>	STM Module	0xFC000000	0xFEFFFFF
<u>dap</u>	DAP Module	0xFF000000	0xFF1FFFFF
<u>lwfpgaslaves</u>	FPGA Slaves Accessed Via Lightweight HPS2FPGA AXI Bridge	0xFF200000	0xFF3FFFFF
lwhps2fpgaregs	LWHPS2FPGA AXI Bridge Module	0xFF400000	0xFF47FFFF
hps2fpgaregs	HPS2FPGA AXI Bridge Module	0xFF500000	0xFF507FFF
fpga2hpsregs	FPGA2HPS AXI Bridge Module	0xFF600000	0xFF67FFFF
<u>emac</u>	EMAC Module	0xFF700000	0xFF701FFF
		0xFF702000	0xFF703FFF
<u>sdmmc</u>	SDMMC Module	0xFF704000	0xFF7043FF
<u>qspiregs</u>	QSPI Flash Controller Module Registers	0xFF705000	0xFF7050FF
<u>fpgamgrregs</u>	FPGA Manager Module	0xFF706000	0xFF706FFF
<u>acpidmap</u>	ACP ID Mapper Registers	0xFF707000	0xFF707FFF
gpio	GPIO Module	0xFF708000	0×FF70807F
		0xFF709000	0xFF70907F
		0xFF70A000	0xFF70A07F
<u>I3regs</u>	L3 (NIC-301) GPV Registers	0xFF800000	0xFF87FFFF
<u>nanddata</u>	NAND Controller Module Data (AXI Slave)	0xFF900000	0xFF9FFFFF
<u>qspidata</u>	QSPI Flash Module Data (AHB Slave)	0xFFA00000	0xFFAFFFF

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<u>usb</u>	USB OTG Controller Module Registers	0xFFB00000	0xFFB3FFFF
		0×FFB40000	0×FFB7FFF
<u>nandregs</u>	NAND Flash Controller Module Registers (AXI Slave)	0×FFB80000	0xFFB807FF
<u>fpgamgrdata</u>	FPGA Manager Module Configuration Data	0×FFB90000	0xFFB90003
<u>uart</u>	UART Module	0xFFC02000	0xFFC020FF
		0xFFC03000	0xFFC030FF
<u>i2c</u>	I2C Module	0×FFC04000	0xFFC040FF
		0×FFC05000	0xFFC050FF
		0xFFC06000	0xFFC060FF
		0xFFC07000	0xFFC070FF
<u>timer</u>	Timer Module	0×FFC08000	0xFFC080FF
		0xFFC09000	0xFFC090FF
		0xFFD00000	0xFFD000FF
		0xFFD01000	0xFFD010FF
<u>sdr</u>	SDRAM Controller	0×FFC20000	0xFFC3FFFF
<u>l4wd</u>	L4 Watchdog Module	0xFFD02000	0xFFD020FF
		0×FFD03000	0xFFD030FF
clkmgr	Clock Manager Module	0×FFD04000	0xFFD041FF
<u>rstmgr</u>	Reset Manager Module	0×FFD05000	0xFFD050FF
<u>sysmgr</u>	System Manager Module	0×FFD08000	0xFFD0BFFF
<u>dmanonsecure</u>	nonsecure DMA Module Address Space	0×FFE00000	0xFFE00FFF
<u>dmasecure</u>	secure DMA Module Address Space	0×FFE01000	0xFFE01FFF
<u>spis</u>	SPI Slave Module	0xFFE02000	0xFFE0207F

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		0xFFE03000	0xFFE0307F	
<u>spim</u>	SPI Master Module	0xFFF00000	0xFFF000FF	
		0xFFF01000	0xFFF010FF	
<u>scanmgr</u>	Scan Manager Module Registers	0xFFF02000	0xFFF0201F	
rom	Boot ROM Module	0×FFFD0000	0xfffDffff	
<u>mpu</u>	MPU Module Address Space	0xFFFEC000	0xfffEDfff	
mpul2	MPU L2 cache controller Module Address Space	0xFFFEF000	0xfffefff	
<u>ocram</u>	On-chip RAM Module	0xFFFF0000	0xfffffff	

Register Summaries for hps

hps

Address map for the HHP HPS system-domain