



ARM ASSEMBLY LAB-4

By

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STATUS UPDATE

The ARM assembly code was written for 32 bit processor and verified using ARMSim simulator successfully.

EXERCISE – COMPARE TWO STRINGS OF ASCII CHARACTERS TO SEE WHICH IS LARGER.

Compare two strings of ASCII characters to see which is larger (i.e., which follows the other in alphabetical ordering). Both strings have the same length as defined by the LENGTH variable. The strings' starting addresses are defined by the START1 and START2 variables. If the string defined by START1 is greater than or equal to the other string, clear the GREATER variable; otherwise set the variable to all ones (0xFFFFFFFF).

The logic to solve this exercise is hand-coded in 32 bit ARM Assembly and verified on ARMSim Simulator.

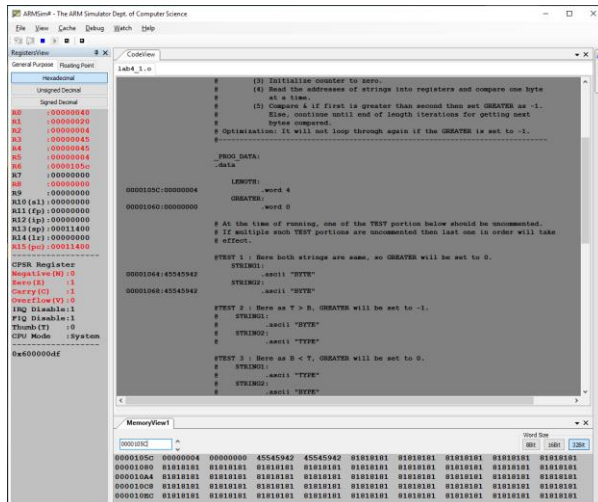
The logic used is mentioned in the code file itself and all the necessary instructions are supplied with comments.

Various unit tests are also part of the code file itself. Need to uncomment and run.

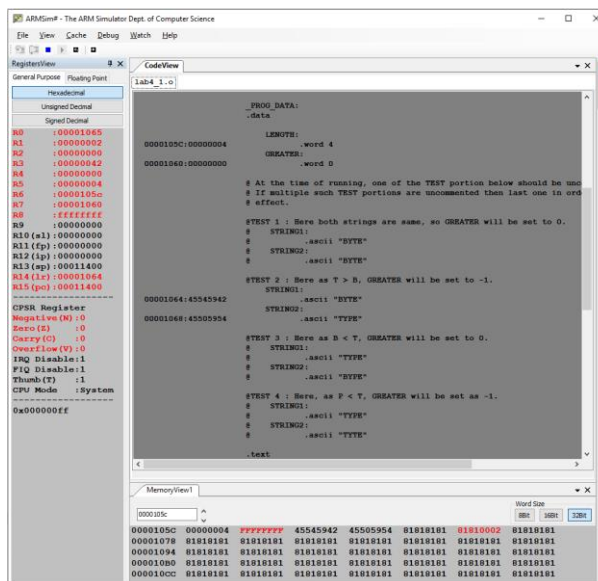
All the unit tests passed (various values of STRINGs).

SCREENSHOT - TEST 1

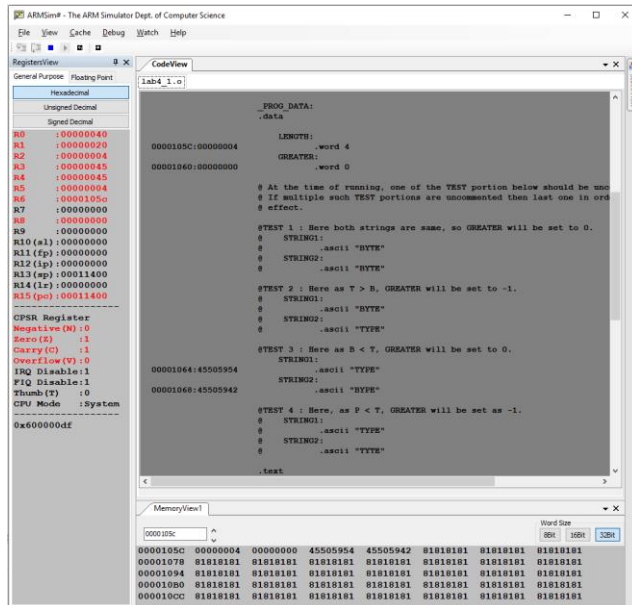
The sample output screenshot for the logic is – (Registers listed on the left are to be noted. All are zero to begin with). Similarly, the memory view in bottom pane of the Simulator Window. Output address (where GREATER is stored) is – 0x00001060.



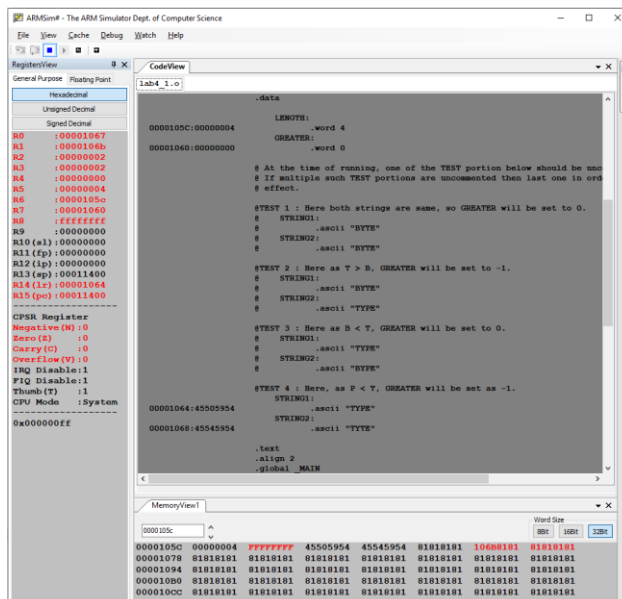
SCREENSHOT - TEST 2



SCREENSHOT - TEST 3



SCREENSHOT - TEST 4



EXERCISE – GIVEN TWO STRINGS, CHECK WHETHER THE SECOND STRING IS A SUBSTRING OF THE FIRST ONE.

Given two strings, check whether the second string is a substring of the first one. The starting addresses of two strings are defined by the STRING and SUBSTR variables, respectively. If the string defined by SUBSTR is not present in the string defined by STRING, clear the PRESENT variable; otherwise set the variable with the position of the first occurrence of the second string in the first string.

The logic to solve this exercise is hand-coded in 32 bit ARM Assembly and verified on ARMSim Simulator.

The logic used is mentioned in the code file itself and all the necessary instructions are supplied with comments.

Various unit tests are also part of the code file itself. Need to uncomment and run.

All the unit tests passed (various values of STRINGs & SUBSTRINGs).

SCREENSHOT – TEST 1

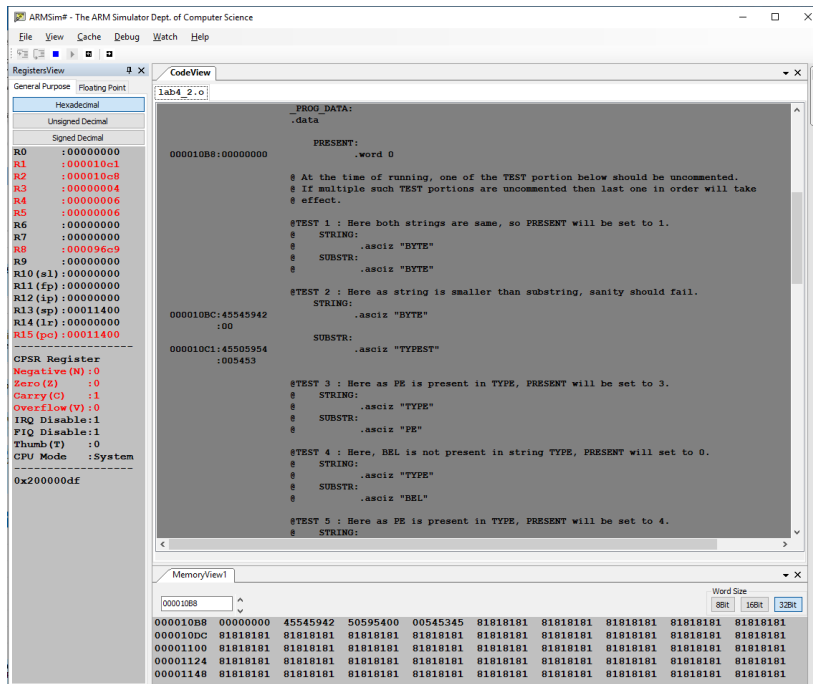
The screenshot displays the ARMSim Simulator interface. The main window is titled 'ARMSim# - The ARM Simulator Dept. of Computer Science'. It features a 'RegistersView' on the left, a 'CodeView' in the center, and a 'MemoryView' at the bottom.

RegistersView: Shows the state of 16 registers (R0-R15) and CPSR. R0-R9 are set to 0x00000004. R10-R15 are set to 0x00000000. CPSR fields: Negative(N):0, Zero(Z):1, Carry(C):1, Overflow(O):0, IRQ Disable:1, FIQ Disable:1, Thumb(T):0, CPU Mode: System.

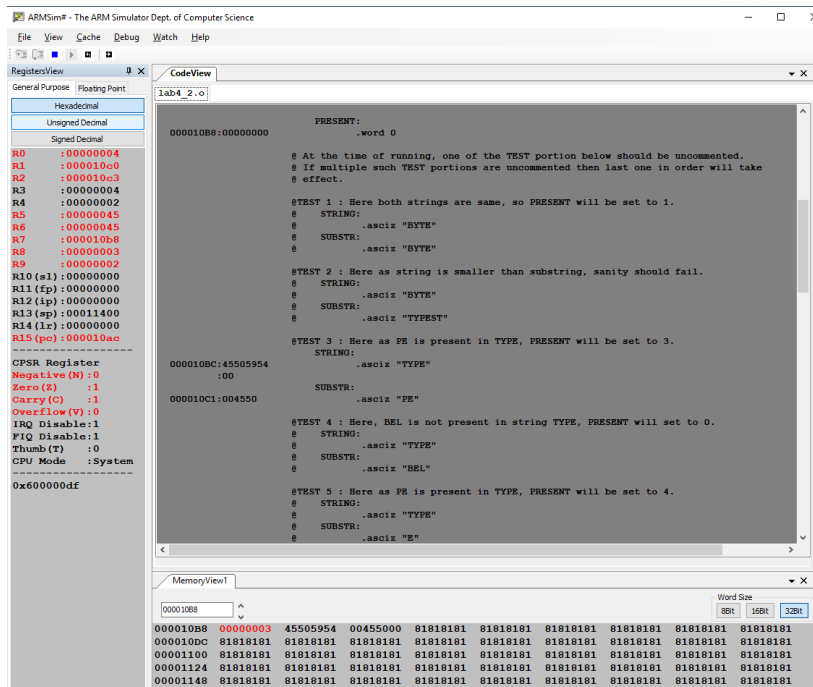
CodeView: Displays assembly code for a function named 'lab4_2.o'. The code includes comments and instructions for setting the 'PRESENT' variable based on string comparisons. Key comments include: 'At the time of running, one of the TEST portion below should be uncommented.', 'If multiple such TEST portions are uncommented then last one in order will take effect.', and several 'TEST' blocks for different string inputs like 'BYTE', 'TYPE', 'PE', and 'BEL'.

MemoryView: Shows the memory contents starting from address 0x00010B8. The memory is filled with 0x00000001 and 0x00000004 values, corresponding to the register values.

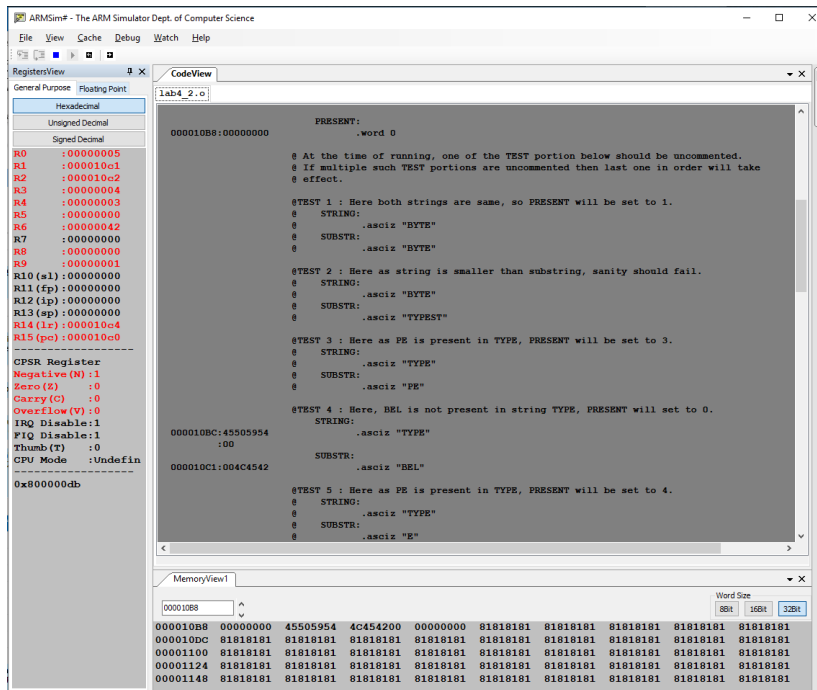
SCREENSHOT – TEST 2



SCREENSHOT – TEST 3



SCREENSHOT – TEST 4



SCREENSHOT – TEST 5

