


|                                      |  |  |  |
|--------------------------------------|--|--|--|
| Project Owner:<br>Robert Perkel      |  |  <b>MICROCHIP</b> |  |
| PCB Layout Contact:<br>Robert Perkel |  |  |  |
| PartNumber:<br>**                    | Project Title<br><b><i>Intelligent Cooling Plate with PIC16F17146 - UI</i></b> | Variant: [No Variations]   |  |
| Sheet Title<br>**                    |  | <div>Designed with</div> <div><b>Altium</b></div> <div>Altium.com</div>                                |  |
| Size    B                            | SCH #: 03-**<br>PCB #: 04-2  |  |  |
| File: 171xx Cold Plate UI.SchDoc     |  |  |  |



Intelligent Cooling Plate, UI Board  
Schematic Rev 2  
PCB Rev 2

DISP1

C2  
C1

C3  
C4

+5V D3 R13  
+12V D4 R14

UI\_BOARD\_CONN  
2 12  
1 11  
C11 C8  
C10 C9

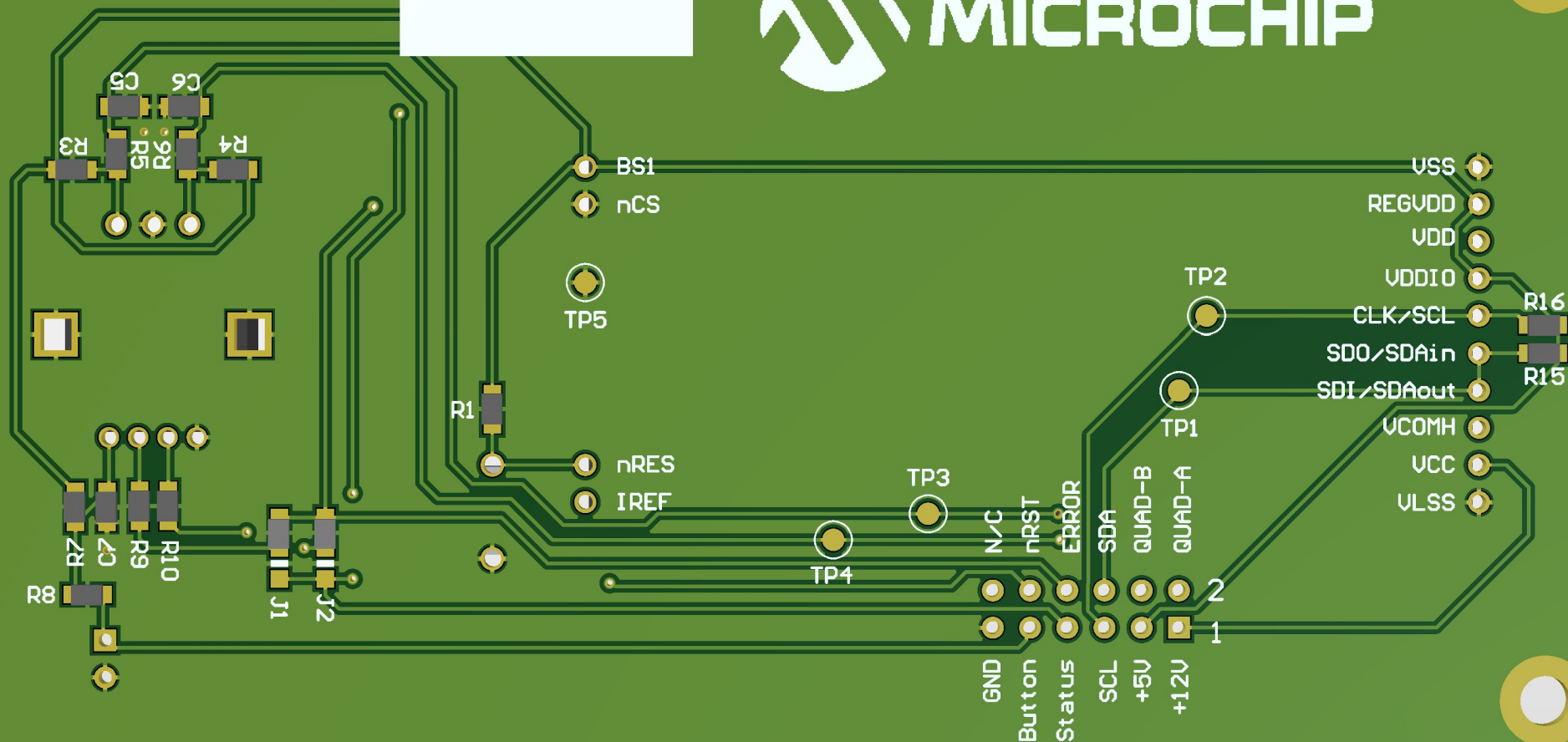
R11 D1 System ON  
R12 D2 Error

DISP\_RST

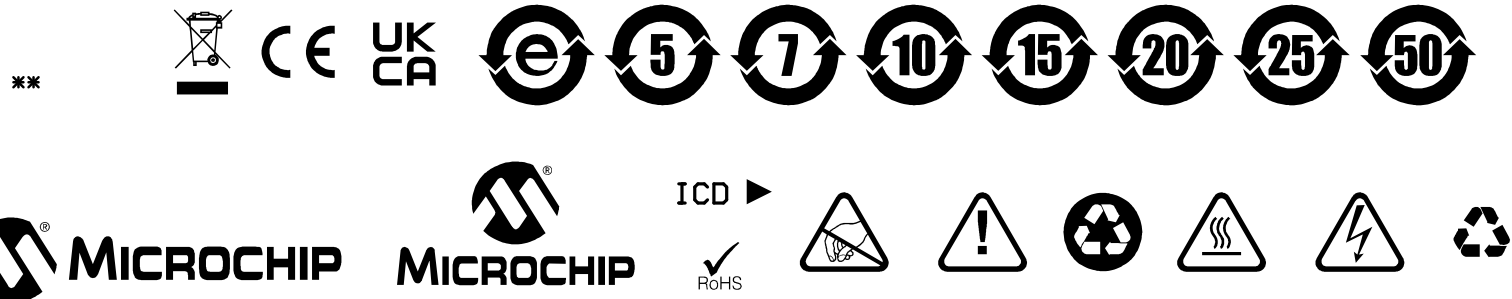
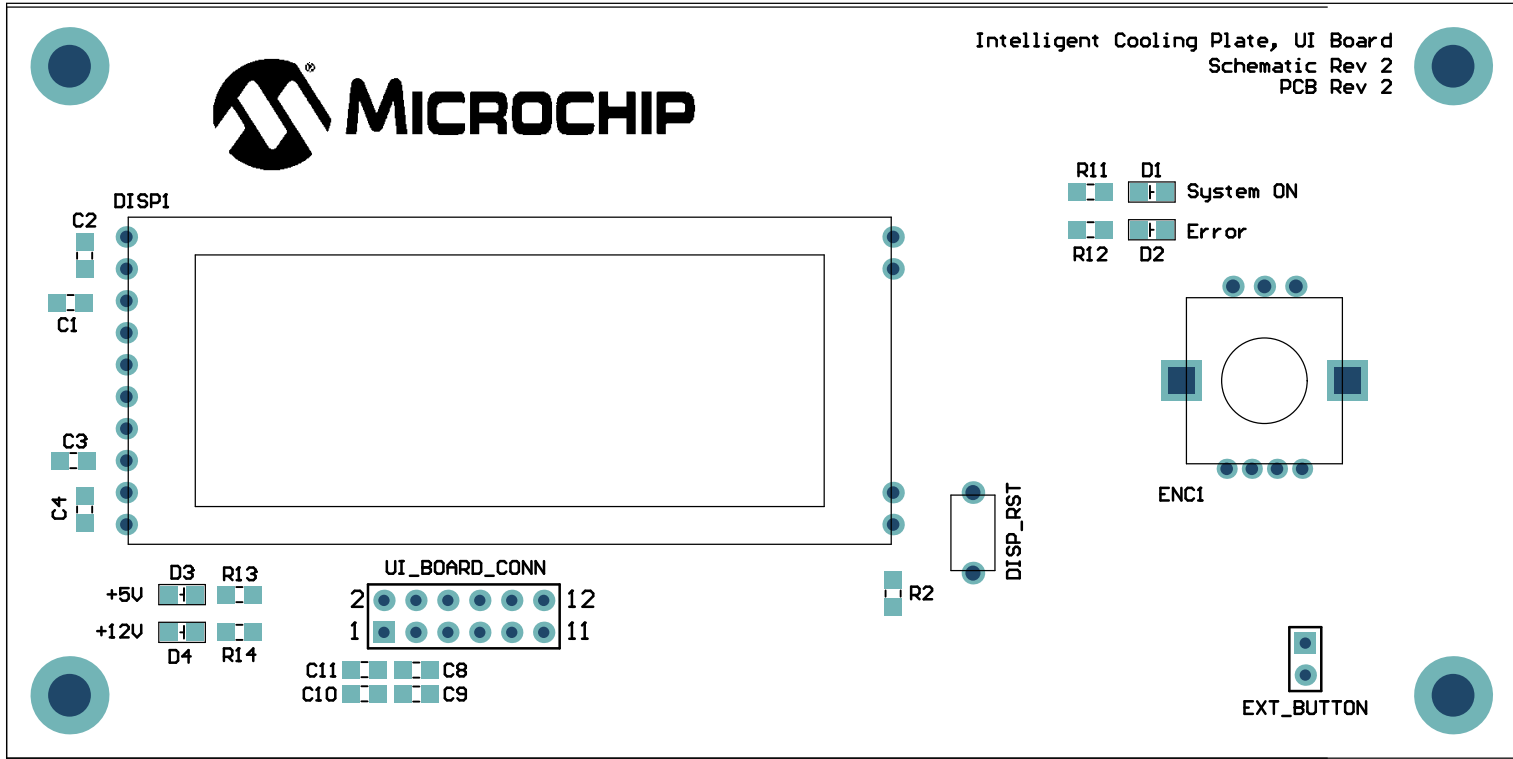
ENC1

EXT\_BUTTON


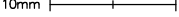
Serial No.



| REV | ECO | Comments | Date |
|-----|-----|----------|------|
|     |     |          |      |



dsPICDEM.net™ mTouch® chipKIT™  
dsPICDEM™ MPLAB® dsPIC®  
PICDEM.net™ PICKit™ PIC®  
PICDEM™ PICtail™

|  |  |                                   |                           |   |
|--|--|-----------------------------------|---------------------------|---|
| <br><b>MICROCHIP</b><br>Microchip Technology Inc.<br>2355 W. Chandler Blvd.<br>Chandler, AZ 85224 | TITLE:<br><b>Intelligent Cooling Plate with PIC16F17146 – UI</b> |                                   | PART NUMBER: <b>**</b>    |   |
|  | PCB Layout Contact:<br><b>Robert Perkel</b>                      | GERBER FILE:<br><b>'TOP Silk'</b> |                           |   |
|  | Project Owner:<br><b>Robert Perkel</b>                           | BOARD NUMBER:<br><b>04-2</b>      | DATE:<br><b>12/8/2022</b> |   |
|  | PCB FILE NAME:<br><b>171xx_Cold Plate UIPcbDoc</b>               | LAYER NAME:<br><b>Board (14)</b>  | REV:<br><b>2</b>          |  |

A

B

C

D

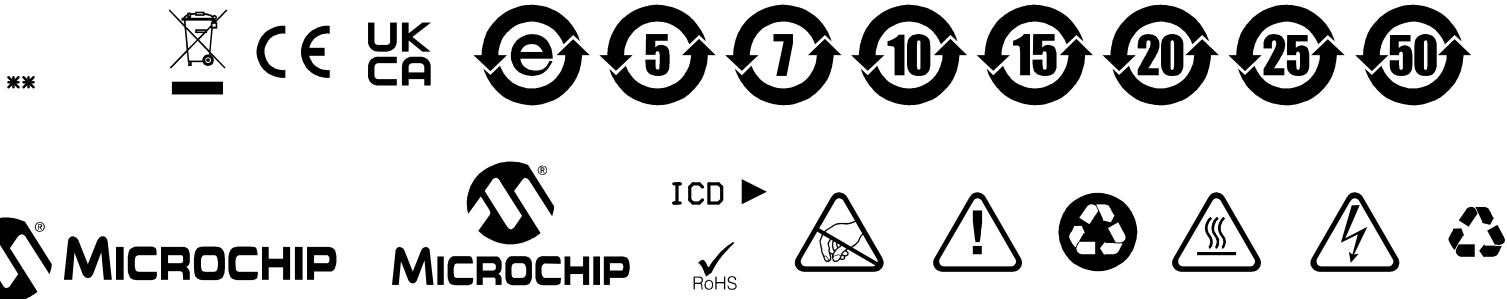
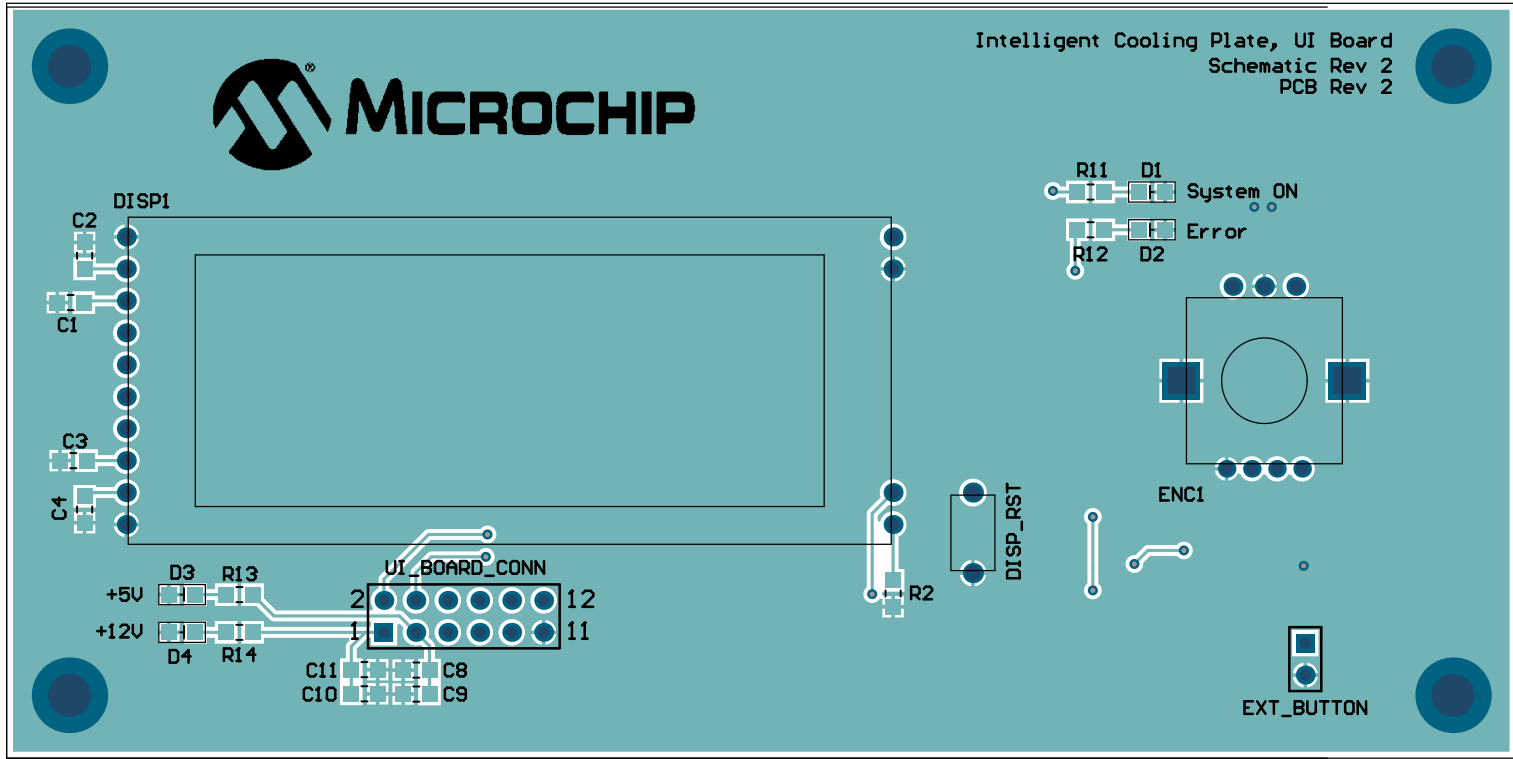
A

B

C

D

| REV | ECO | Comments | Date |
|-----|-----|----------|------|
|     |     |          |      |



dsPICDEM.net™ mTouch® chipKIT™

dsPICDEM™ MPLAB® dsPIC®

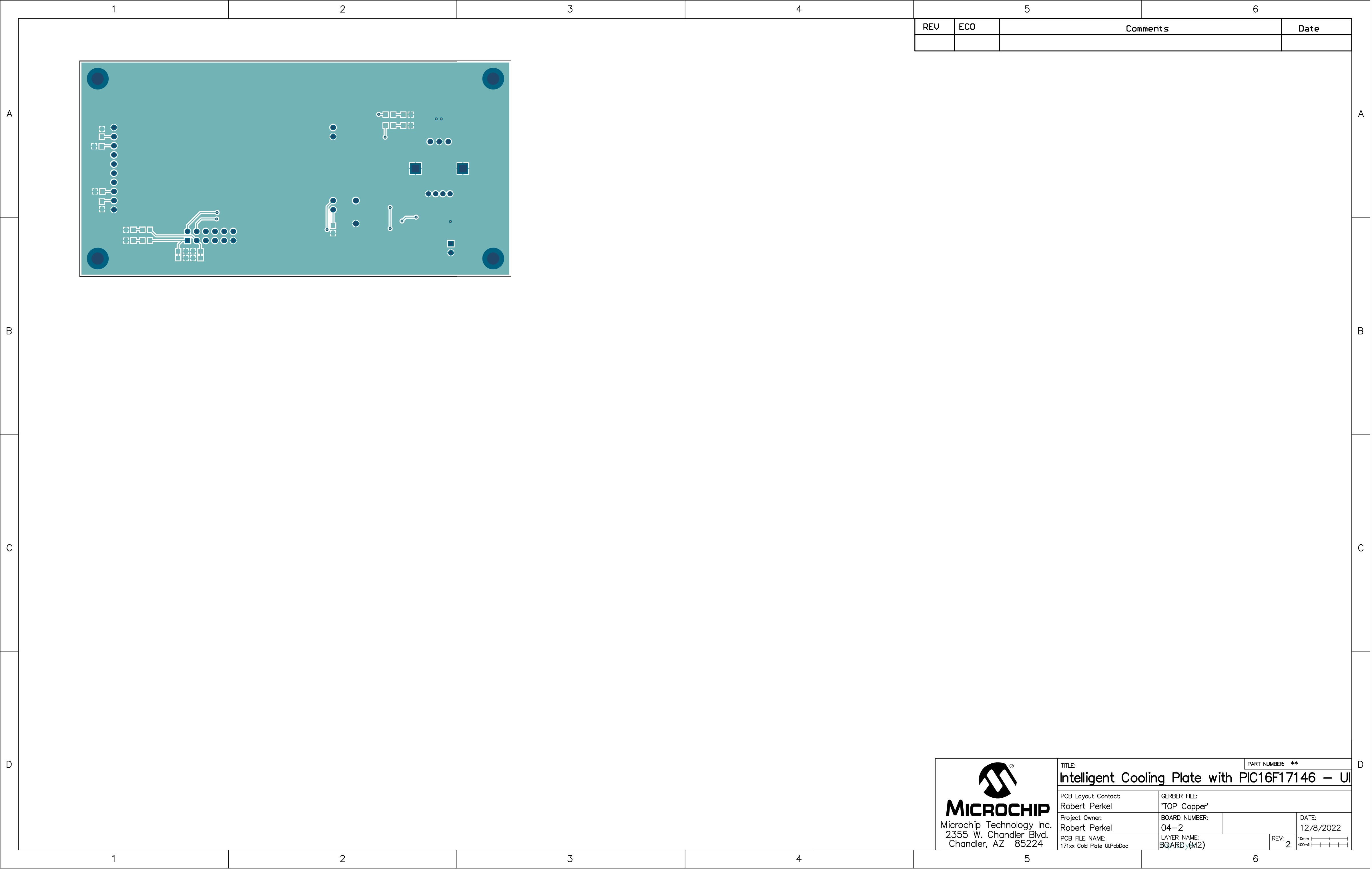
PICDEM.net™ PICKit™ PIC®

PICDEM™ PICtail™


**MICROCHIP**  
Microchip Technology Inc.  
2355 W. Chandler Blvd.  
Chandler, AZ 85224

|  |  |                                     |                    |
|--|--|-------------------------------------|--------------------|
| TITLE:<br><b>Intelligent Cooling Plate with PIC16F17146 – UI</b> |  | PART NUMBER: **                     |                    |
| PCB Layout Contact:<br>Robert Perkel                             |  | GERBER FILE:<br>'TOP Copper & Silk' |                    |
| Project Owner:<br>Robert Perkel                                  |  | BOARD NUMBER:<br>04-2               | DATE:<br>12/8/2022 |
| PCB FILE NAME:<br>171xx_Cold Plate UIPcbDoc                      |  | LAYER NAME:<br>BOARD (1/2)          | REV:<br>2          |

10mm  
400mil



| REV | ECO | Comments | Date |
|-----|-----|----------|------|
|     |     |          |      |

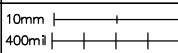


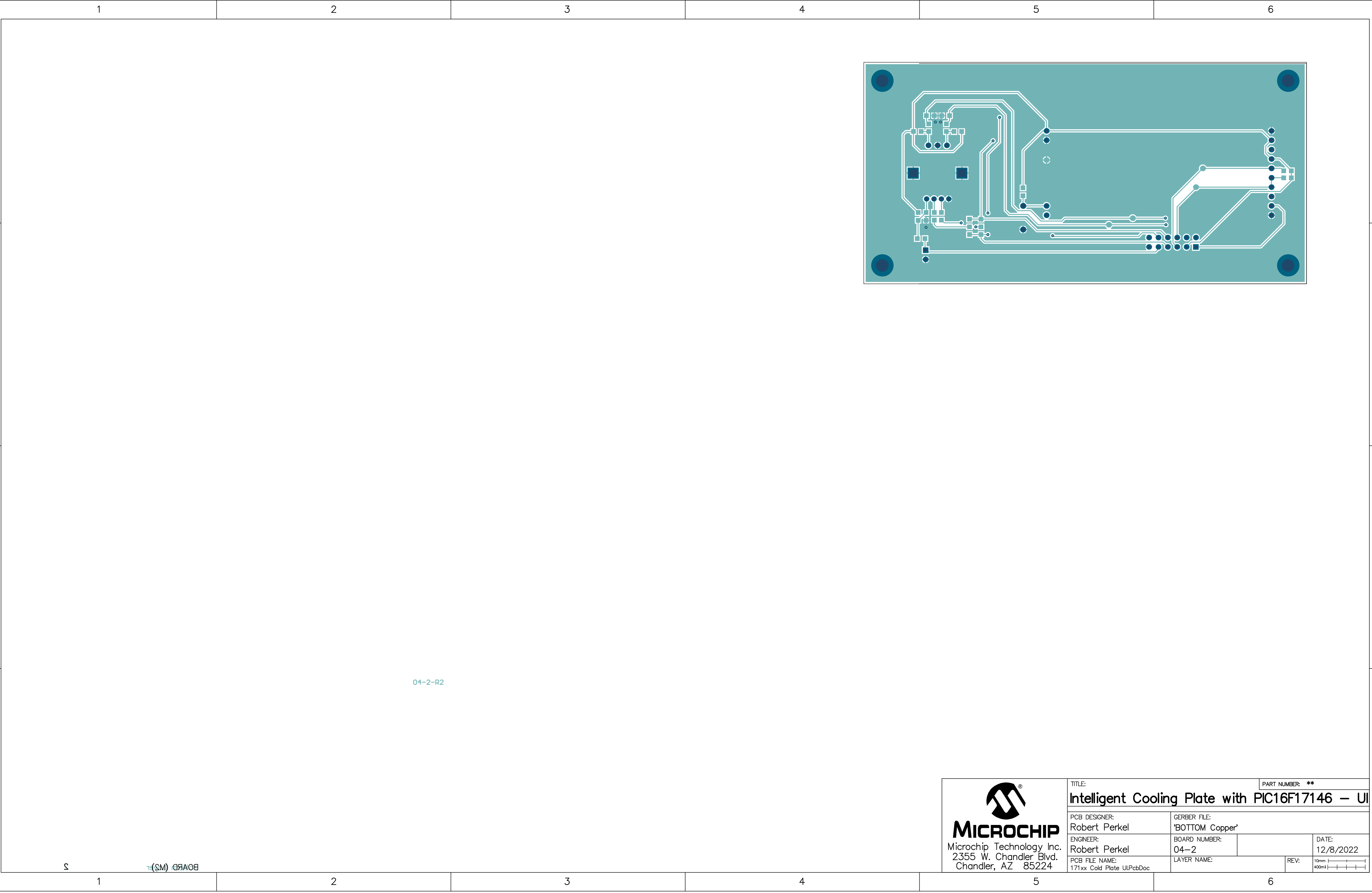
**MICROCHIP**  
Microchip Technology Inc.  
2355 W. Chandler Blvd.  
Chandler, AZ 85224

TITLE:  
**Intelligent Cooling Plate with PIC16F17146 – UI**

|  |  |                              |                    |
|--|--|------------------------------|--------------------|
| PCB Layout Contact:<br>Robert Perkel         |  | GERBER FILE:<br>'TOP Copper' |                    |
| Project Owner:<br>Robert Perkel              |  | BOARD NUMBER:<br>04-2        | DATE:<br>12/8/2022 |
| PCB FILE NAME:<br>171xx_Cold Plate_UI.PcbDoc |  | LAYER NAME:<br>BOARD (M2)    | REV: 2             |

PART NUMBER: \*\*



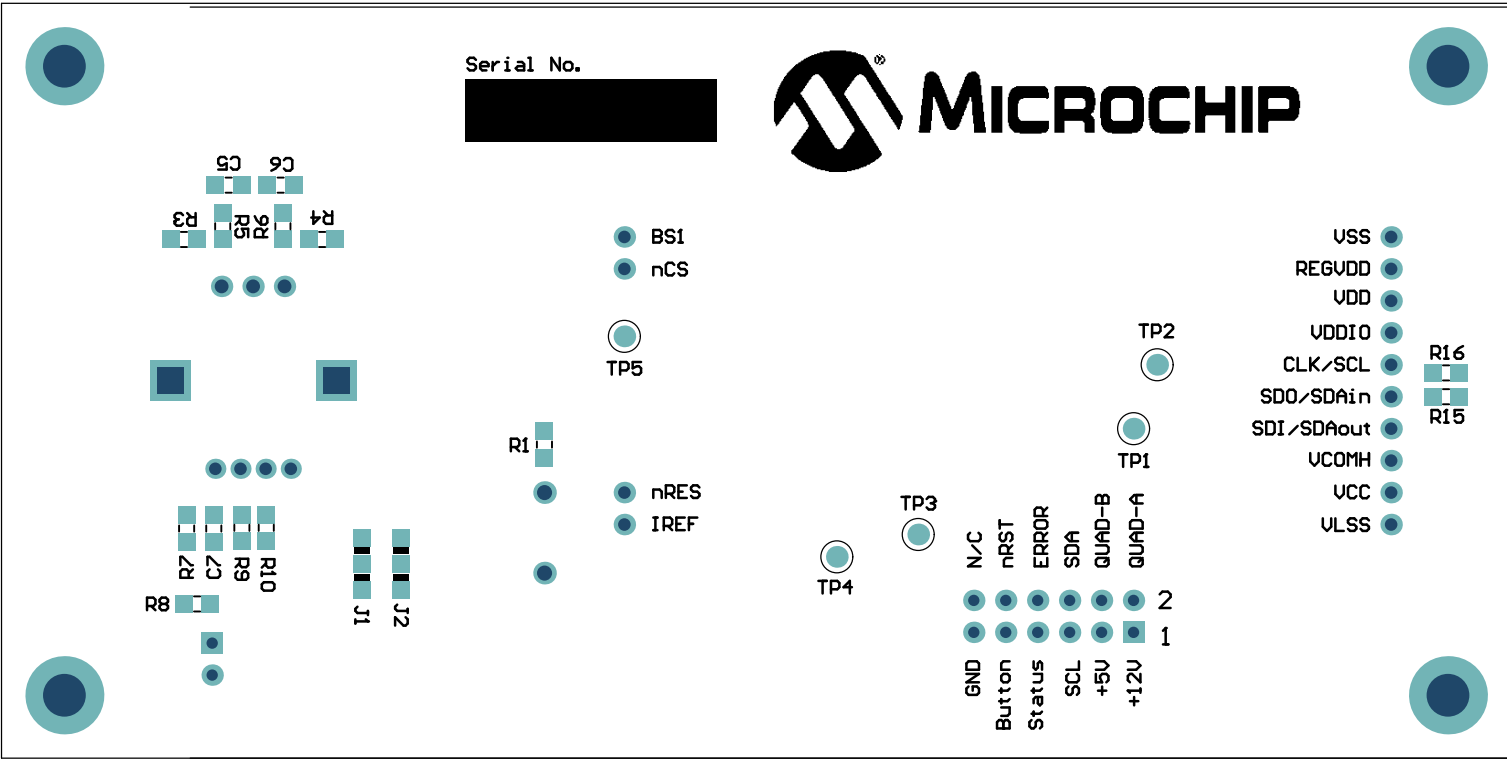


04-2-R2

BOARD (MS) 171xx Cold Plate UIPcbDoc











**MICROCHIP**  
Microchip Technology Inc.  
2355 W. Chandler Blvd.  
Chandler, AZ 85224

TITLE:

PART NUMBER: \*\*

Intelligent Cooling Plate with PIC16F17146 – UI

|   |                               |      |  |
|---|-------------------------------|------|--|
| PCB DESIGNER:<br>Robert Perkel              | GERBER FILE:<br>'BOTTOM Silk' |      |  |
| ENGINEER:<br>Robert Perkel                  | BOARD NUMBER:<br>04-2         |      | DATE:<br>12/8/2022   |
| PCB FILE NAME:<br>171xx Cold Plate UIPcbDoc | LAYER NAME:                   | REV: | 10mm<br><br>400mil<br> |