

[00] - Title and System Overview

AMMDK Common Compute Board

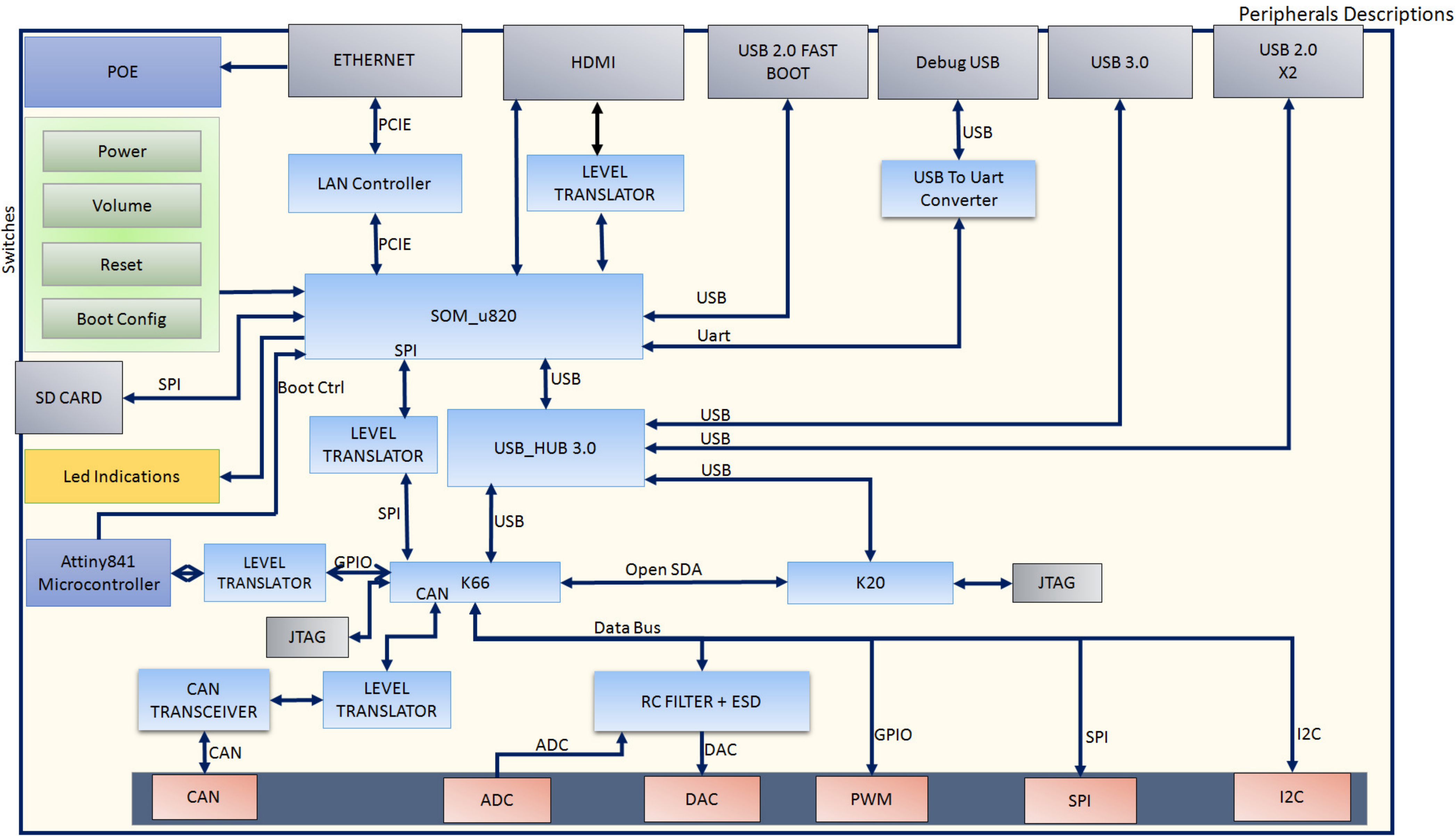
SHEET TITLE:	INTERNAL CODE NAME:	PART NAME:	<div>ENTROPIC ENGINEERING</div> <div>Entropic Engineering LLC 670 Transfer Rd St Paul, MN 55114 United States Phone: +1 (612) 860 8901 Email: team@entreng.com</div> <div>© ENTROPIC ENGINEERING 2017</div>
[00] - Title Block	AMMDK	AMM Common Compute Board	
VARIANT ID	CLIENT PROJECT NAME:	PART NUMBER	
No Variant	Advanced Modular Manikin	100000	
DOCUMENT NUMBER:	CLIENT CONTRACT NUMBER:	AUTHOR:	
Q08SQ0RV	BPO27990	Matthew Pang	
REVISION	MAIN CONTRACT NUMBER:	APPROVED BY:	
0.1.0	W81XWH-14-C-0101	Benjamin Riggs	
RELEASE LEVEL:	PREPARED FOR CLIENT:	CHECKED BY:	
Draft	UW CREST	Gus O'Hanley	
Date: 2/14/2018		Time: 5:03:11 PM	Sheet 1 of
Distribution Statement:		Approved for Public Release, Distribution Unlimited	Size: A3

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LOGICAL BLOCK DIAGRAM



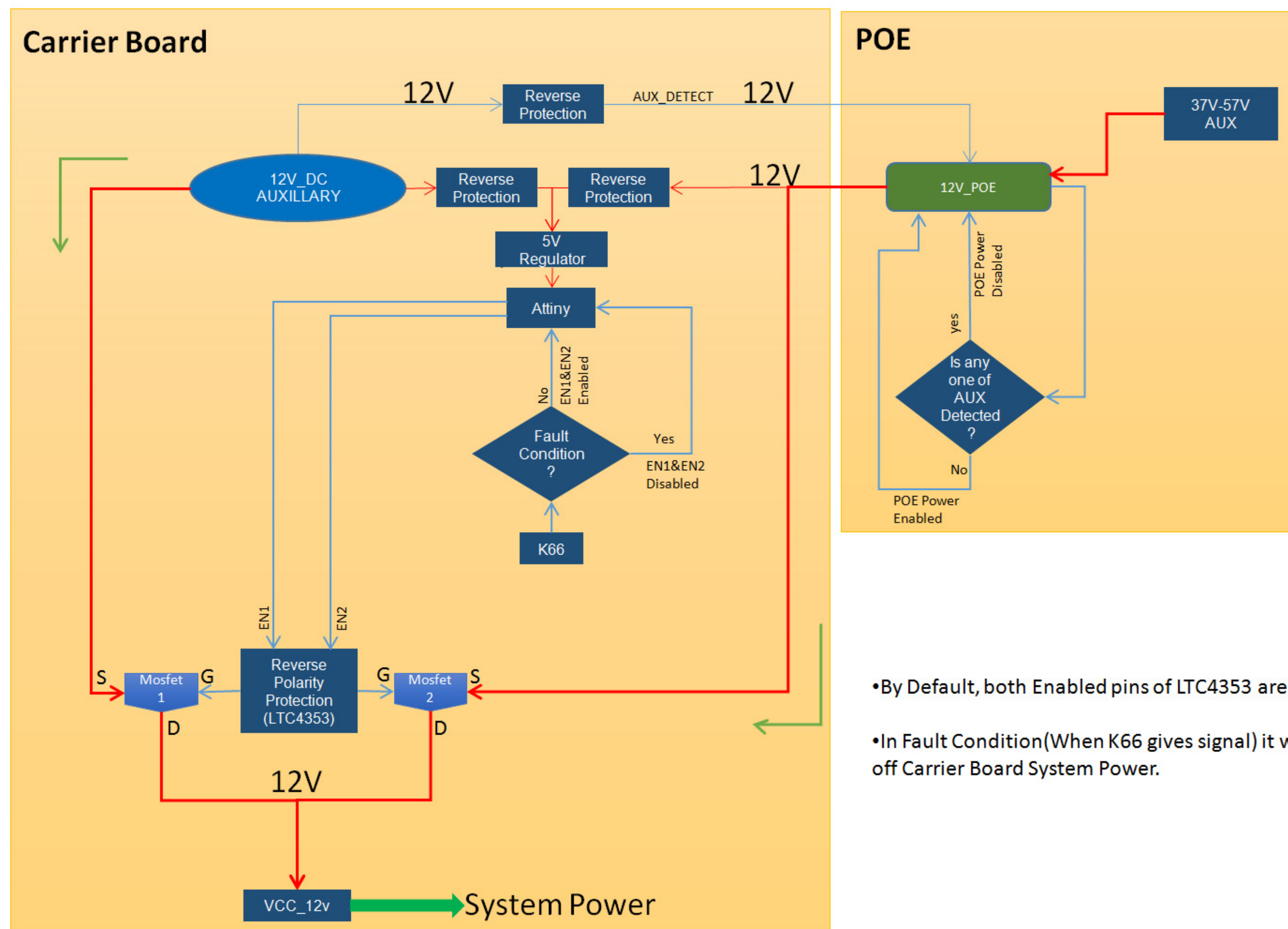
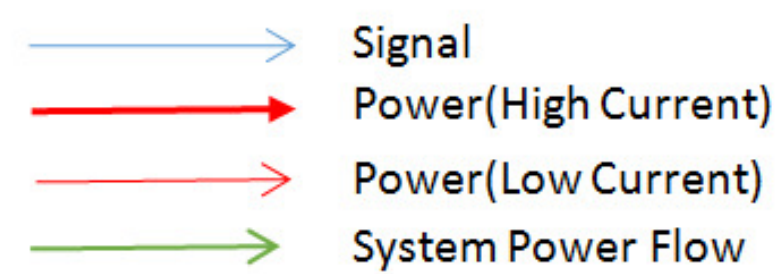
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Power Flow & Distribution

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POWER FLOW BLOCK DIAGRAM 2

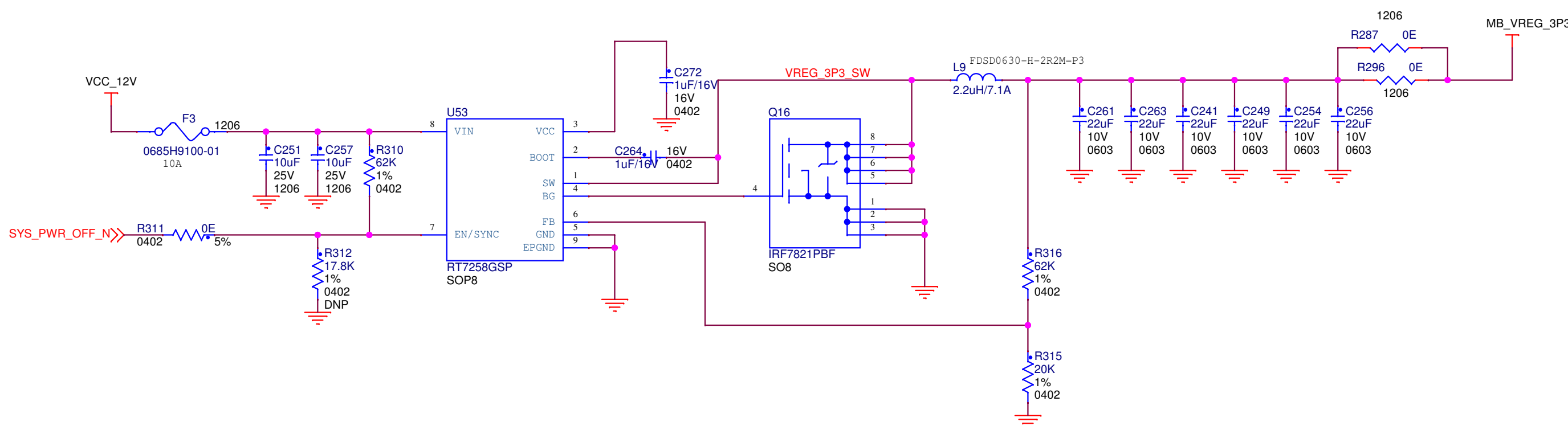


- By Default, both Enabled pins of LTC4353 are pulled low.
- In Fault Condition(When K66 gives signal) it would turn off Carrier Board System Power.

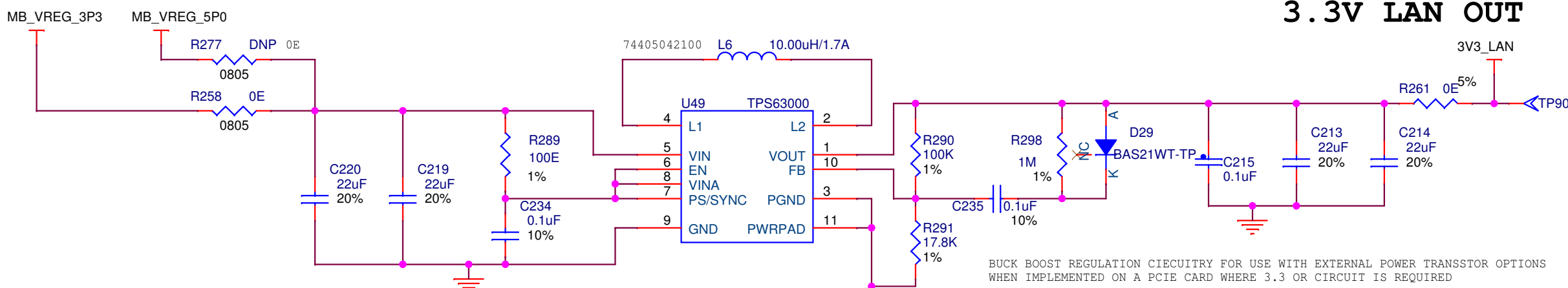
Entropic Manikin LLC, 670 Transfer Rd, St Paul, MN55114 USA	TITLE Block Diagram 2				
	SIZE C	DRAWING NO AMMDK-0A1			REV 2.1
	SCALE NONE	RELEASE DATE 21/08/2018		SHEET 5 OF 24	

12V TO 3V3 REGULATOR

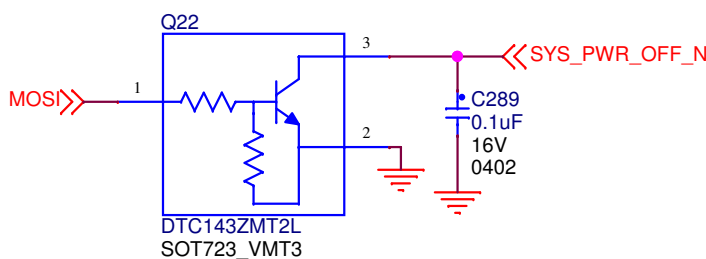
3.3V OUT



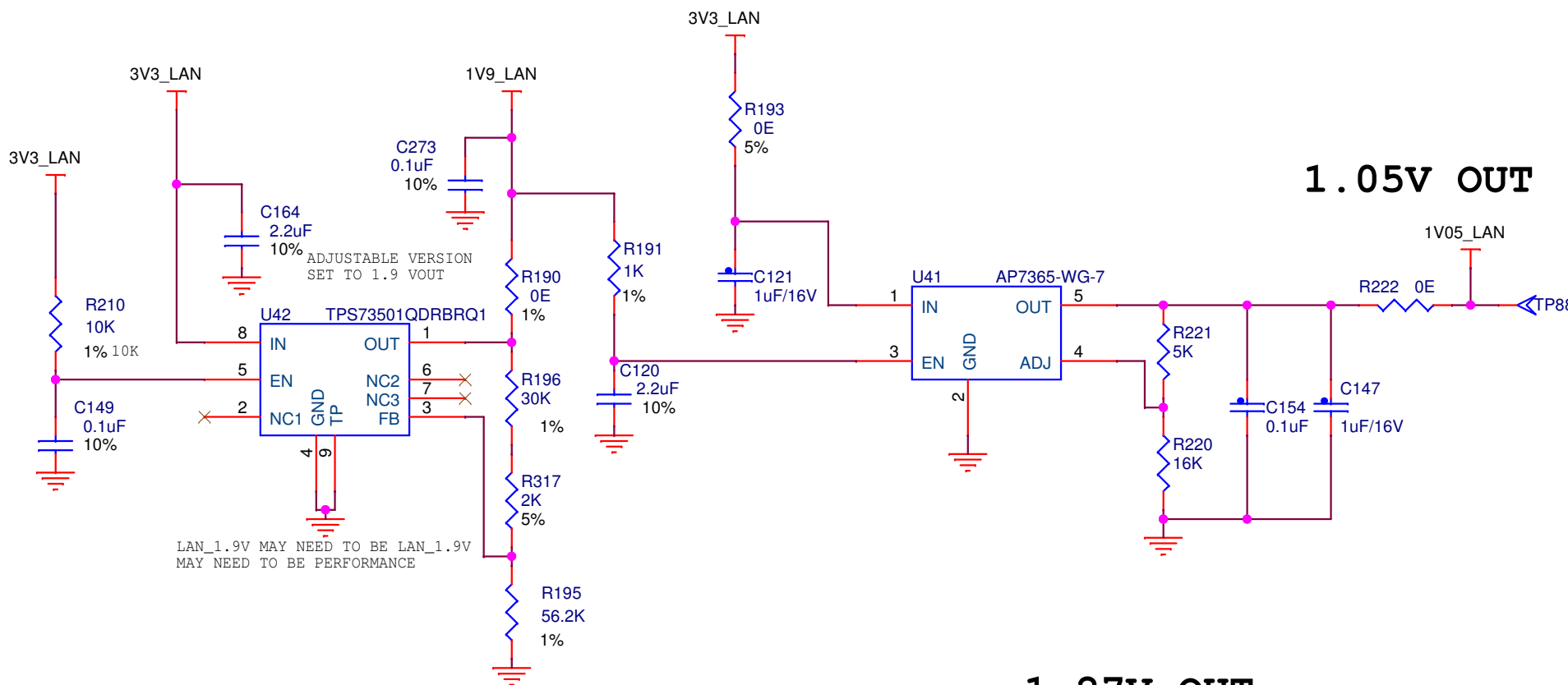
3.3V LAN OUT



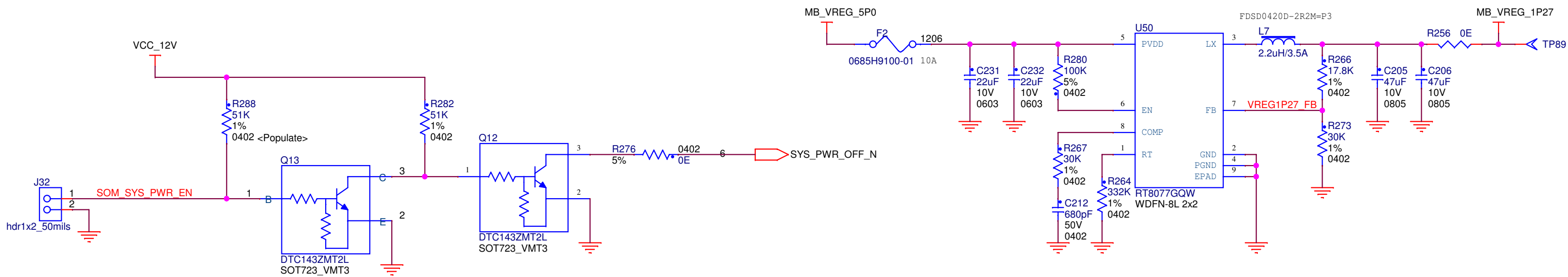
BUCK BOOST REGULATION CIRCUITRY FOR USE WITH EXTERNAL POWER TRANSISTOR OPTIONS WHEN IMPLEMENTED ON A PCIE CARD WHERE 3.3 OR CIRCUIT IS REQUIRED



1.05V OUT



1.27V OUT



TITLE
Power Regulator 2

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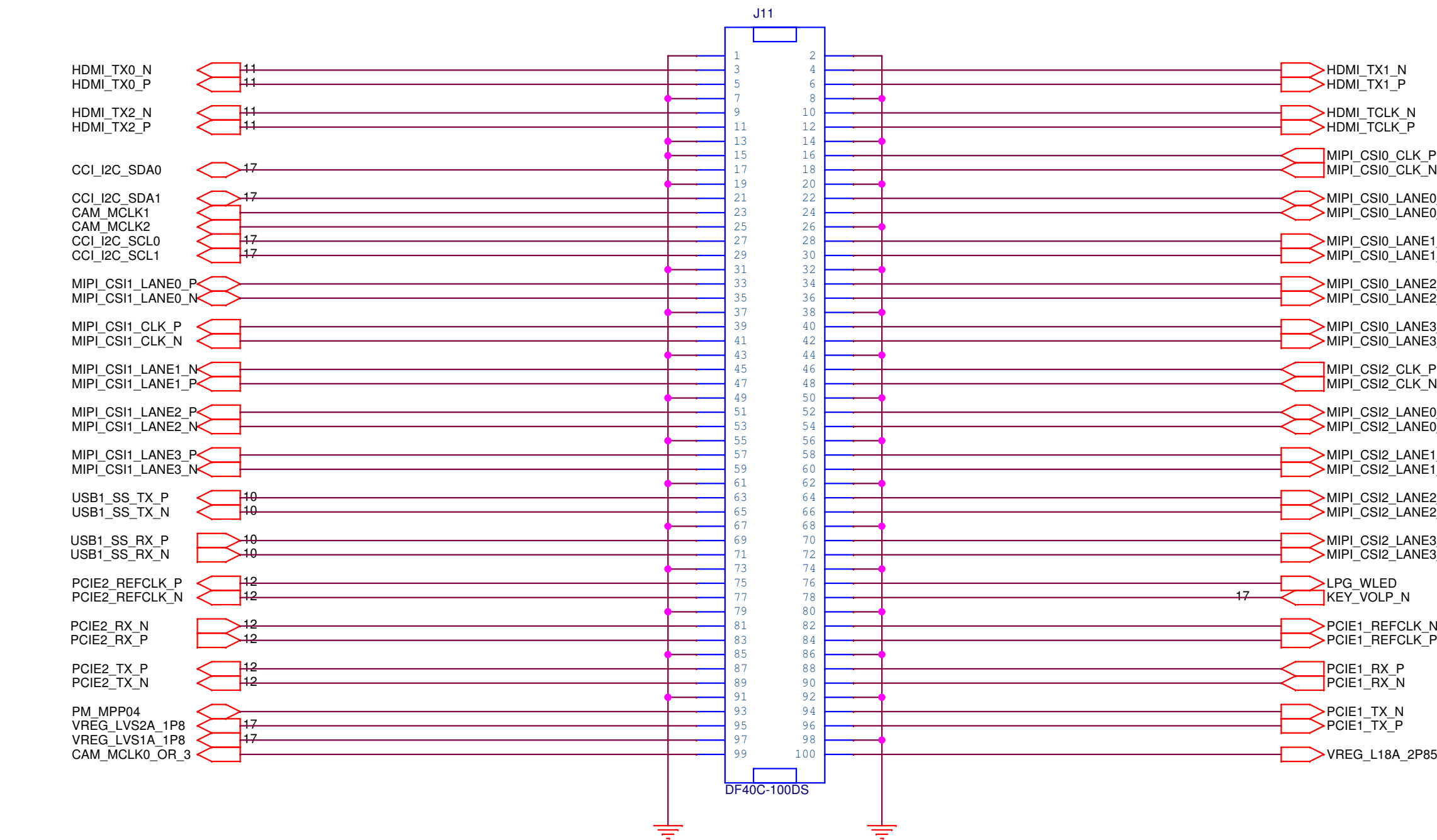
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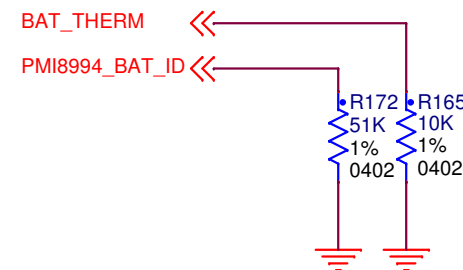
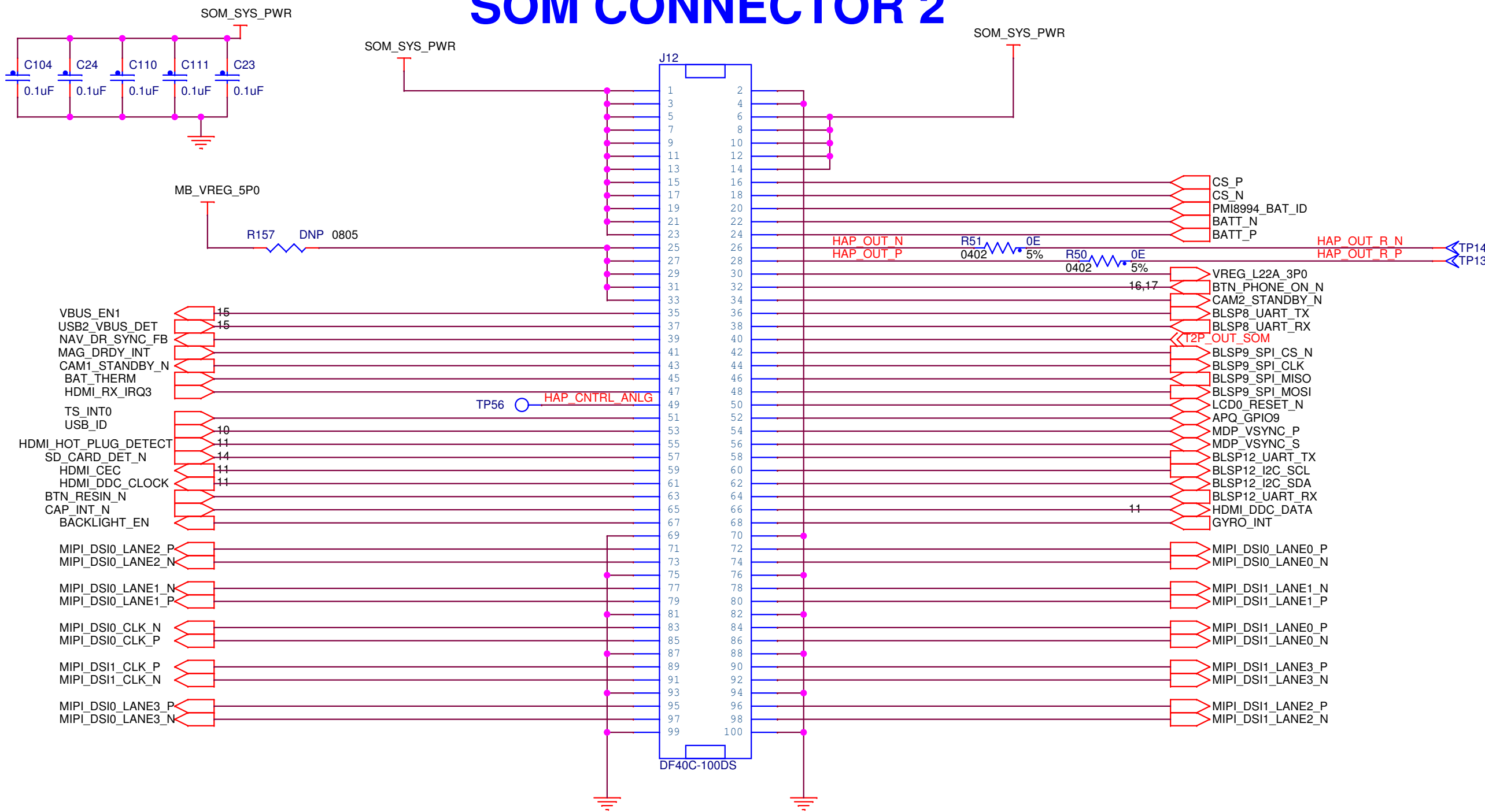
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SOM CONNECTOR 1



SOM CONNECTOR 2



TITLE
SOM B2B-1

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C

DRAWING NO
AMMDK-0A1

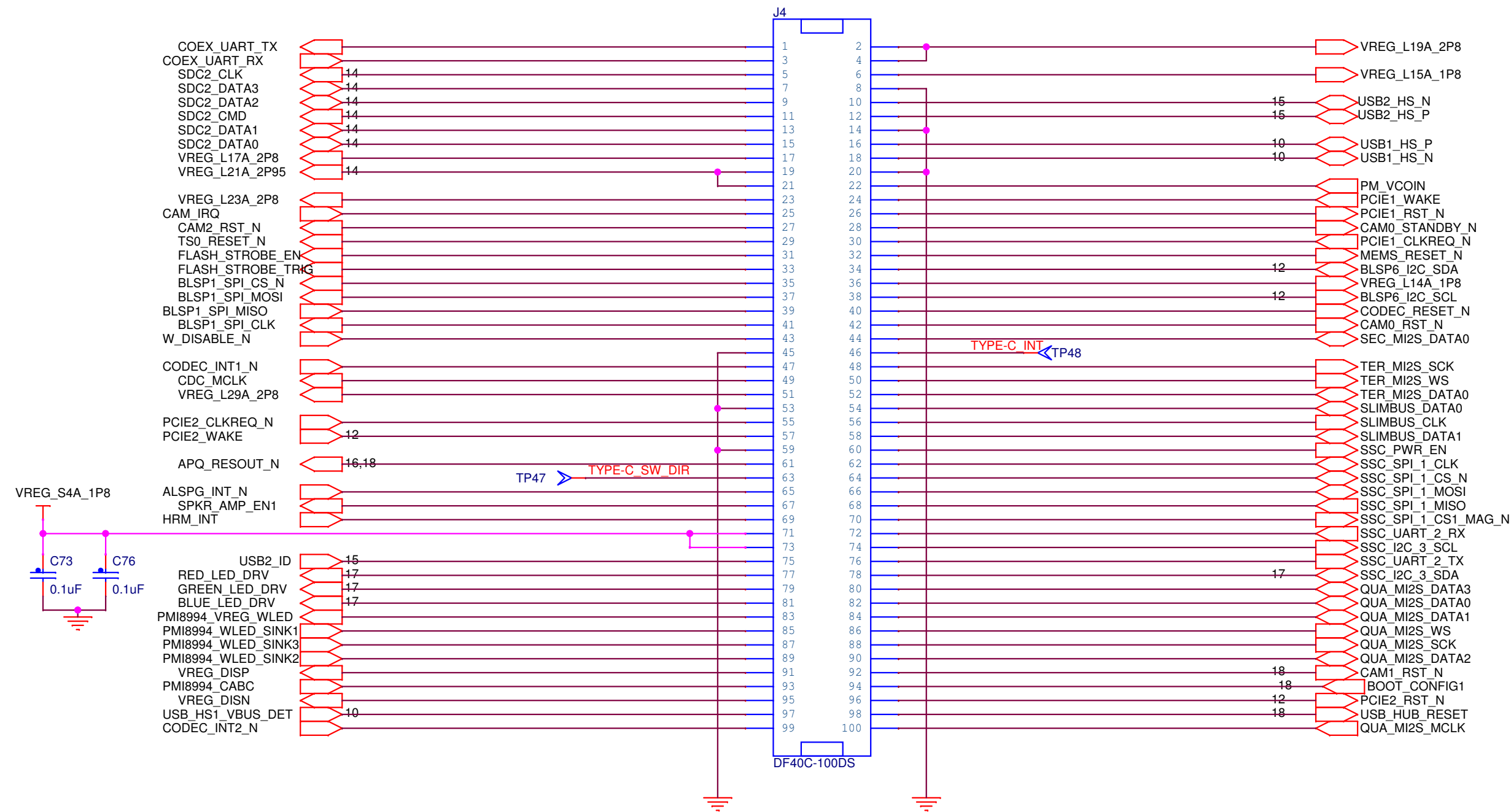
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SOM CONNECTOR 3



TITLE
SOM B2B-2

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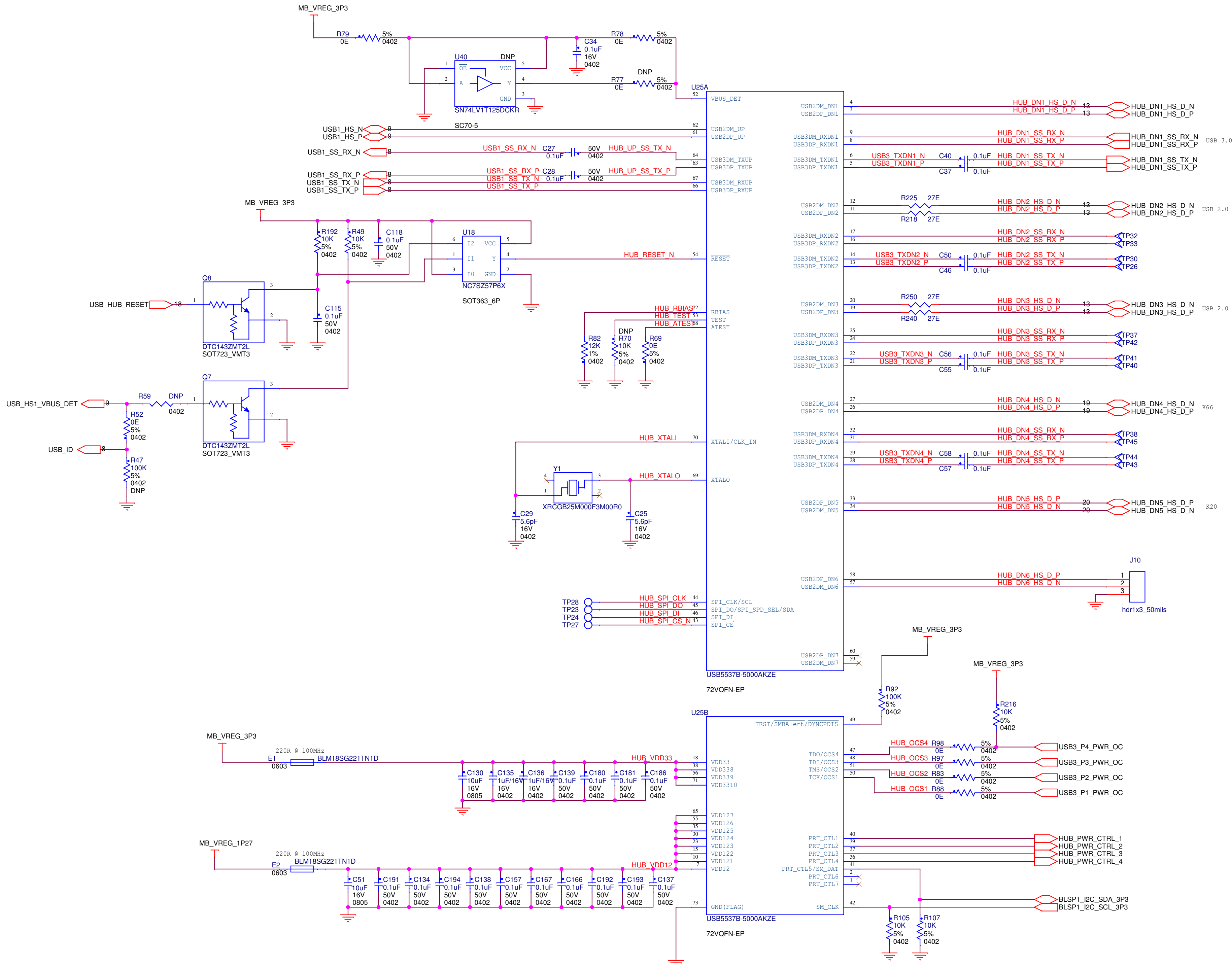
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USB HUB 3.0



TITLE USB HUB 3.0

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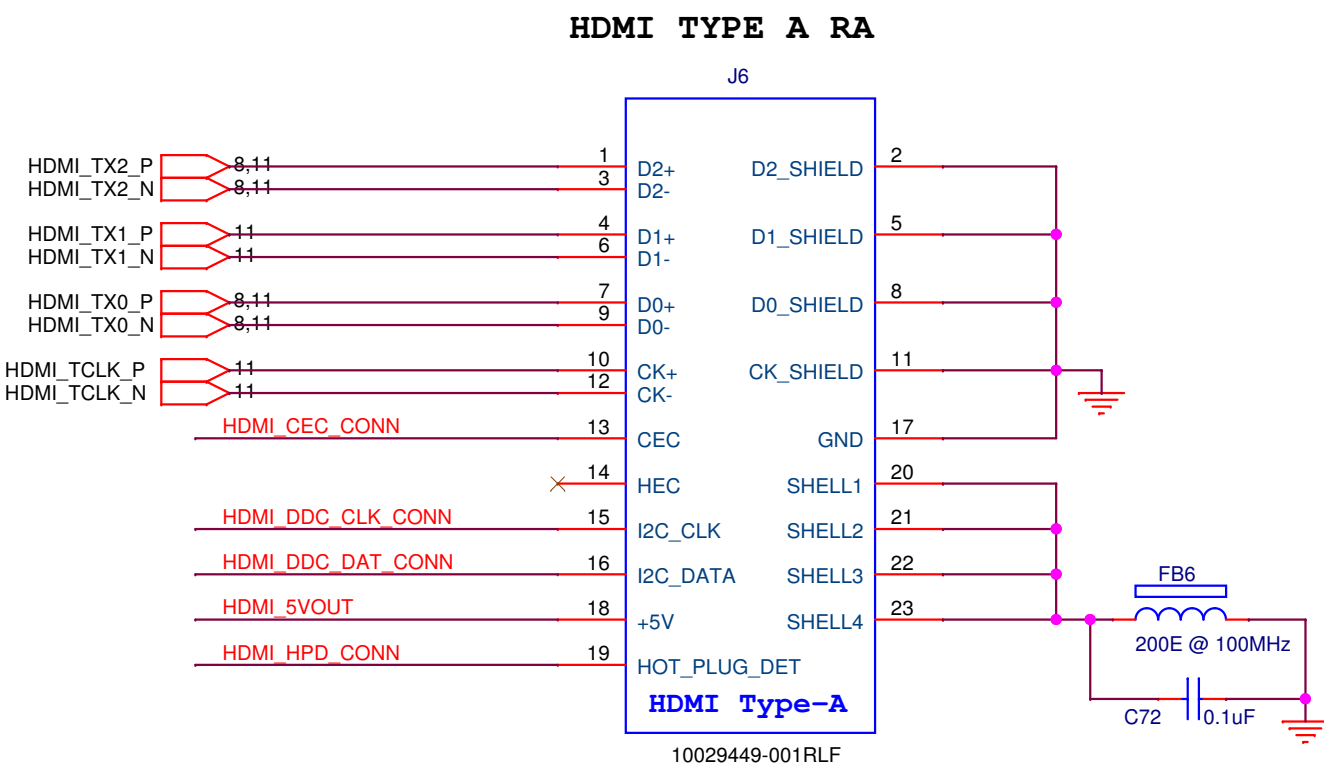
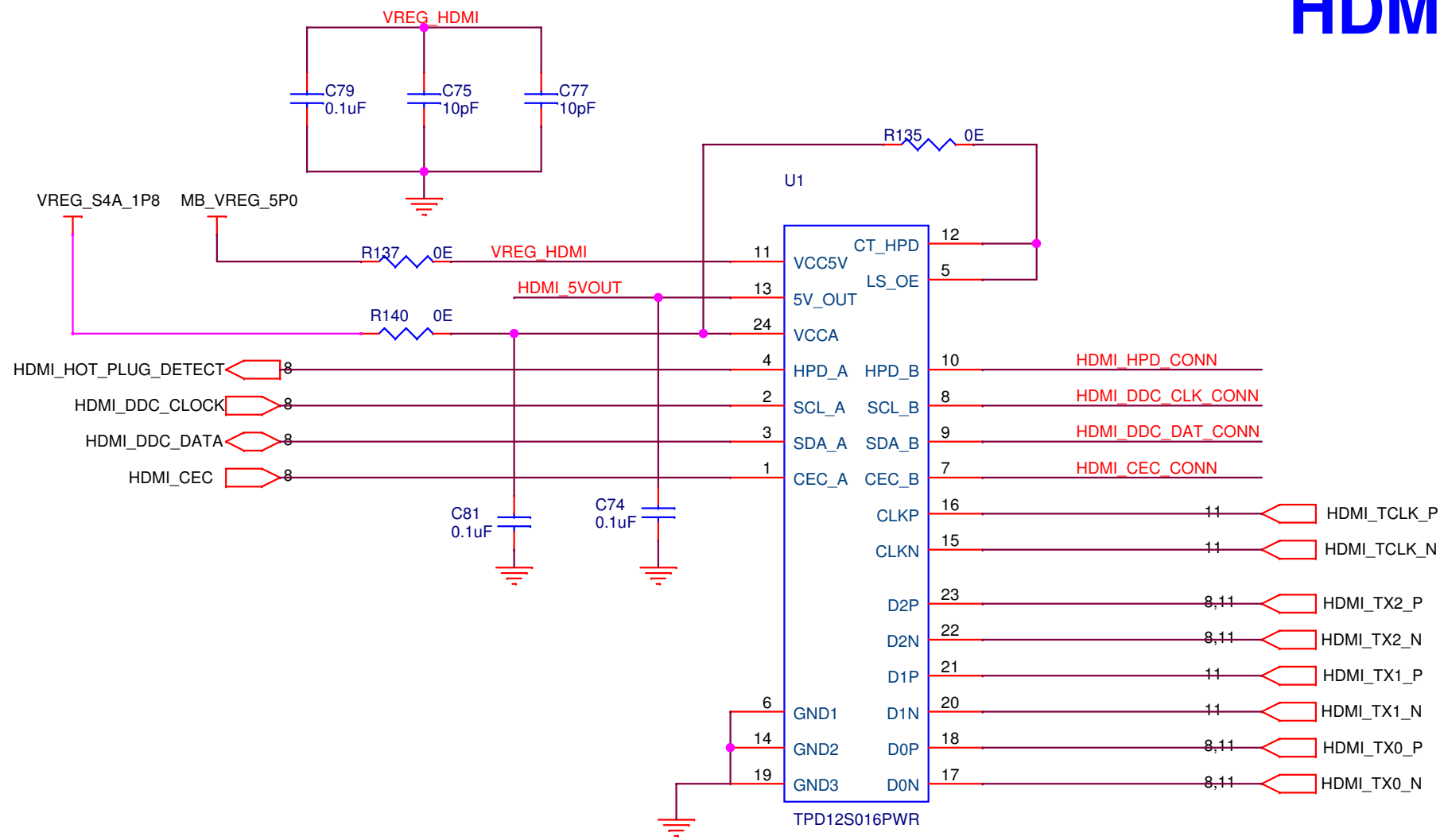
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HDMI



TITLE
HDMI

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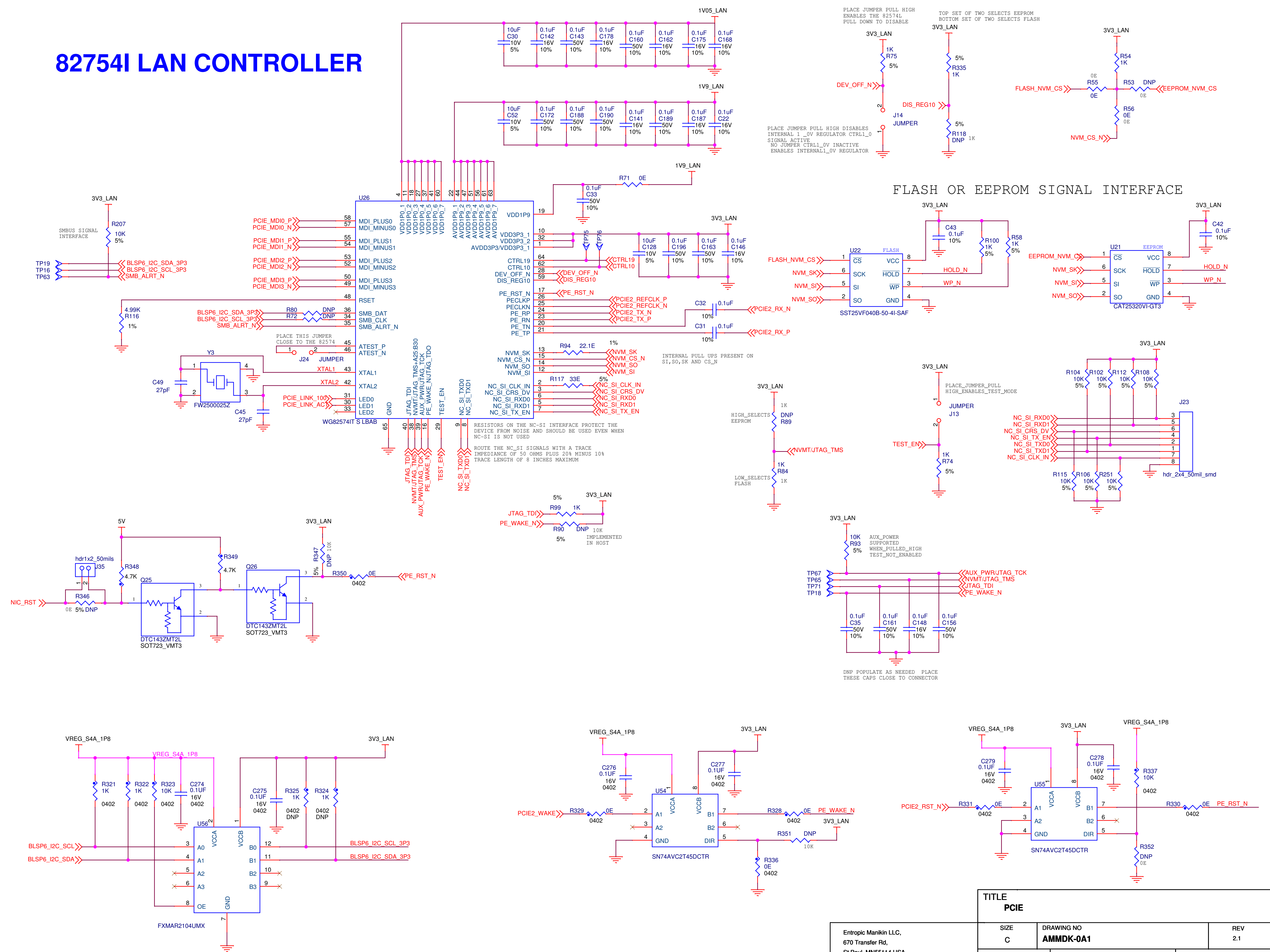
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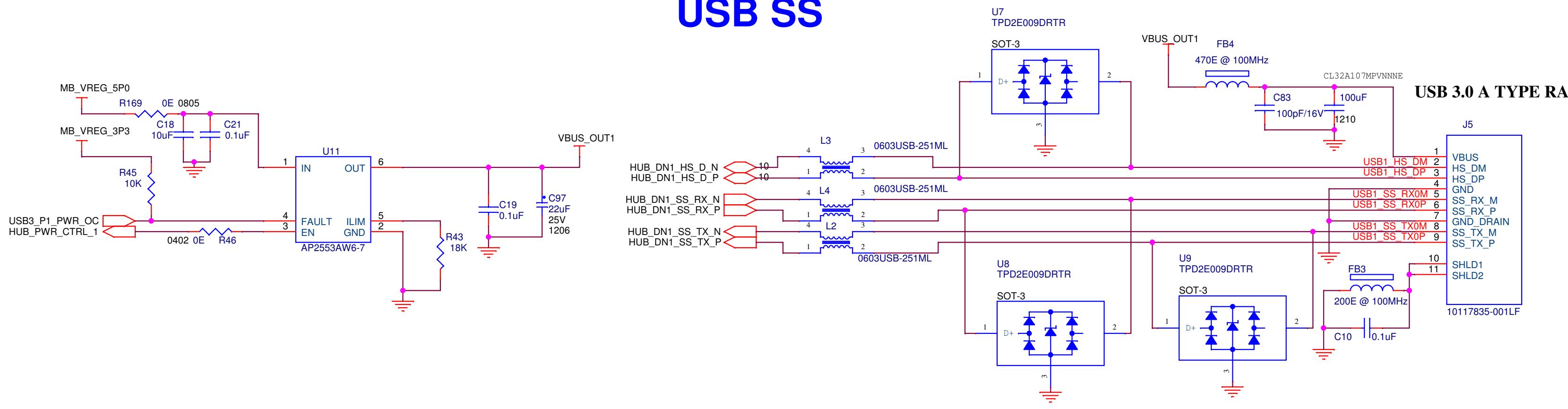
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82754I LAN CONTROLLER

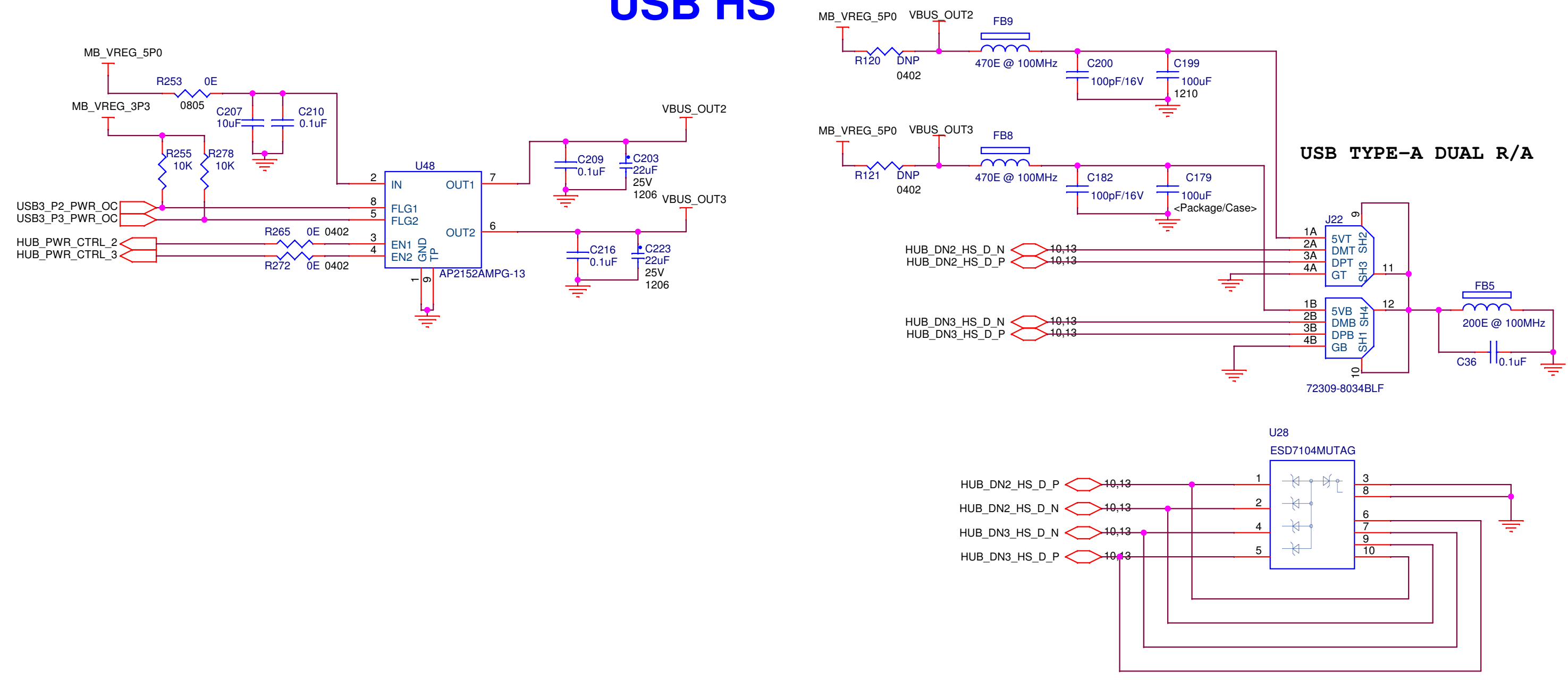


TITLE PCIE			
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USB SS



USB HS



TITLE
USB SS and HS

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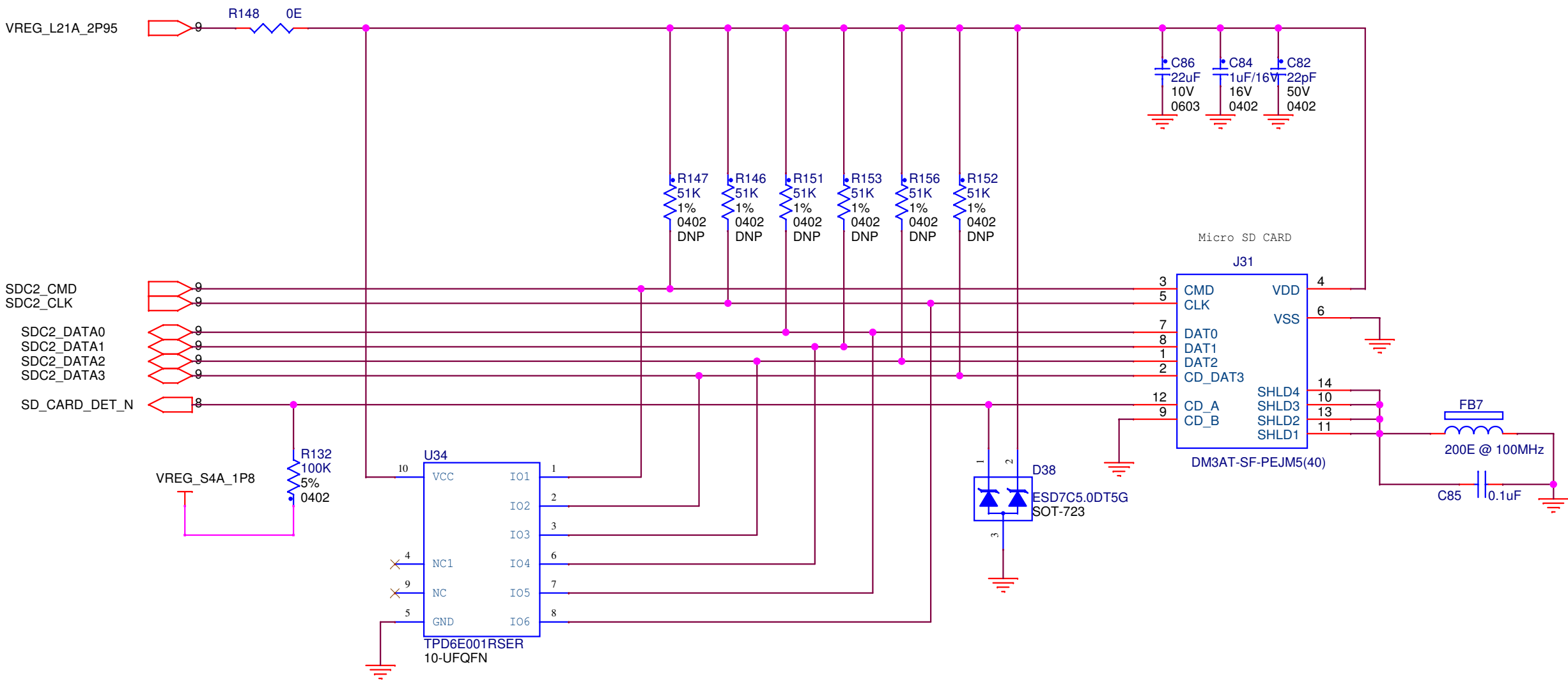
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Micro SD CARD



TITLE
Micro Sd Card

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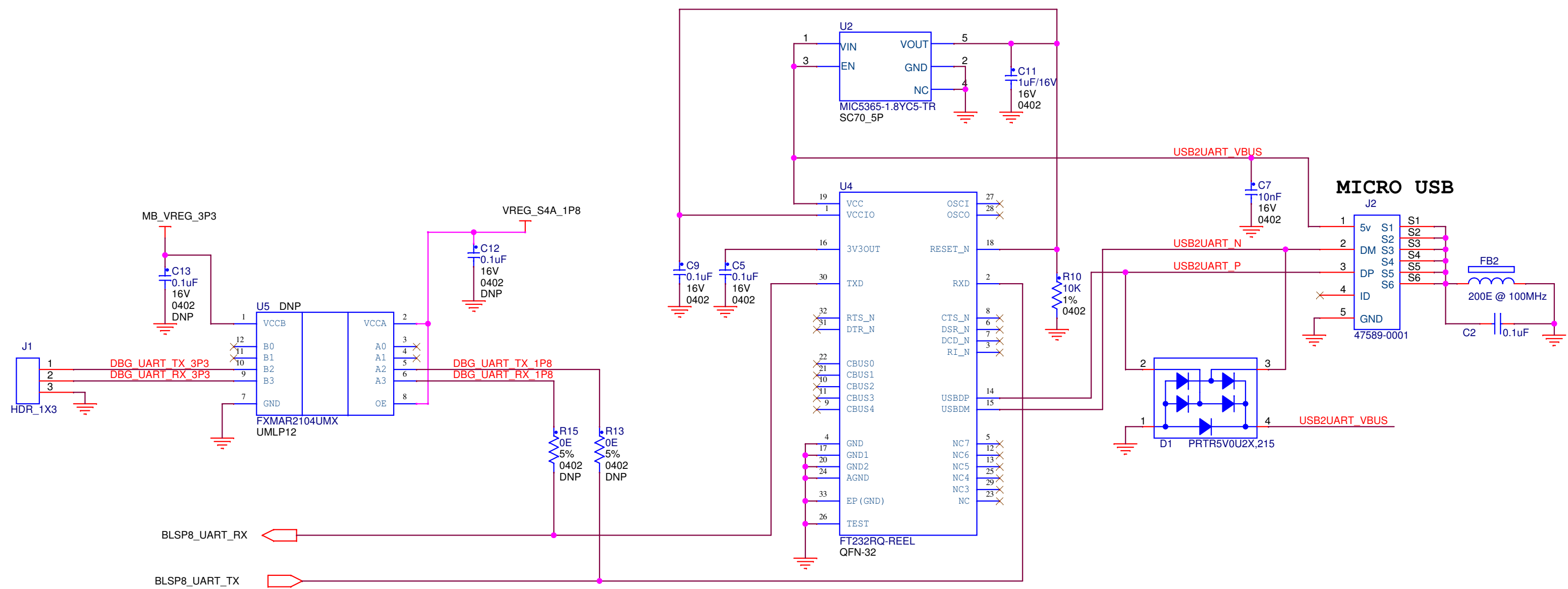
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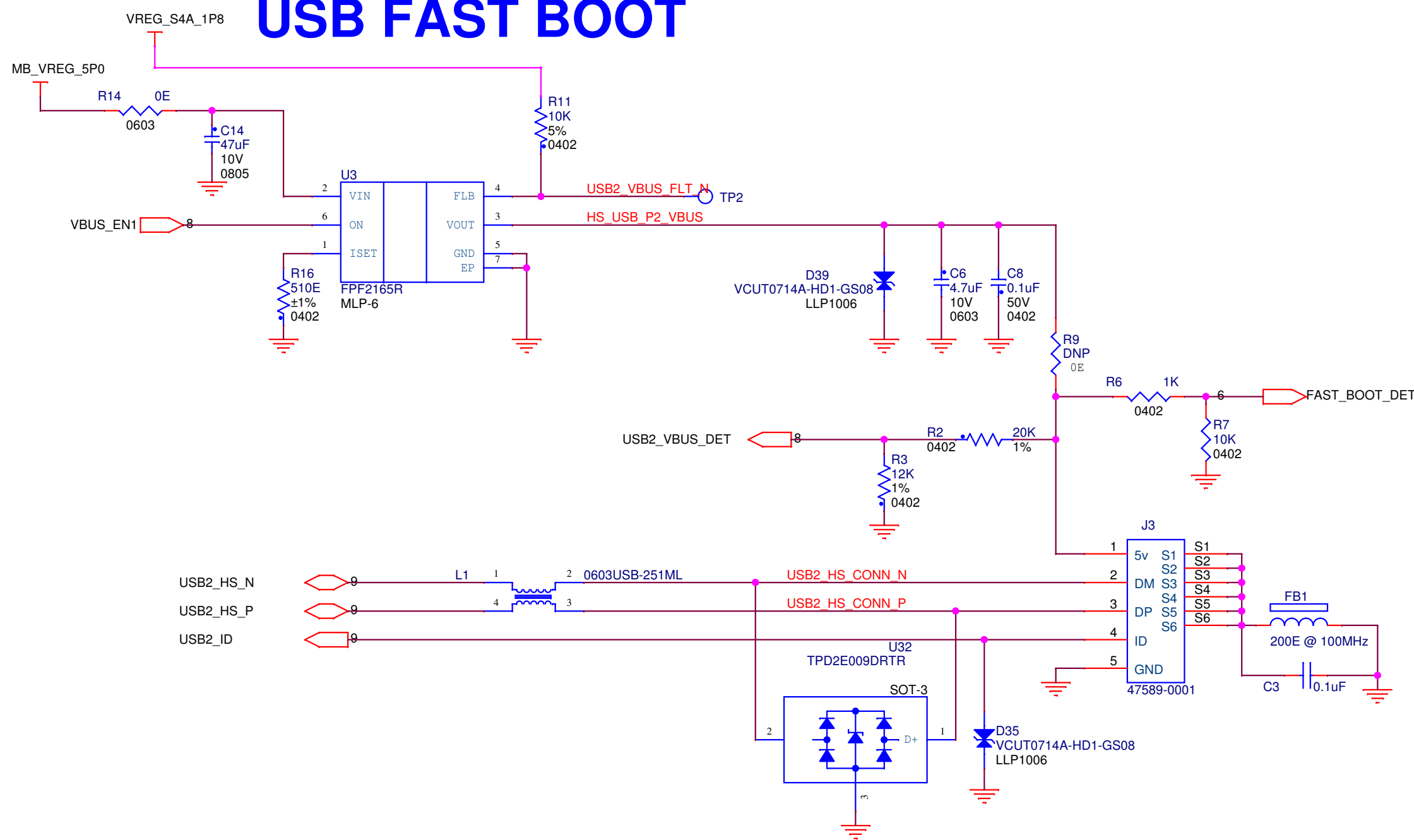
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DEBUG SERIAL



USB FAST BOOT



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Debug Serial and Fast Boot

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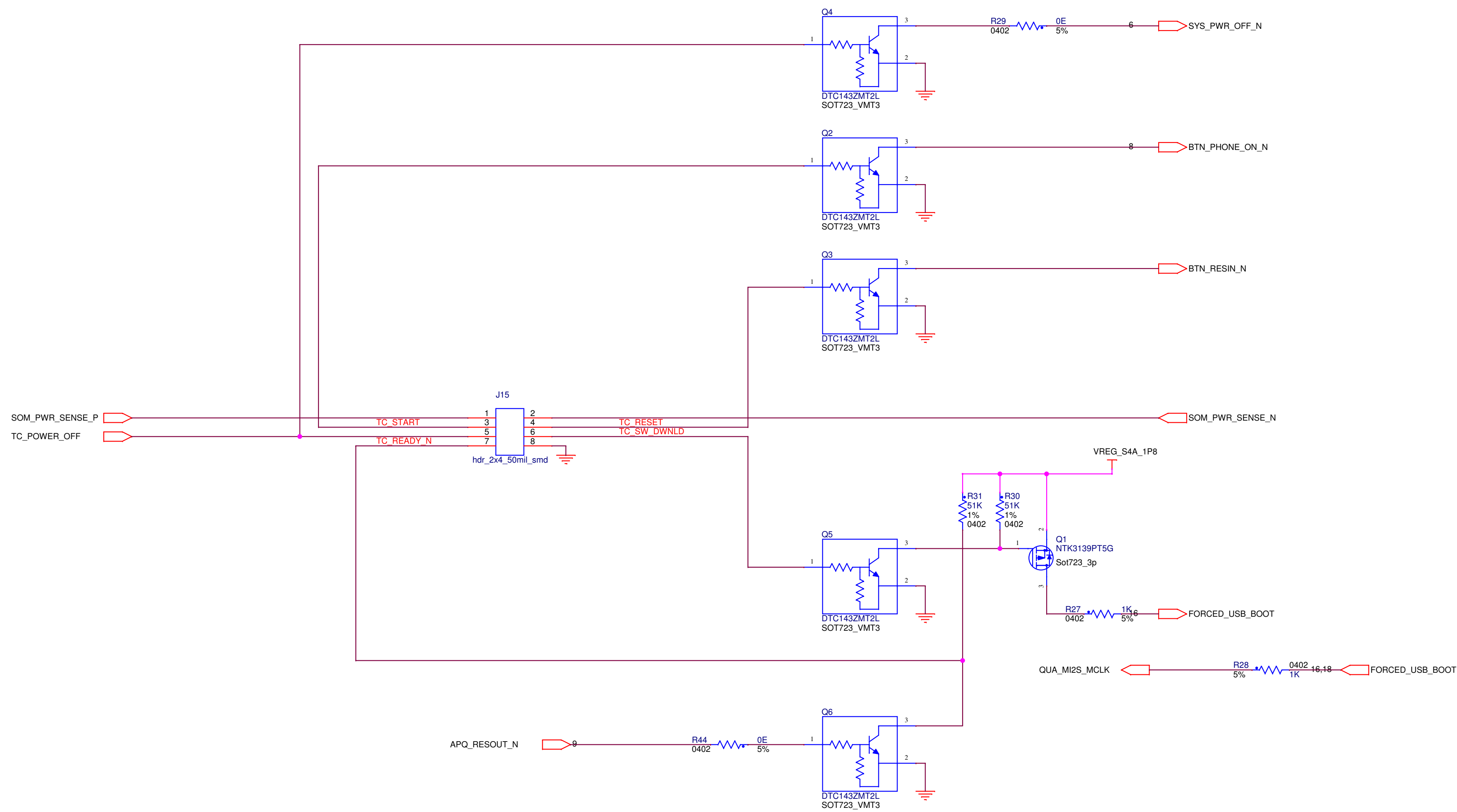
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SCALE NONE

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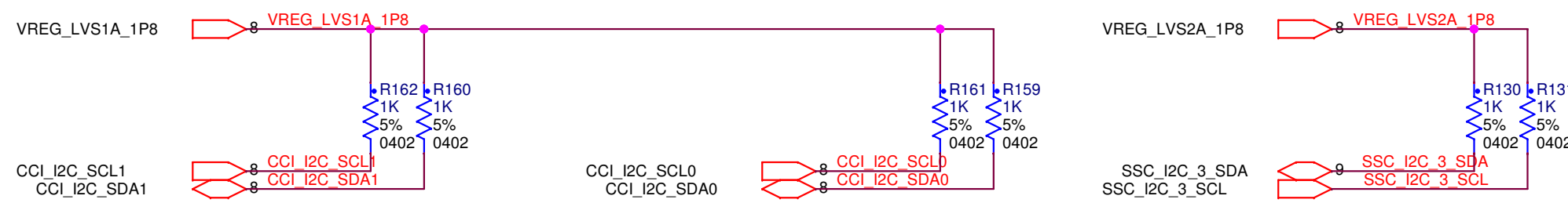
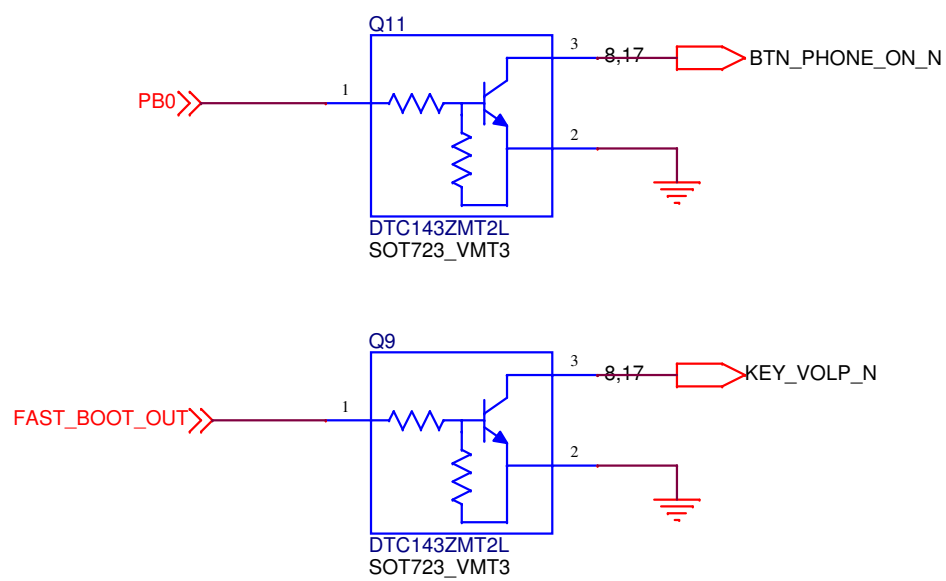
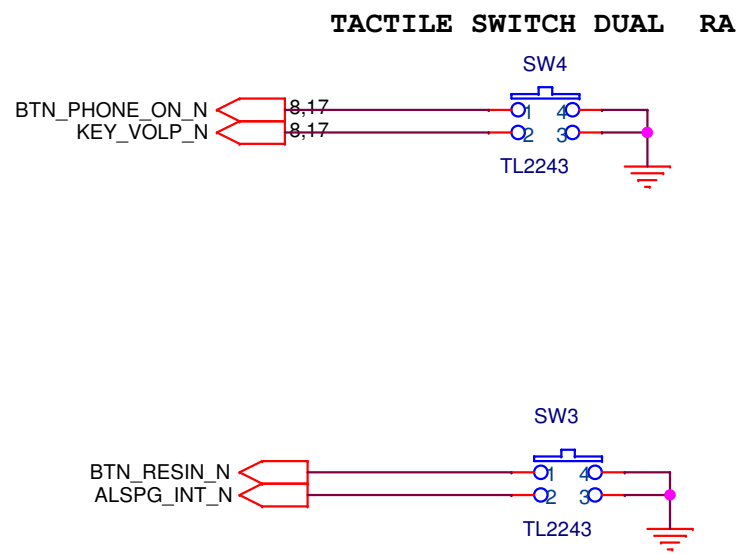
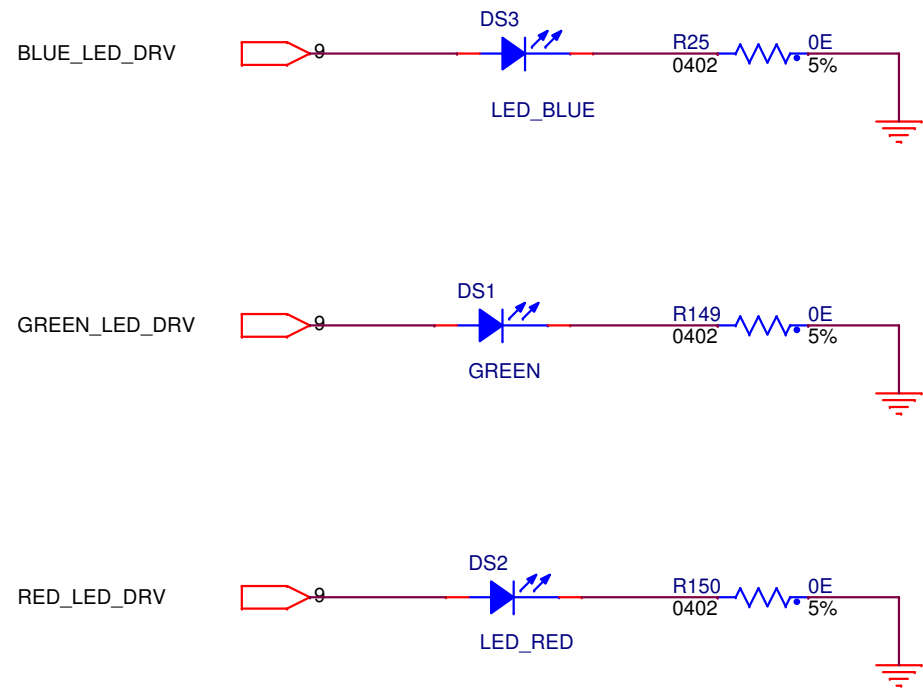
AUTOMATION CONN



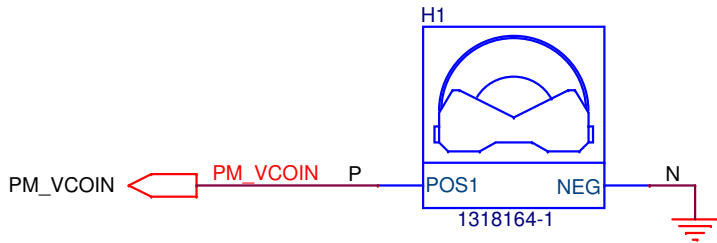
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Automation Connector			
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LEDS AND SWITCHES



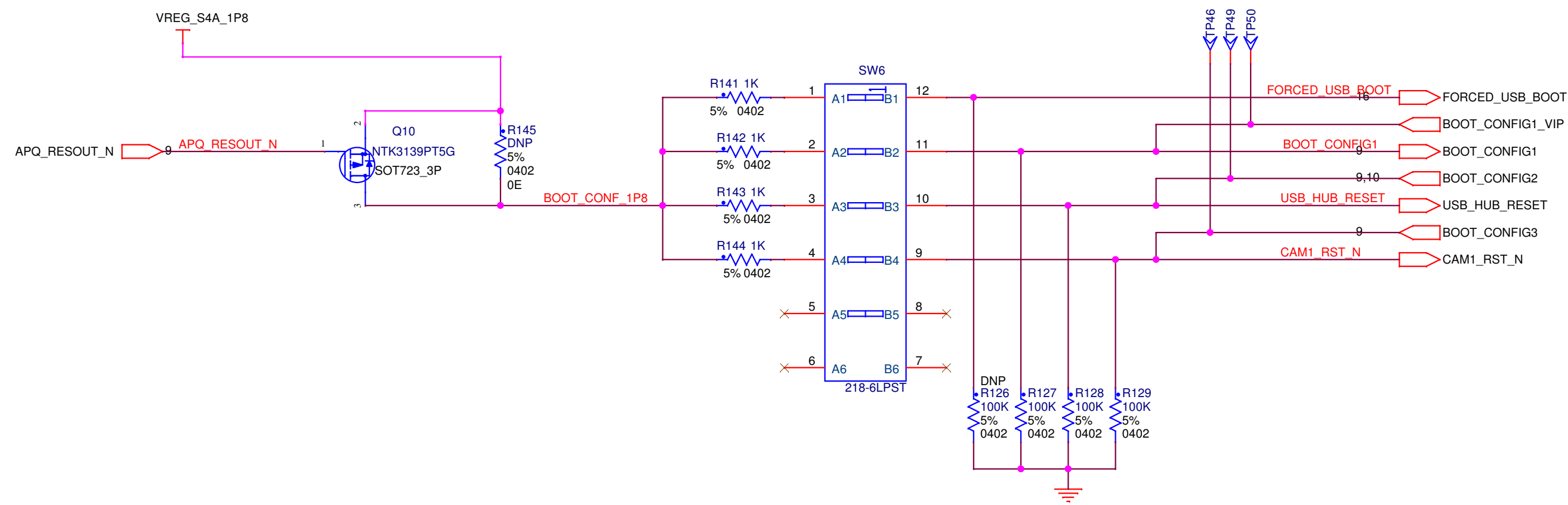
COIN CELL



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LEDs and Switches			
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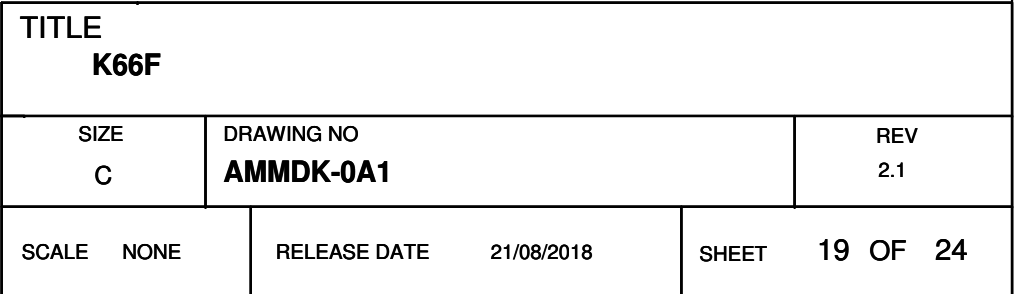
SOM BOOT CONFIG



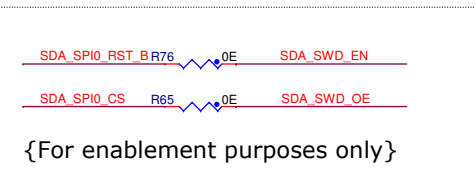
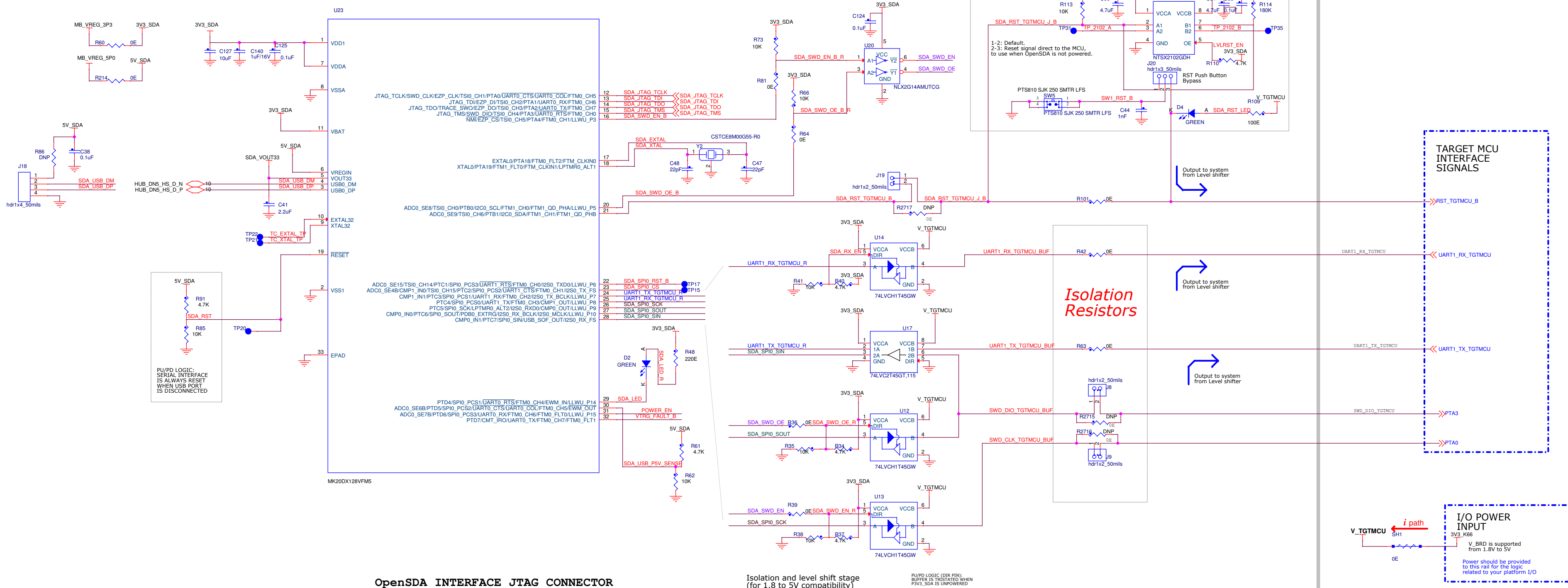
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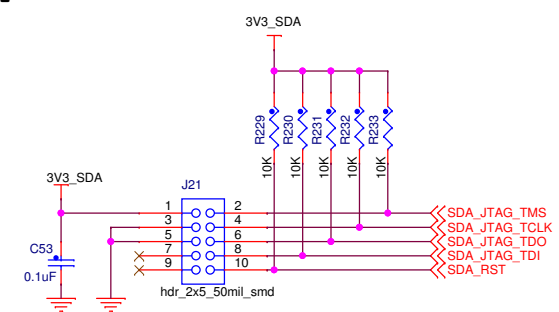
K66F



OpenSDA Interface



OpenSDA INTERFACE JTAG CONNECTOR

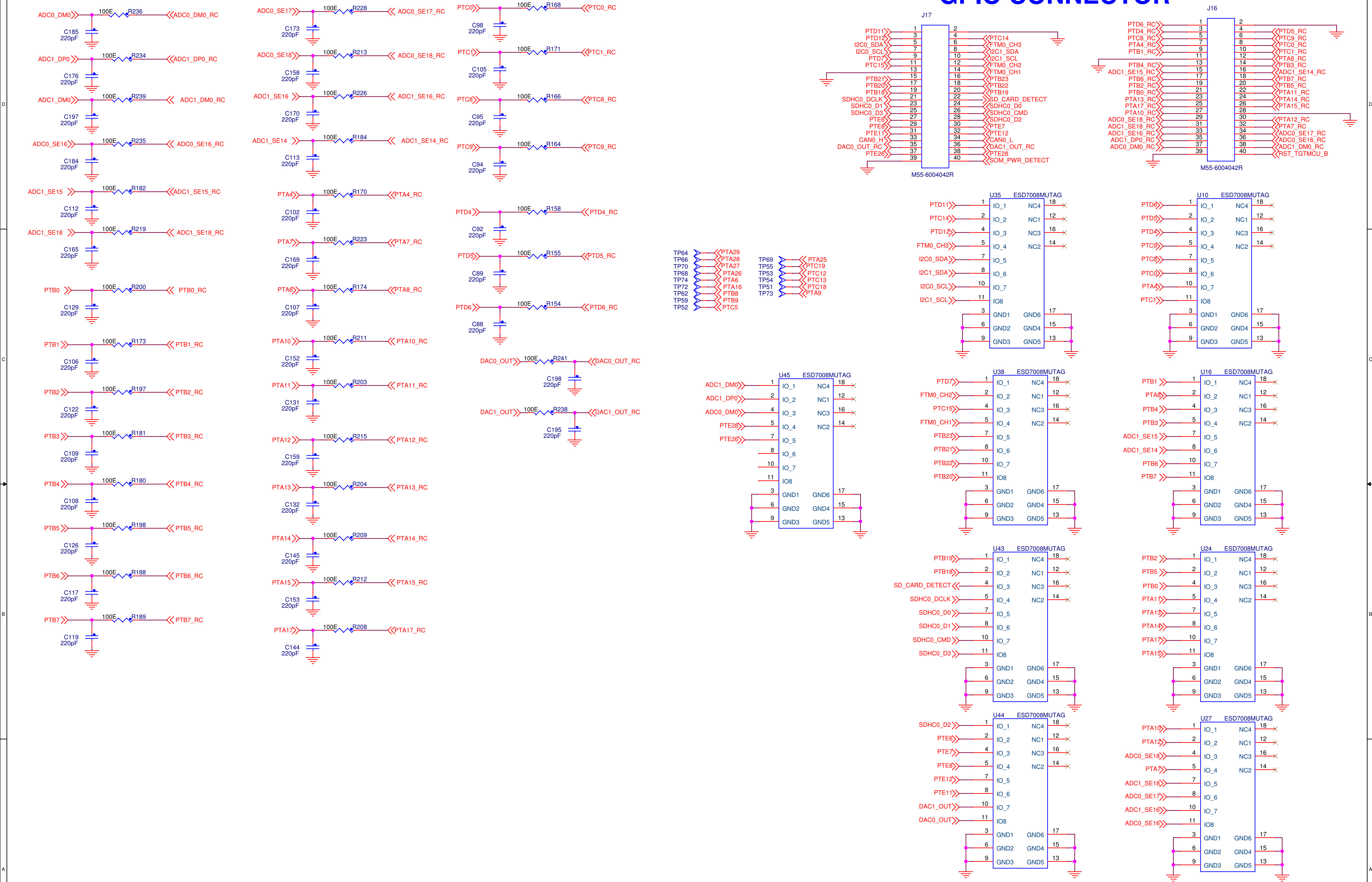


Isolation and level shift stage (for 1.8 to 5V compatibility)

PULLUP LOGIC (OR PIN): BUFFER IS TRI-STATED WHEN PVS1_SDA IS UNPOWERED

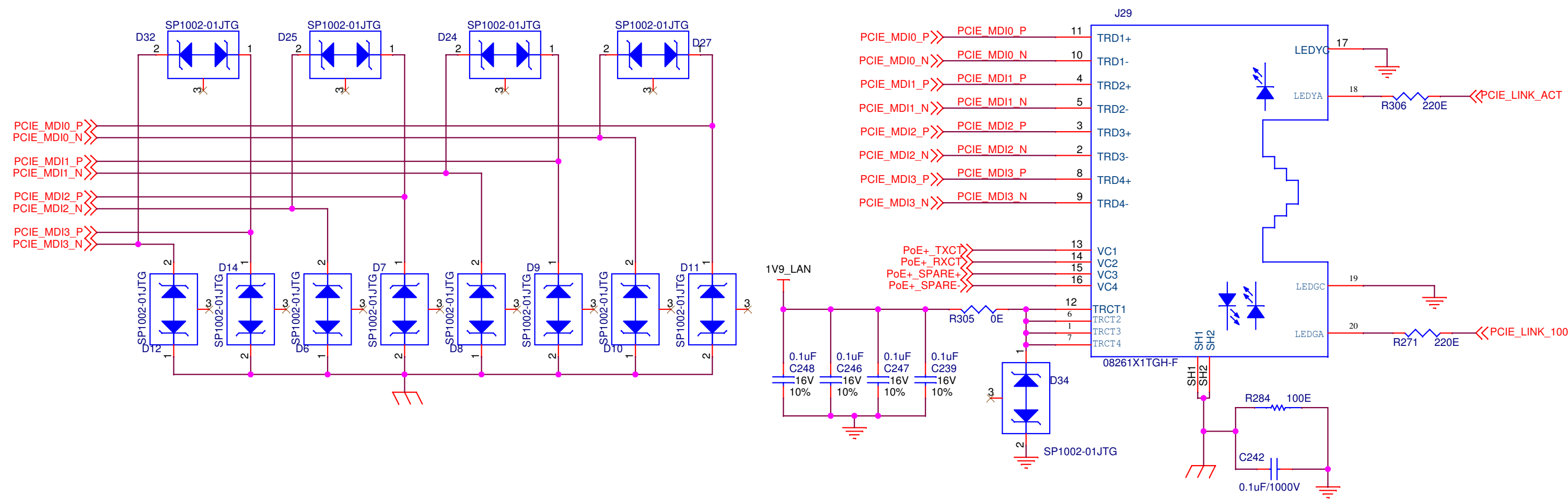
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Open SDA Interface			
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GPIO CONNECTOR

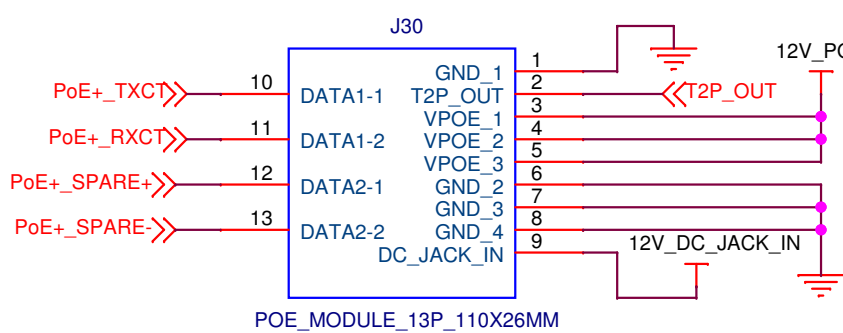


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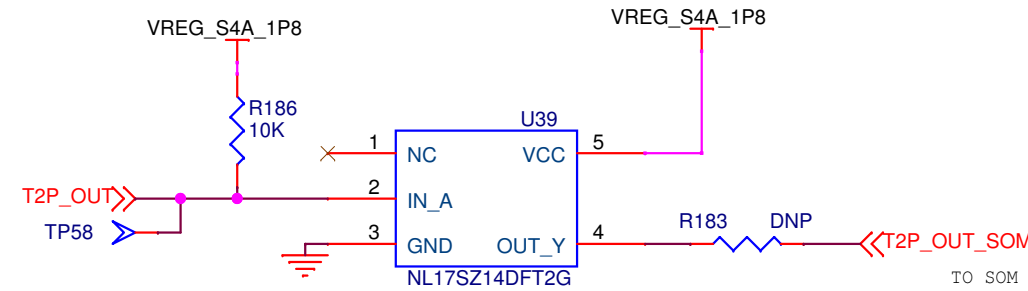
PHY INTERFACE



POE MODULE



LOGIC INVERTER



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ETHERNET			
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REVISION HISTORY

[illegible]

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