

Quartus Prime Lite Edition - C:/AlteraPr/pipemultQP16_1/Schematic/pipemult - pipemult2

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Entity/Instance: MAX 10: 10M08DAF484C8GES | Logic Cells: 592 (3)

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Flow Summary:

Flow Status: Successful - Sat Jun 03 23:15:11 2017

Quartus Prime Version: 16.1.0 Build 196 10/24/2016 SJ Lite Edition

Revision Name: pipemult2

Top-level Entity Name: pipemult

Family: MAX 10

Device: 10M08DAF484C8GES

Timing Models: Preliminary

Total logic elements: 592 / 8,064 (7 %)

Total registers: 281

Total pins: 167 / 250 (67 %)

Total virtual pins: 0

Total memory bits: 512 / 387,072 (< 1 %)

Embedded Multiplier 9-bit elements: 1 / 48 (2 %)

Total PLLs: 0 / 2 (0 %)

UFM blocks: 0 / 1 (0 %)

ADC blocks: 0 / 1 (0 %)

IP Catalog:

- Installed IP
- Project Directory
- Library
- Basic Functions
- DSP
- Interface Protocols
- Memory Interfaces and Controllers
- Processors and Peripherals
- University Program
- Search for Partner IP

Tasks: Compilation

Task: Compile Design OK

Task: Analysis & Synthesis OK

Task: Fitter (Place & Route) OK

Task: Assembler (Generate programming files) OK

Task: TimeQuest Timing Analysis OK

Task: EDA Netlist Writer OK

Quartus Prime Tcl Console

Quartus Prime EDA Netlist writer was successful. 0 errors, 2 warnings

293000 Quartus Prime Full Compilation was successful. 0 errors, 30 warnings

System (2) Processing (311)

-54,275 100% 00:01:16