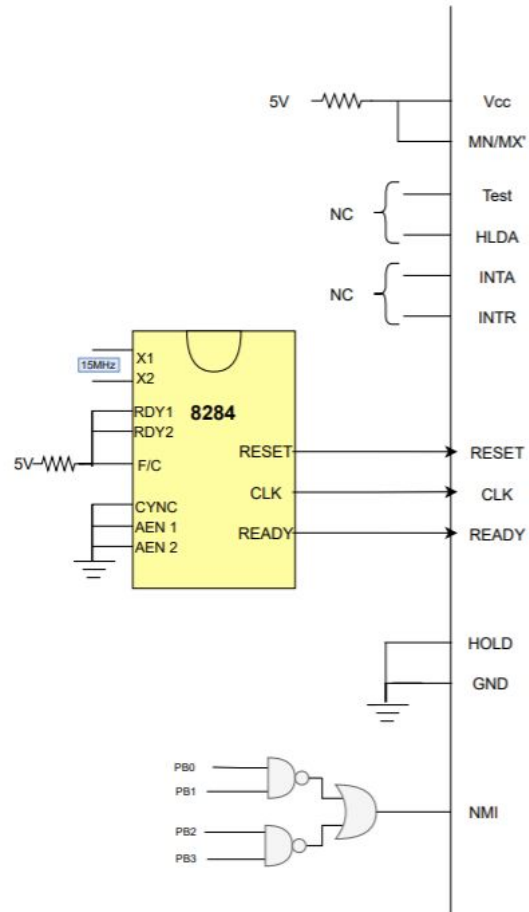
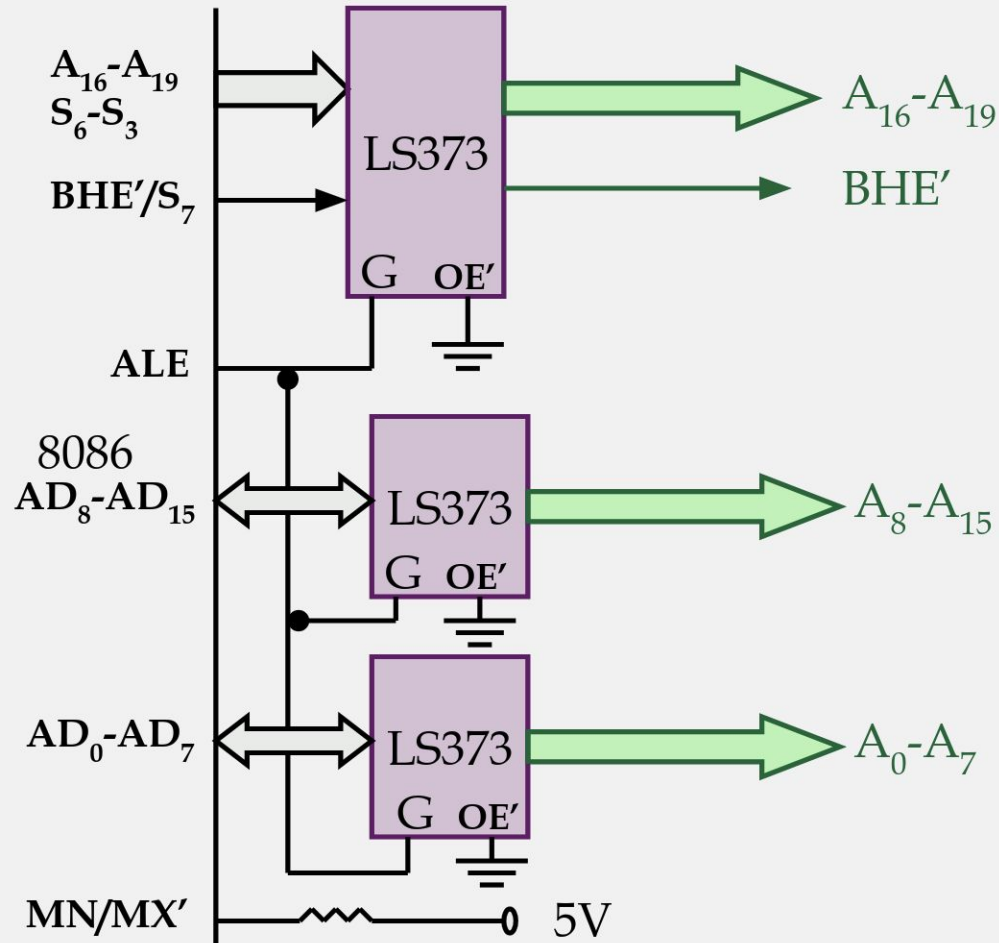


Elevator Control System

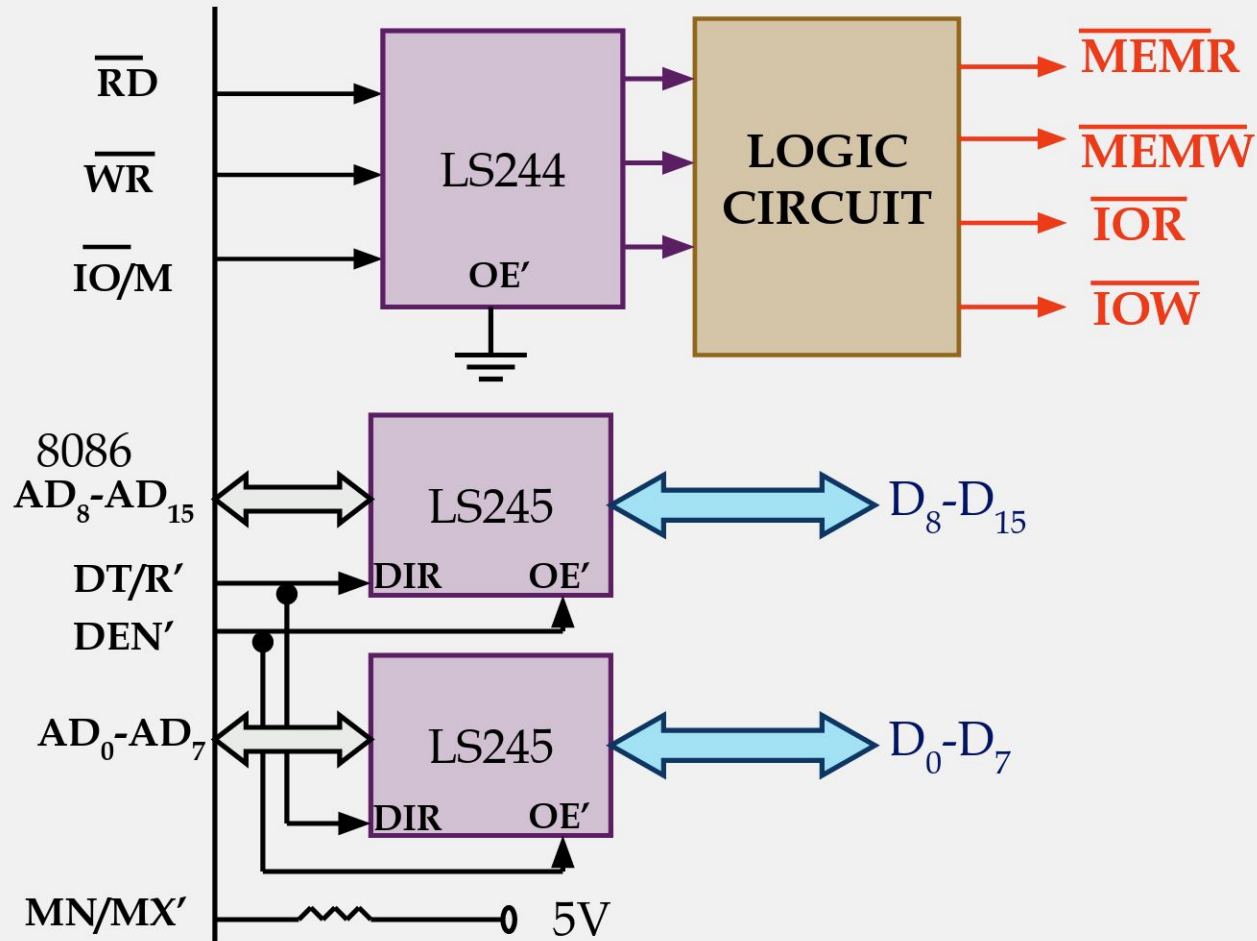


8086 Inputs

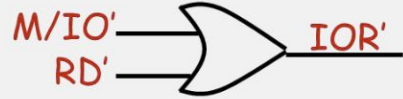
System Bus of 8086 (Address)



System Bus of 8086(Data + Control)



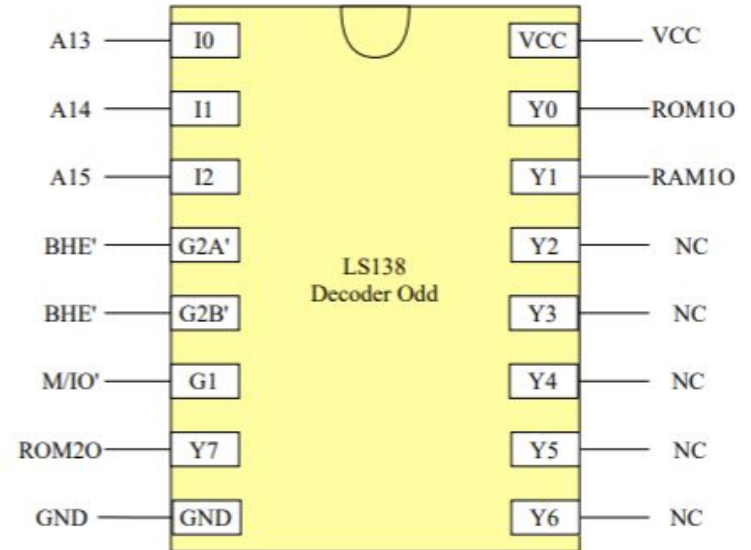
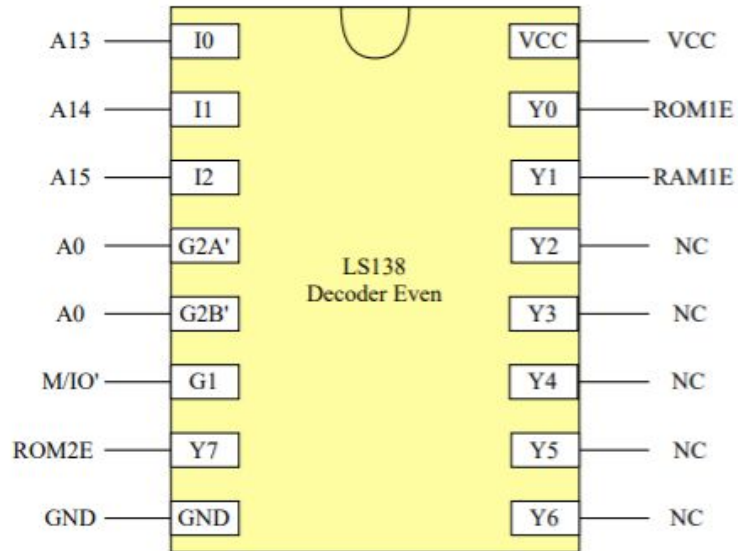
Logic Circuit



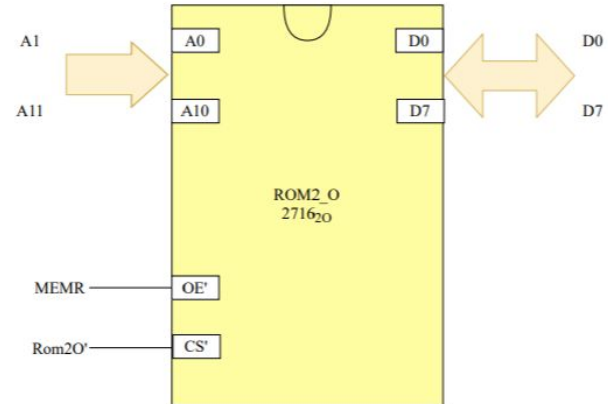
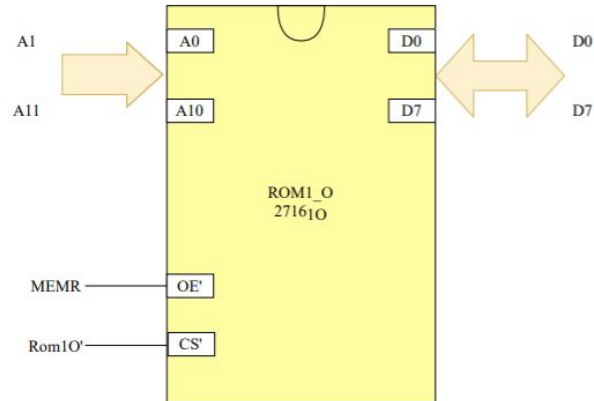
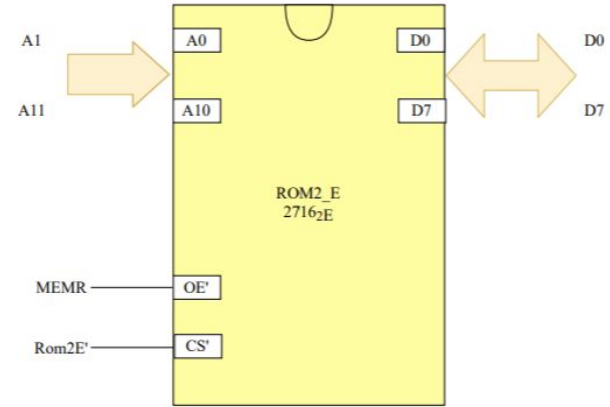
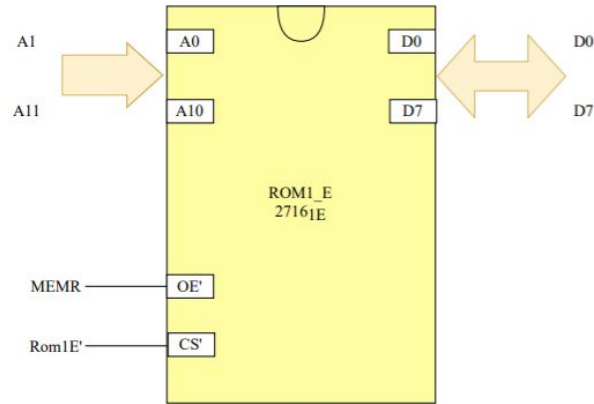
M/IO'	RD'	WR'	Bus cycle
1	0	1	$MEMR'$
1	1	0	$MEMW'$
0	0	1	IOR'
0	1	0	IOW'



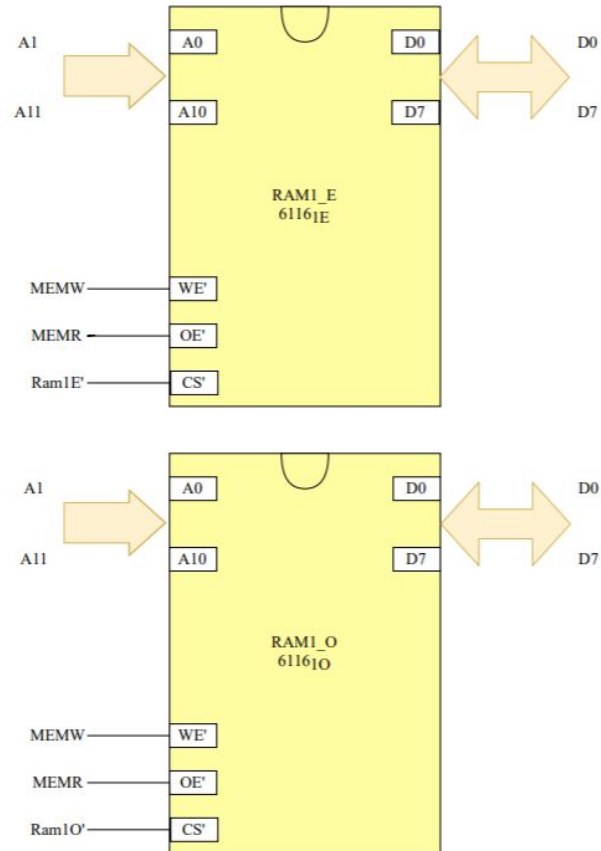
Memory Interfacing



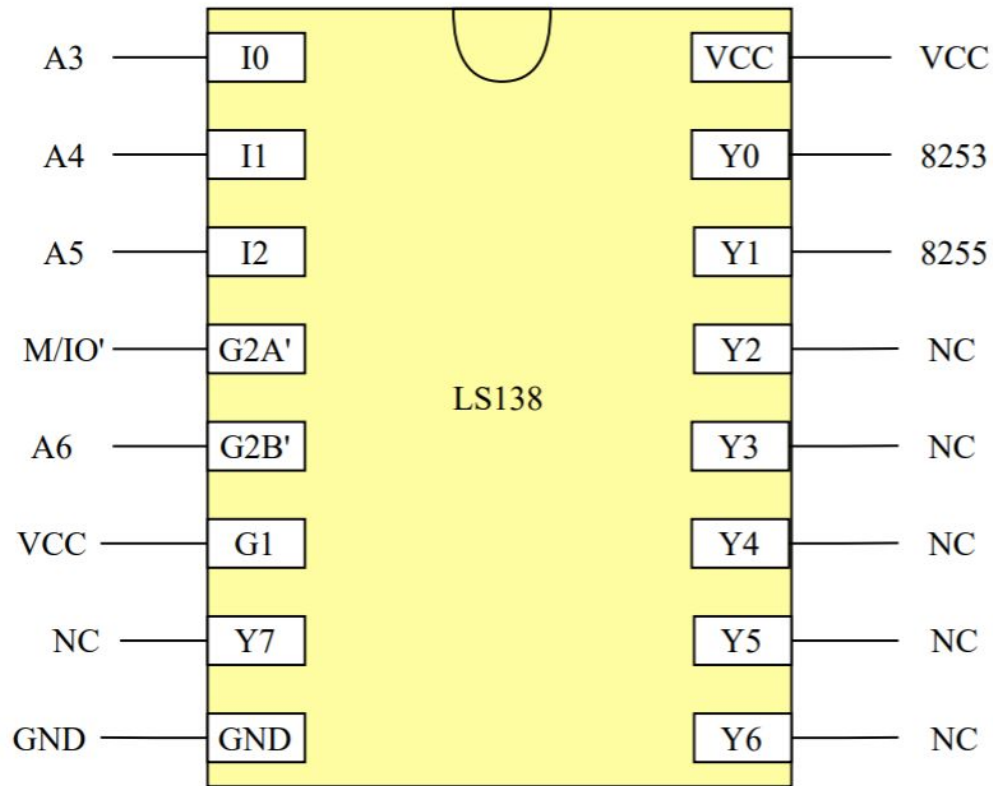
Memory Interfacing



Memory Interfacing

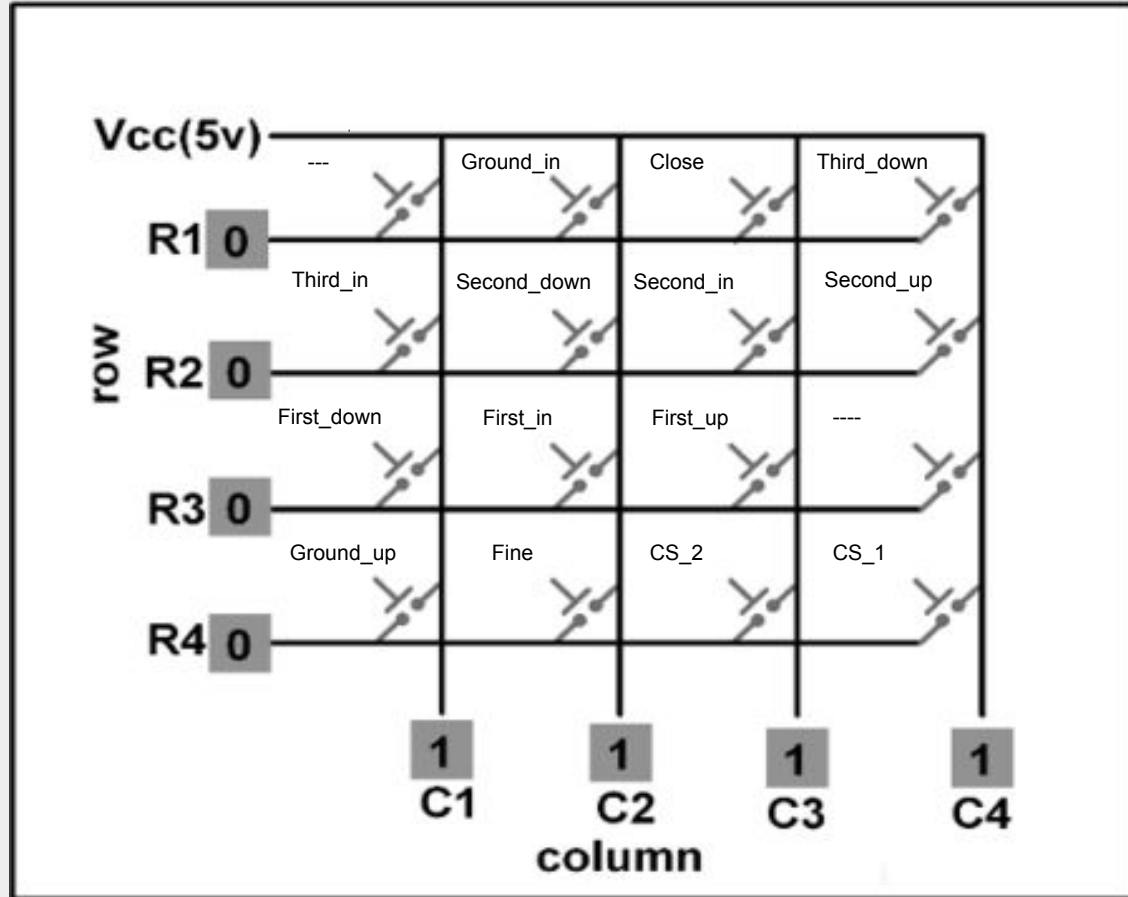


Demultiplexer



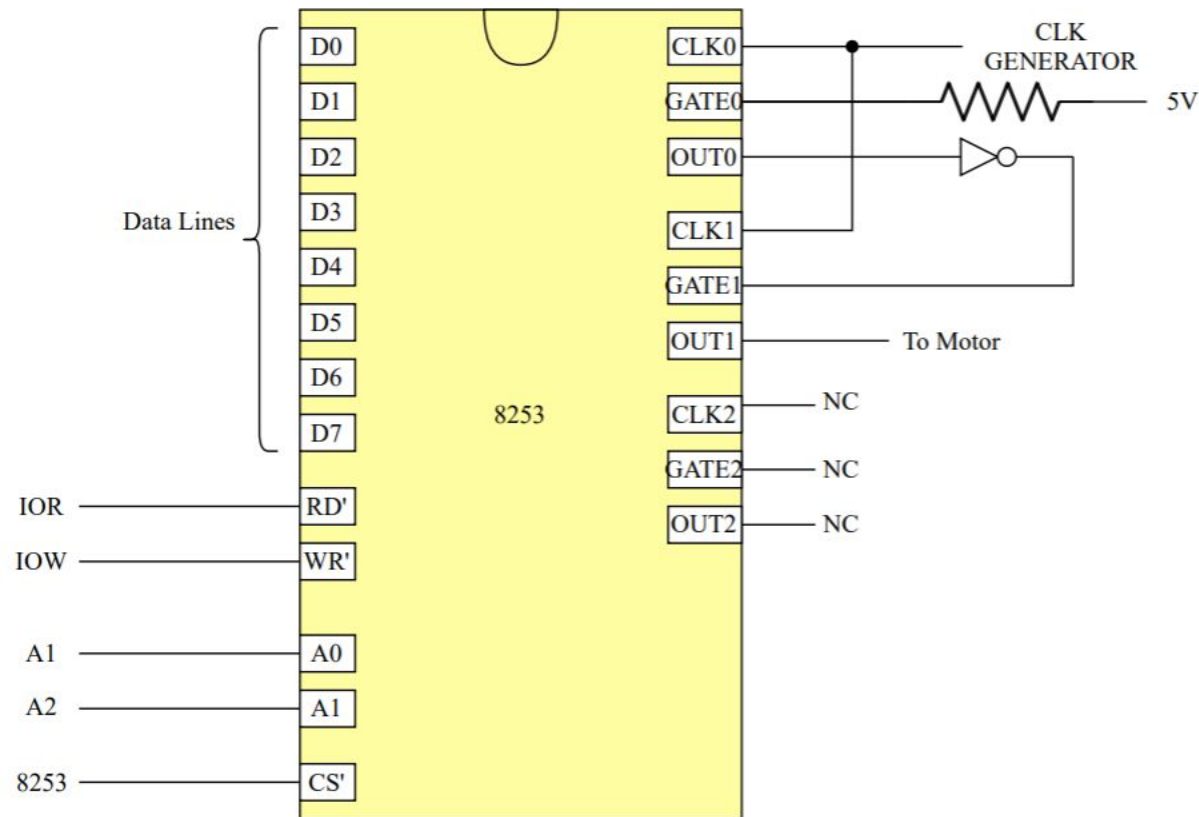
Keyboard Interfacing

4X4 Matrix Keypad

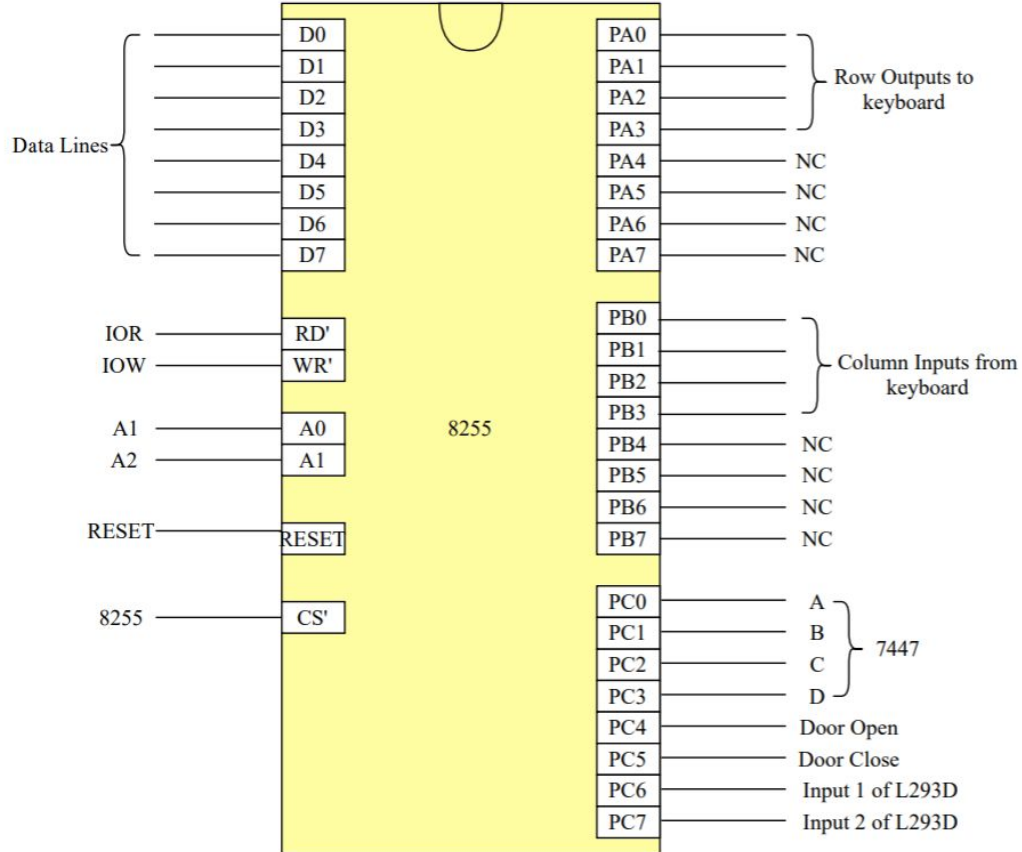


Keyboard Interfacing									
R3	R2	R1	R0	C3	C2	C1	C0	Hex	Mapping
0	1	1	1	0	1	1	1	77	CS_1
0	1	1	1	1	0	1	1	7B	Fine
0	1	1	1	1	1	0	1	7D	CS_2
0	1	1	1	1	1	1	0	7E	Ground_up
1	0	1	1	1	0	1	1	BB	First_up
1	0	1	1	1	1	0	1	BD	First_in
1	0	1	1	1	1	1	0	BE	First_down
1	1	0	1	0	1	1	1	D7	Second_up
1	1	0	1	1	0	1	1	DB	Second_in
1	1	0	1	1	1	0	1	DD	Second_down
1	1	0	1	1	1	1	0	DE	Third_in
1	1	1	0	0	1	1	1	E7	Third_down
1	1	1	0	1	0	1	1	EB	Close
1	1	1	0	1	1	0	1	ED	Ground_in

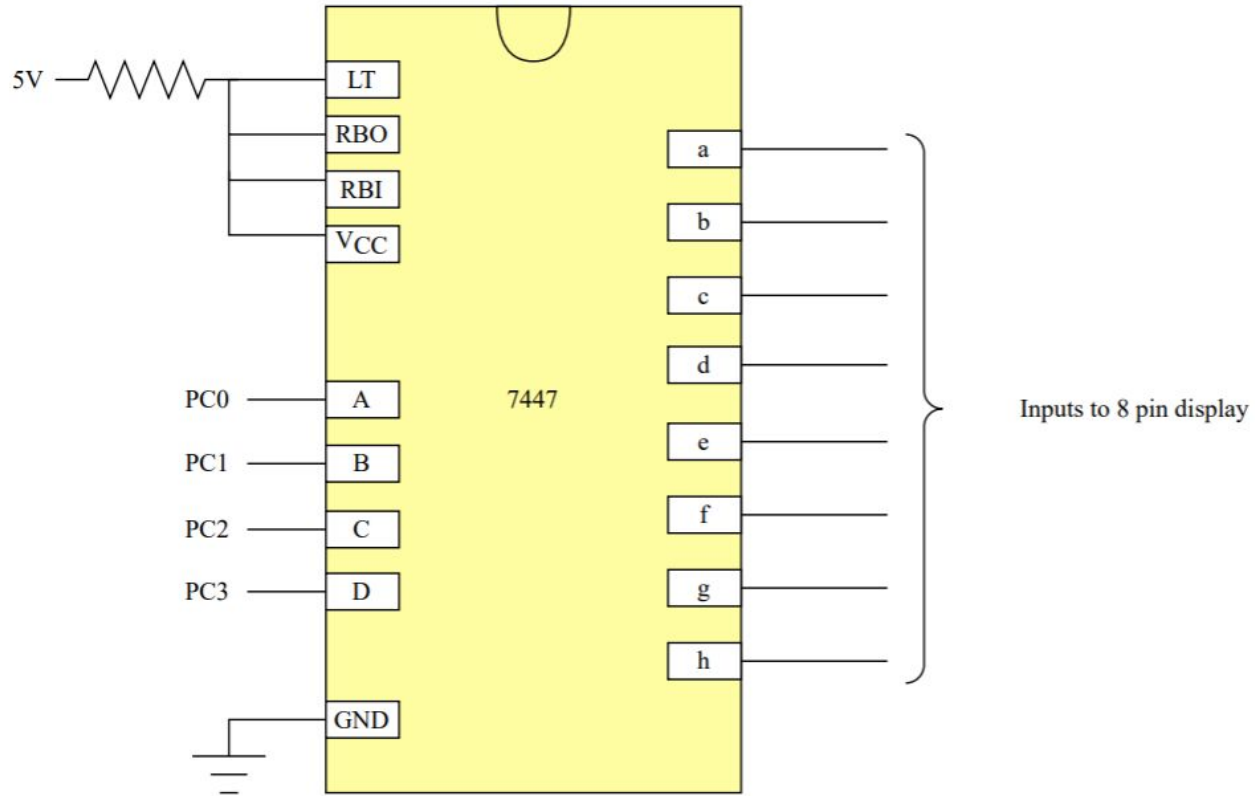
8253 Interfacing



8255 Interfacing



7 Segment Display



Motor Interfacing

