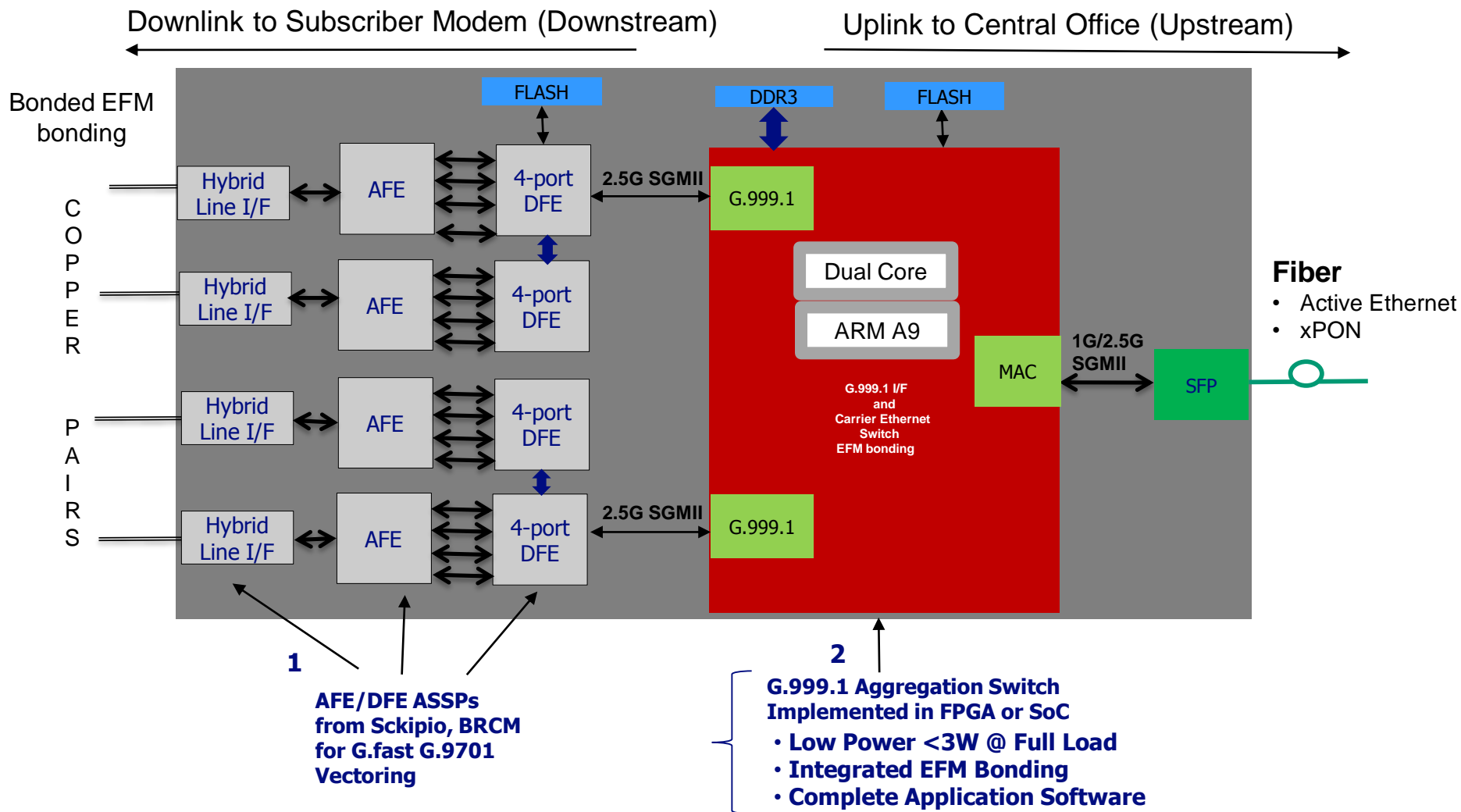


G.fast/xDSL Solution

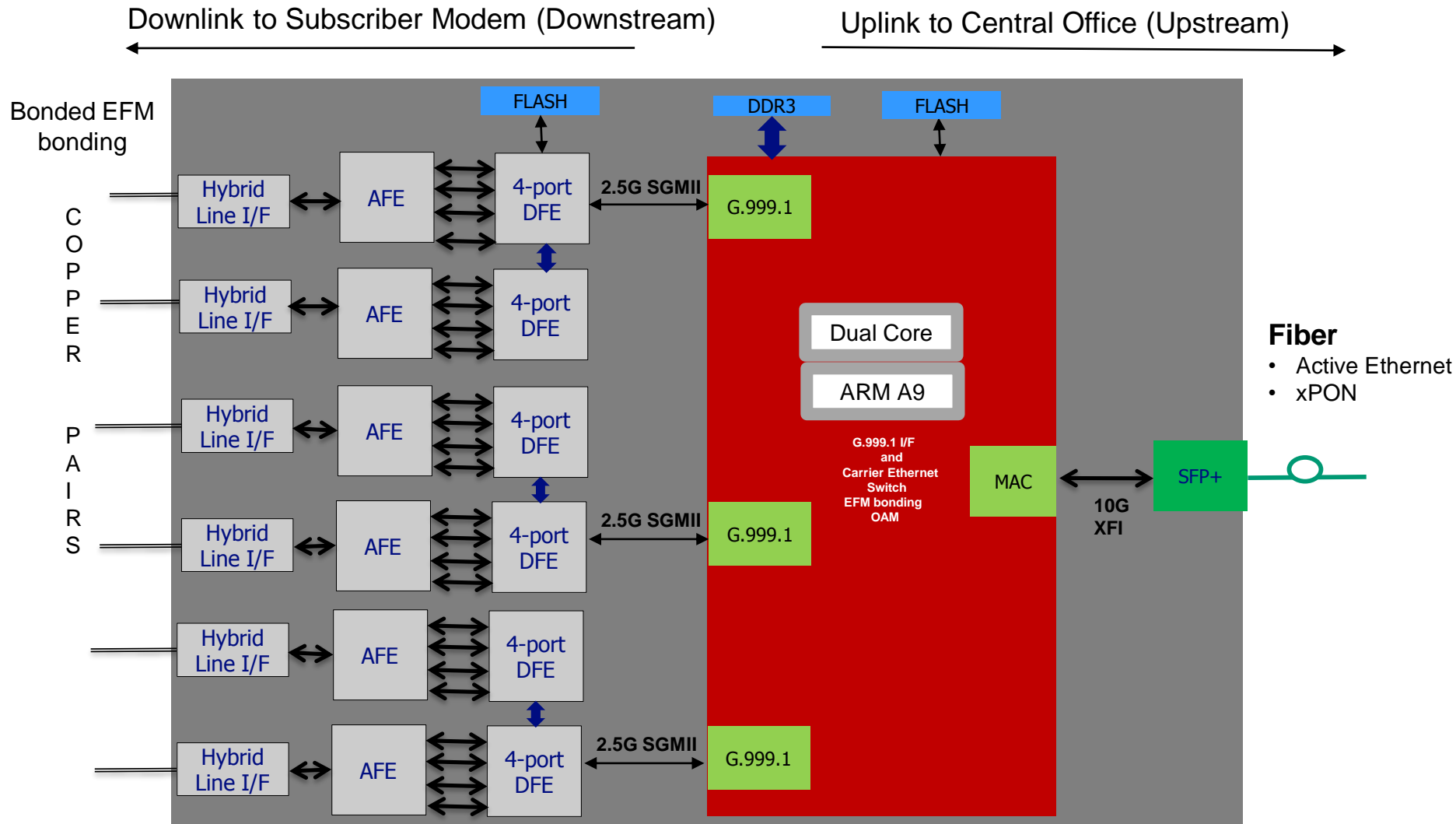
ENET38xxZ/99 Products Family

Ethernity Off-the-Shelf SoC Offering

Single Chip G.fast DPU Architecture (16 Ports)



Single Chip G.fast DPU Architecture (24 Ports)



Single Chip G.fast DPU Architecture (48 Ports)

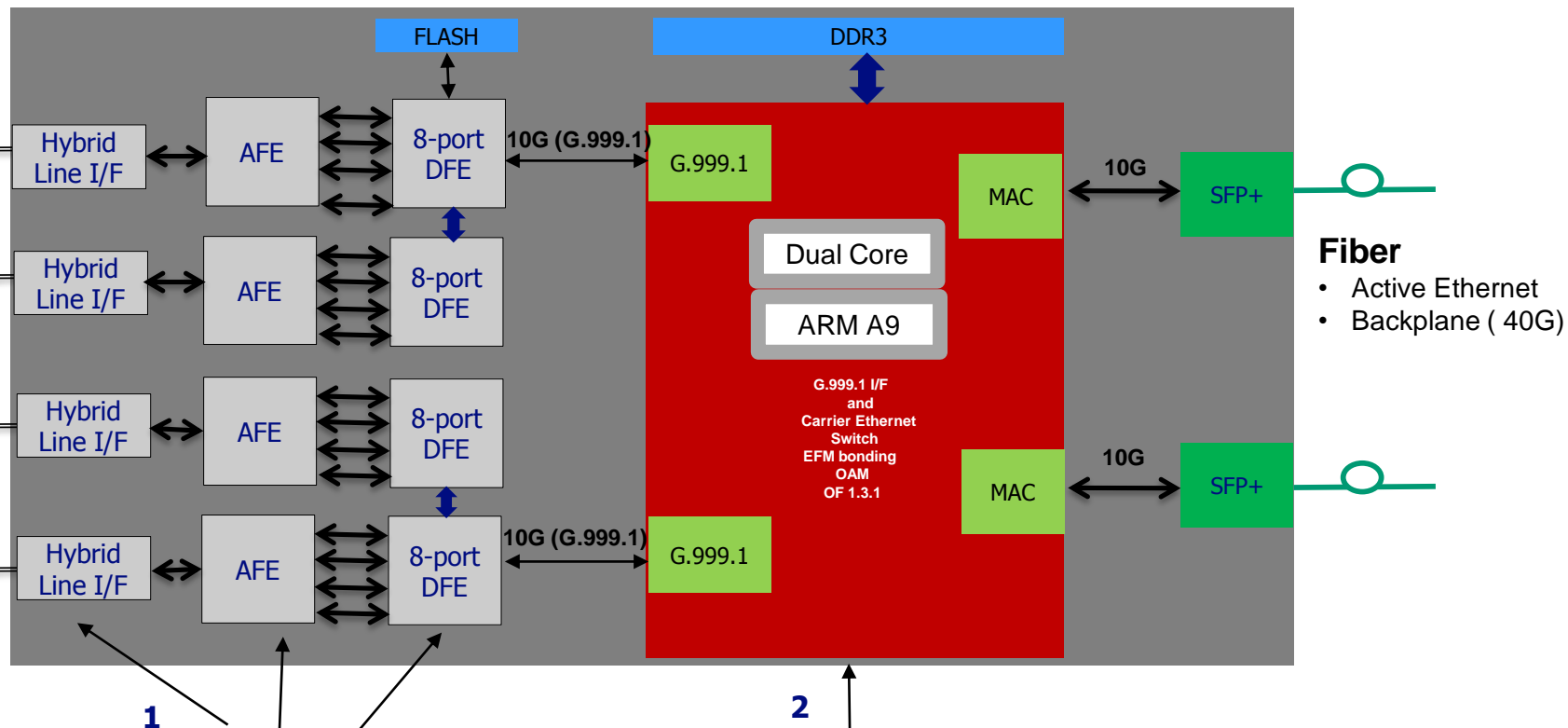
Downlink to Subscriber Modem (Downstream)

Uplink to Central Office (Upstream)

Bonded EFM
bonding

C
O
P
P
E
R

P
A
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S



1

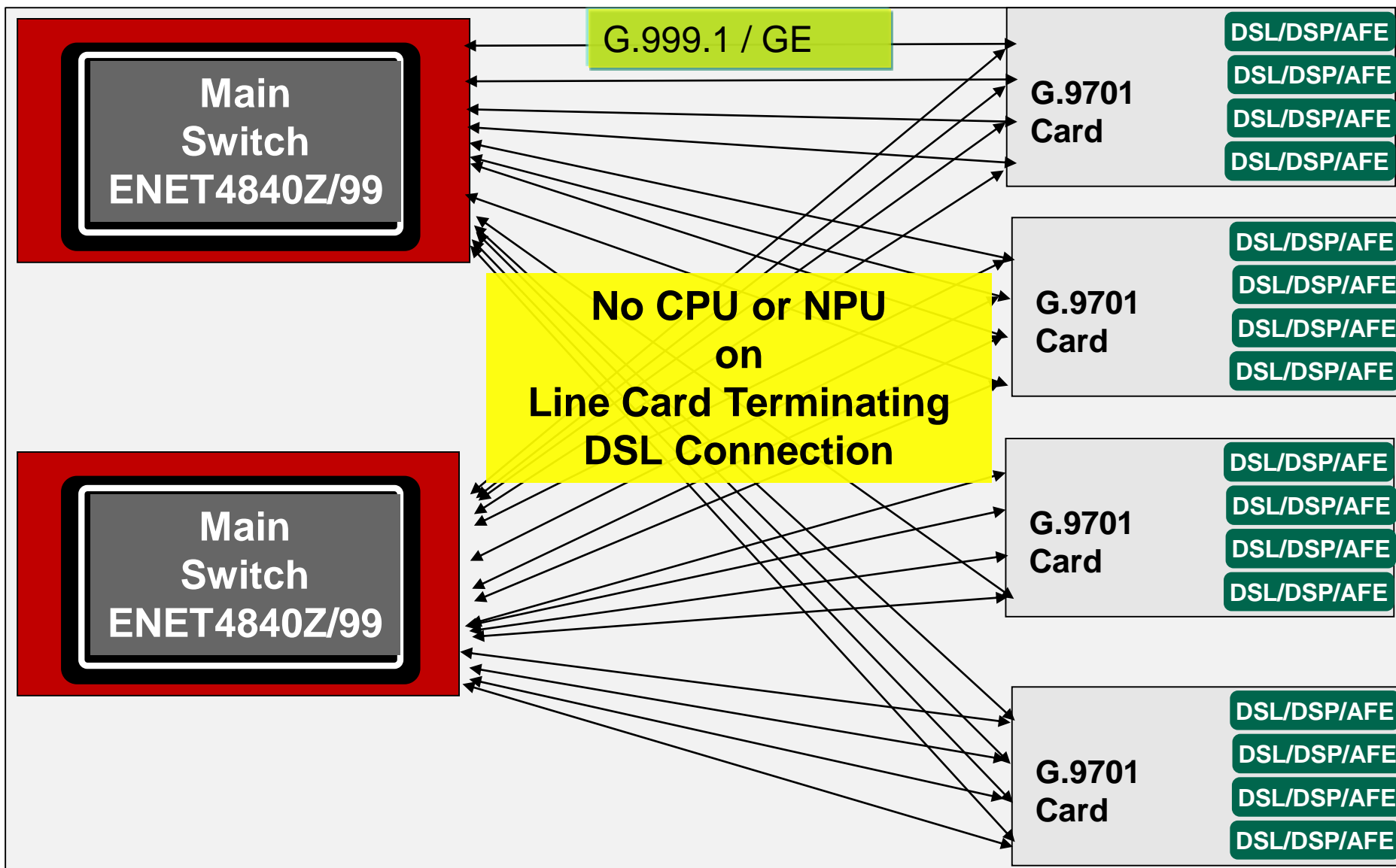
**AFE/DFE ASSPs
from Sckipio, BRCM
for G.fast G.9701
Vectoring**

2

**G.999.1 Aggregation Switch
Implemented in FPGA or SoC**

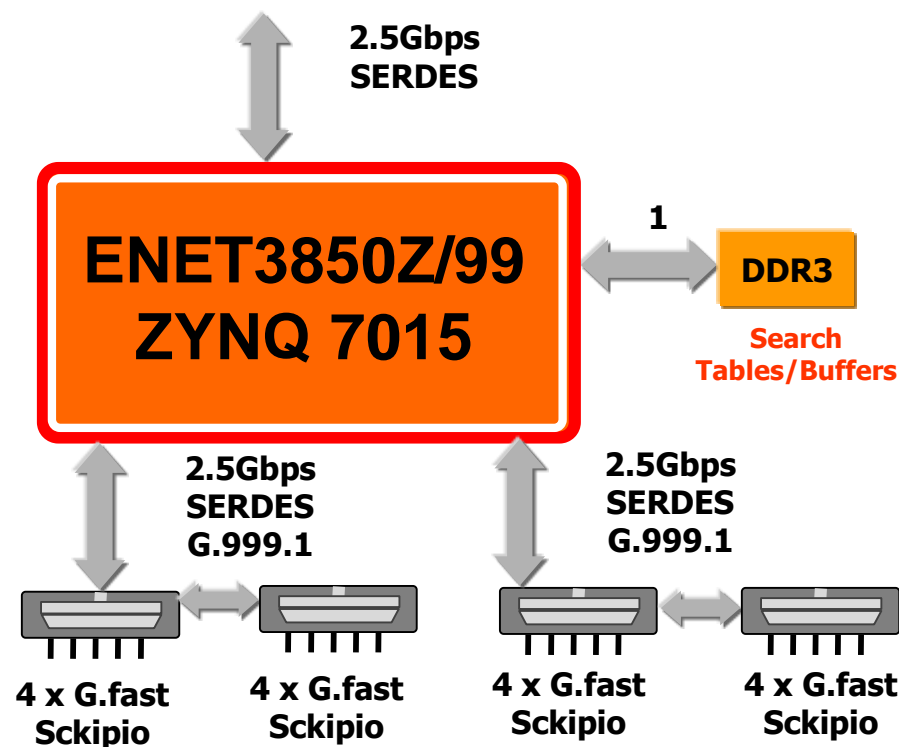
- Integrated EFM Bonding
- NAT
- OAM/CFM
- 1588
- OF 1.3.1
- Complete Application Software

G.999.1 Aggregation with Centralized Switching



G.fast 16 Port DPU

- MEF compliant switching
- Hierarchical scheduling, shaping
- G.999.1 channelized Ethernet interface
 - 16 virtual ports per I/F
- Interfaces
 - 10/2.5/1 GE I/F
 - 2 - 6 x 2.5G – G.999.1
 - DDR3
- Supports
 - TR-101
 - Ethernet bonding, EFM bonding
 - MEF



**The Best Low-Power Solution on
ENET38xxZ/99 <3W for CPU and
Switch Subsystem**

Solution Options

G.fast DPU	G.999.1 Aggregation	Packet Processing Traffic Management
Functionality	Up to 256 Ports	Up to 40G
Port Data Rate	1.25G	Flexible Uplinks
Supported Devices	7 Series, Zynq	7 Series, Zynq
Evaluation Board	Z7045 / 7015	Z 7045
Availability	Yes	Yes

- DPU
 - Multi-Port G.FAST DPU
 - Mix VDSL2/G.FAST DPU
 - 48 G.fast with 4 x 10G
- FTTdp
 - NGPON2
 - XGPON
 - Active Ethernet over Direct Fiber
 - EFM
- Single Chip Advantages
 - G.999.1 Interface
 - Scalable Packet Processing, Switching and Traffic Management
 - Dual ARM A9s for CPU/Host Processing
 - Flexible and Scalable Configuration