

$$\sim (B * \sim C)$$

$$(\sim C * D) \vee (F * \sim C)$$

$$\begin{aligned} (B \vee C) &\Rightarrow B \rightarrow C \\ (\sim C * D) \vee (F * \sim C) \\ (\sim C * D) \vee (\sim C * F) \end{aligned}$$

$$\sim C * (D \vee F)$$

$$\rightarrow \sim C$$

$$\Rightarrow \sim B \quad (MT)$$

$$\sim (N * P)$$

$$\sim N \vee \sim P$$

$$\begin{aligned} \sim M &\rightarrow N \\ \sim M &\rightarrow P \\ (\sim M \rightarrow N) * (\sim M \rightarrow P) \\ \sim N \vee \sim P \\ \Rightarrow \sim \sim M \\ \Rightarrow M \end{aligned}$$

$$A * B \rightarrow C$$

B

$$(A \rightarrow C) * B$$

$$C(x) (P(x) \rightarrow Q(x))$$

$$C(x) (\sim R(x) \rightarrow \sim Q(x)) \quad \vee (P(x) \rightarrow R(x))$$

$$P(x) \rightarrow Q(x)$$

$$\sim R(x) \rightarrow \sim Q(x)$$

$$Q(x) \vee \sim Q(x)$$

$$P(x) \vee R(x)$$

$$P(x) \rightarrow R(x)$$

$$\forall x P(x) \rightarrow R(x)$$

$$1) (M \vee N) \rightarrow (P \wedge Q)$$

$$2) N \quad / P$$

$$3) M \vee N \quad (\text{addition, 2})$$

$$4) P \wedge Q \quad (\text{Modus ponens } 3, 1)$$

$$5) P \quad (\text{Simp 4})$$

}  $\therefore$

$$1. (A \wedge B) \rightarrow (C \wedge D)$$

$$2. A$$

$$3. B$$

$$4. (A \wedge B) \quad \text{Conj } [2, 3]$$

$$5. (C \wedge D) \quad \text{MP } [1, 4]$$

$$1. (T \rightarrow K) * (R \rightarrow S)$$

$$2. S \rightarrow D$$

$$3. D \rightarrow T$$

$$4. R \quad / T$$

$$5. R \rightarrow S \quad \text{Simp. 1}$$

$$6. S \quad \text{MP [4, 5]}$$

$$7. D \quad \text{MP [2, 6]}$$

$$8. T \quad \text{MP [3, 7]}$$

$$1. (P \rightarrow Q) * (R \rightarrow S)$$

$$2. \sim A \rightarrow \sim Q \quad / \text{If } (\sim P \vee \sim S)$$

$$3. A \rightarrow \sim B$$

$$4. B$$

$$5. \sim \sim B \quad [\text{D.N. 4}]$$

$$6. \sim A \quad [\text{M.T. [3, 5]}]$$

$$7. \sim Q \quad [\text{M.P. [2, 6]}]$$

$$8. P \rightarrow Q \quad (\text{Simp. 1})$$

$$9. \sim P \quad [\text{M.T. [8, 7]}]$$

$$10. \sim P \vee \sim S \quad [\text{ADD 9.}]$$

$$X \rightarrow 10 \text{ mult}$$

$$Z \quad \text{no LUI, mult 80 cycles}$$

$$\text{Processor Z ALU 12ns}$$

$$P \times \text{LUI 10ns}$$

$$X \rightarrow 30 \text{ more instructions MUL}$$

(no. of instruction ratio)

Average time/inst

$$\left[ 1 + 0.05 \times 30 \right] \times \begin{pmatrix} 0.05 \times (30) \\ 0.2 \times (50) \\ 0.1 \times (40) \\ 0.45 \times (40) \\ 0.2 \times (30) \end{pmatrix}$$

$$\begin{aligned} & 0.05 \times (960) \\ & + (0.25 \times 60) \\ & + (0.1 \times 48) \\ & + (0.4 \times 48) \\ & + 0.2 \times (36) \end{aligned}$$

extra instructions

$$\text{Average } 98.75 \text{ ns}$$

$$94.25 \text{ ns}$$

time of a program

OUR BASE ASSUMPTION IS THAT THE SW IMPLEMENTATION OF MUL HAS SAME INSTRUCTION DISTRIBUTION

max instructions processor  $x$  can take for mul =  $\lfloor 27.69 \rfloor$   
CW implement  
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$$(P \rightarrow Q) * (R \rightarrow S)$$

$$1. (X \vee \sim Y) \rightarrow Z$$

$$2. \sim Z$$

$$3. \sim(X \vee \sim Y) \text{ [MT 1, 2]}$$

$$4. \sim X * \sim Y \text{ [DeM 3]}$$

$$5. \sim \sim Y \text{ [Simp 4]}$$

$$6. Y \text{ [D.N. 5]}$$