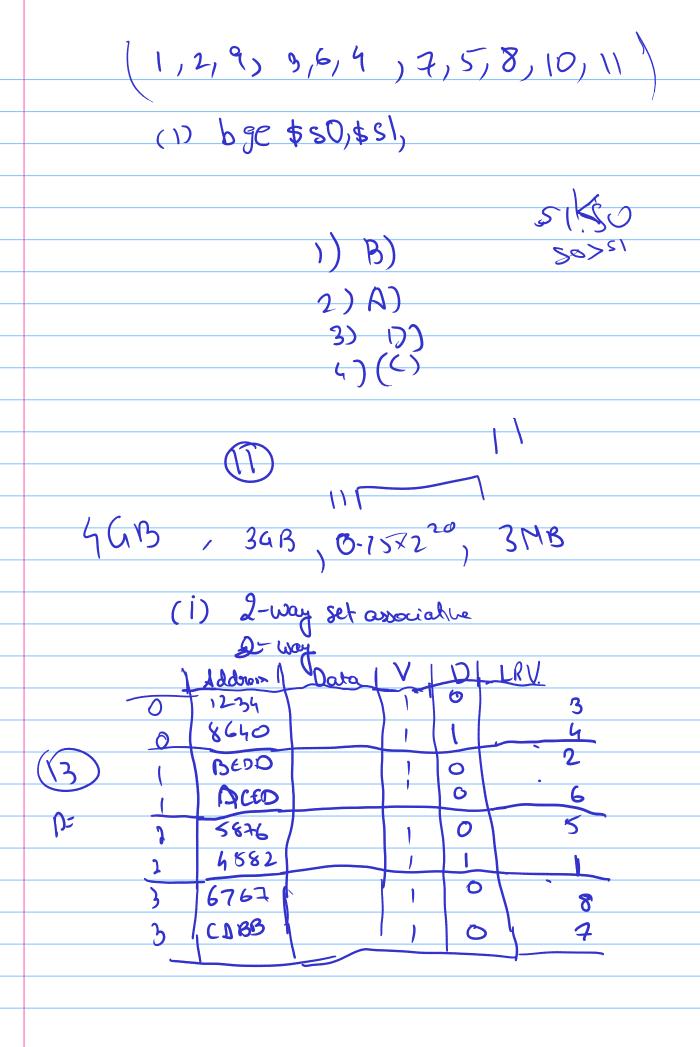
25/4 Instruction Type
int ariumtic CCC Inst Count 45000 Data transfer
FP
Control basger 32000 15000 8000 45000×1+32000×2+15000×2+8000×2 100,000 1550 400MNZ: 1.55 CPI ab ad

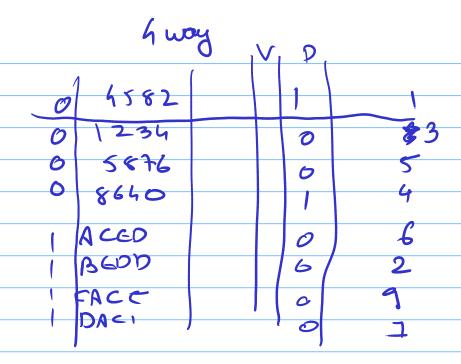
64 Byses Set Block 96+ set number 10+9+4= 23 bits for a word -Imodn 0.6 X(10ms) +0.4x(80+80+10) (6) 1024 integers C[1]Cj]+=aci][k]+bck)[] (104°+1024+1024)

1.25 × 10306 0.02×10-306 5.25-125-0625 03125 05025 -00000000 1-1226 -1023 MT: Ideal M2: EX Stage => 2 penalty cycles pedict not taken ID => Branch Delay MI: CPI1 1+ 26.05) = 11 CPI

(+ \x(0:05)(0-7) 1.035 (a) (b) O(1) Coots of theolist)
set O(ks) (serial panch)
fully O(n) (Q7)(a) -176.375 m 1011,0000 FF 50,0110 - 1.76375 C3B06000 101100000112 

(a+b)+C= 0+1.0 70 ar(b+c) = -2-721023 = 2 7×1023 9.7600 X10 · 25 90 × 10 9.7621025 ·25×1025 19.0190x1027 1,00 19 x1026 \$1.00×10267 internulpt exception so a gcd(a,b) ged(a,a-b) lu \$50,0(\$40) Lw \$ SI, 4 (\$t0) L2: Sub \$50, \$0, £5 المال LI: beg \$ 50,5\$1, exit exit 5 ub 451, 81, 50 -Stt 4+3, \$50, \$51 beg \$\$3,240, L2





1 x 6000

lb \$a0, 0(\$t1) | 1 pixel = 4 instr add \$a0, \$a0, 3 | 10000 = 40000 instr shl \$a0, \$a0, 2 | 5+3999x | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 10000 | 100000 | 10000 | 100000 | 100000 | 100000 | 100000 | 100000 | 100000 | 100000 | 100000 | 100000 |

0 1000 1000 111111000011101011100 (1) X 1.111100010001 1111100011,00011 2019.066 1011.10000101 390625 0.10000 0100010 ,84 0 x 41 08 0000 4250000 0100000 (000000000 1.1011110 10.00000000 0100,00000 10.25 26 = 64

