Model Advisor Report – ASW_AST.slx

Simulink version: 10.7

Model version: 6.223

System: ASW_AST

Current run: 2024/12/30 15:26:59

Treat as Referenced Model: off

Run Summary

Incomplete	Failed	Warning	Justified	Passed	Not Run	Total
0	0	4	0	113	0	117

📁 模型顾问

📁 By Task 🚫0 ❌0 ⚠4 ☑0 ✅113 ▤0

📁 Modeling Standards for ISO 26262 🚫0 ❌0 ⚠1 ☑0 ✅53 ▤0

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Display configuration management data (必需)
 显示模型配置和校验和信息

模型配置与校验和信息

属性	值
模型版本	6.223
作者	xiaoqiang.yuan
日期	Mon Dec 30 14:42:21 2024
模型校验和	2158922281 1855901173 1994306916 3954882216

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Display model metrics and complexity report (必需)
 显示模型或子系统的元素数以及子系统的名称、级别和深度

模型度量信息

显示 Simulink 模块和 Stateflow 构造的元素数

摘要

元素类型	计数
Inport	274
Outport	210
SubSystem	159
Stateflow	17

Simulink

模块类型	计数
From	388
Goto	279
Inport	274
Outport	210
SubSystem	159
DataTypeDuplicate	80
Constant	75
DataTypeConversion	65
Product	39
Switch	38

Saturate	30
RelationalOperator	27
Logic	19
Sum	17
Gain	14
Abs	13
UnitDelay	13
ActionPort	11
MinMax	7
Terminator	5
Merge	5
If	4
Lookup_n-D	4
Demux	3
Selector	3
S-Function	2
Concatenate	2
Width	2
BusSelector	2
SwitchCase	1
EnablePort	1
BusCreator	1

∧ 更少

Stateflow

Stateflow 构造	计数
Stateflow Transitions	74
Stateflow Data	71
Stateflow States	28
Stateflow Junctions	23
Stateflow Charts	8

_____ 模型复杂度信息

显示子系统的名称、级别和深度

最大子系统深度: 9

子系统深度

子系统名称	级别	深度
ASW_AST/AST_SlwTask	1	8
ASW_AST/AST_SlwTask/rss_PhCurrCalc	2	4
ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0" title="ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0	3	3

ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/Compare To Constant" title="ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/Compare To Constant	4	1
ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/Compare To Constant1" title="ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/Compare To Constant1	4	1
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ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/lib_Conversion_6" title="ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/lib_Conversion_6	5	1
ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/lib_u16p15b0_1" title="ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/lib_u16p15b0_1	5	1
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ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_RefTqLimConf/lib_CfgPar_5/vss_SaturationActivation/vss_SaturationRequired" title="ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_RefTqLimConf/lib_CfgPar_5/vss_SaturationActivation/vss_SaturationRequired	6	1
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ASW_AST/AST_SlwTask/vrs_AgEvalModeCalc/rss_AgEvalModeCalcNeed" title="ASW_AST/AST_SlwTask/vrs_AgEvalModeCalc/rss_AgEvalModeCalcNeed	3	1
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ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/rss_RefElecSpdCalc/lib_CfgPar_2/vss_SaturationActivation/vss_SaturationRequired" title="ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/rss_RefElecSpdCalc/lib_CfgPar_2/vss_SaturationActivation/vss_SaturationRequired	7	1
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title="ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/rss_RefElecSpdCalc/lib_Cnvr_0		
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13

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ASW_AST/AST_SlwTask/vrs_SlwTaskOut	2	2
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ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY" title="ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY	3	1

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Check for unconnected objects (必需)

标识模型中未连接的信号线、输入端口和输出端口

20

通过

此模型中不存在未连接的信号线、输入端口和输出端口。

📁 *High-Integrity Systems* ⛔0 ❌0 ⚠1 ☑0 ✅50 🗐0

📁 Simulink ⛔0 ❌0 ⚠1 ☑0 ✅25 🗐0

📋 Check usage of Abs blocks (必需)

标识具有不可达代码或产生溢出的 Abs 模块。

通过

未发现导致不可达代码或产生溢出的 Abs 模块。

📋 Check usage of Math Function blocks (rem and reciprocal functions) (必需)

标识导致非有限结果的余数和倒数运算。

通过

没有发现输入可以为零且使用余数和倒数运算的模块。

📋 Check usage of Math Function blocks (log and log10 functions) (必需)

标识导致非有限结果的 log 和 log10 运算。

通过

未发现输入可能为零或负值且使用 log 和 log10 运算的模块。

📋 Check usage of While Iterator blocks (必需)

标识迭代次数最大值不为正值的 While Iterator 模块。

通过

未发现可能导致无限循环的 While Iterator 模块

📋 Check usage of For and While Iterator subsystems (必需)

标识 While Iterator 和 For Iterator 子系统中的采样时间依赖模块。

通过

For Iterator 或 While Iterator 子系统中的采样时间依赖模块。

_____

Check usage of For Iterator blocks (必需)
标识导致变量循环的 For Iterator 模块。

通过
未发现导致变量循环的 For Iterator 模块。

_____

Check usage of If blocks and If Action Subsystem blocks (必需)
标识没有 else 条件的 If 模块和 If Action Subsystem 模块。

通过
未发现配置或连接有问题的 If 模块。

_____

Check usage of Switch Case blocks and Switch Case Action Subsystem blocks (必需)
标识使用不适当的 Switch Case 模块和 Switch Case Action Subsystem 模块。

通过
未发现配置或连接有问题的 Switch Case 模块。

_____

Check usage of conditionally executed subsystems (必需)
标识条件执行子系统中不合适的模块。

通过
未发现采样时间不正确的模块或异步执行的依赖于采样时间的模块。

_____

Check usage of Merge blocks (必需)
标识可能导致多义性的行为的 Merge 模块构造。

通过
未发现可能导致多义性的行为的 Merge 模块。

_____

Check relational comparisons on floating-point signals
标识对浮点数据类型执行相等或不相等比较的关系模块或运算。

通过
未发现对浮点数据类型执行相等或不相等比较的关系模块或运算。

Check usage of Relational Operator blocks (必需)

标识对不同数据类型执行运算或具有非布尔输出的 Relational Operator 模块。

通过

未发现对不同数据类型执行运算或具有非布尔输出的 Relational Operator 模块。

Check usage of Logical Operator blocks (必需)

标识对非布尔数据类型执行运算的 Logical Operator 模块。

通过

未发现对非布尔数据类型执行运算的 Logical Operator 模块。

Check usage of bitwise operations

标识以有符号数据类型作为输入的位运算。

通过

未发现以有符号数据类型作为输入的位运算。

Check for blocks not recommended for C/C++ production code deployment (必需)

标识代码生成不支持的模块或不推荐用于 C/C++ 生产代码部署的模块。

通过

未发现不推荐用于 C/C++ 生产代码部署的模块。

Check for inconsistent vector indexing methods (必需)

标识模型或子系统中向量索引方法的使用不一致的情形。

通过

未发现使用不一致的索引模式的模块。

Check data types for blocks with index signals

标识索引信号的数据类型不是整数或枚举的模块。

通过

未发现索引信号或变量的数据类型不是整数或枚举的模块或图。

Check usage of variant blocks (必需)

检查可能导致代码无法追溯到需求的变体模块设置。

通过

不存在 "变体激活时间" 设置为 '代码编译' 的变体模块。

_____

Check usage of lookup table blocks (必需)

检查不会生成超范围情形检查代码的 Lookup Table 模块、Prelookup 模块和 Interpolation 模块。

通过

未发现不会生成超范围情形检查代码的 Lookup Table 模块。

_____

Check usage of Signal Routing blocks (必需)

标识 Simulink 中可能影响安全性的 Signal Routing 模块的使用情形。

通过

未发现 Switch 模块可能生成其中不相等运算(~=)表达式至少一侧为浮点变量和常量的代码。

_____

Check for root Outports with missing range definitions (必需)

标识 Simulink 模型或架构模型的具有缺失或错误的最小或最大值的根级输出端口模块。

通过

Simulink 或架构模型的顶层不存在缺失范围定义的输出端口。

_____

Check usage of Assignment blocks (必需)

标识其数组字段未初始化的 Assignment 模块。

通过

所有 Assignment 模块配置的模块参数 "未分配任何输出元素时的动作" 都设置为 "错误"。

_____

Check global variables in graphical functions (必需)

标识对同一全局数据进行读写的表达式。

通过

未发现对同一全局数据进行读写的表达式。

_____

Check usage of Gain blocks (必需)

标识值解析为 1 的 Gain 模块。

通过

未发现值解析为 1 的 Gain 模块。

_____

Check for length of user-defined object names (必需)

标识长度大于阈值的用户定义对象名称。

警告

以下数据对象的名称长度大于阈值(31)。

数据对象	源
Gs16_iSplyCurrTempLim_CUR_tPCBTemp_X	dd_foc_tunable_parameter.sldd
Gs16_iSplyCurrVltgLim_CUR_uSplyVltg_X	dd_foc_tunable_parameter.sldd
Gu16_etaCtlBrd_MAP_ampActPhCurrAmp_X	dd_foc_tunable_parameter.sldd
Gu16_etaCtlBrd_MAP_nActElecSpd_Y	dd_foc_tunable_parameter.sldd

建议采取的操作

请将提到的数据对象的名称更改为长度小于 31。

_____

Check data type of loop control variables (必需)

标识使用非整数数据类型的循环控制项变量。

通过

25

未发现对循环控制计数器变量使用非整数数据类型的 For Iterator 模块或 MATLAB Function 模块。

📁 Stateflow ⛔0 ❌0 ⚠0 ☑0 ✅13 🗒0

📋 Check state machine type of Stateflow charts (必需)
标识状态机类型不同于模型顾问配置编辑器中设置的类型的 Stateflow 图。

通过

未发现采用非推荐的状态机类型的 Stateflow 图。

📋 Check Stateflow charts for ordering of states and transitions (必需)
标识不对并行状态和转移进行显式排序的 Stateflow 图。

通过

未发现使用非推荐的状态/转移执行顺序设置的 Stateflow 图。

📋 Check usage of bitwise operations in Stateflow charts (必需)
标识 Stateflow 图中按位运算使用了有符号数据类型操作数的情形。

通过

未发现其中按位运算使用了有符号数据类型操作数的 Stateflow 对象。

📋 Check for Strong Data Typing with Simulink I/O (必需)
验证 Simulink 和 Stateflow 之间边界上强数据定型的配置设置。

通过

未发现将 '对 Simulink I/O 使用强数据定型' 设置为 off 的 Stateflow 图。

📋 Check Stateflow charts for transition paths that cross parallel state boundaries (必需)
标识 Stateflow 图中跨并行状态边界的转移路径。

通过

在 Stateflow 图中未发现跨并行状态边界的转移路径。

Check for inappropriate use of transition paths (必需)
标识经过状态而未终止于子状态的转移路径。

通过

未发现经过状态而未终止于子状态的转移路径。

_____

Check Stateflow charts for strong data typing (必需)
标识 Stateflow 对象中具有不同数据类型的变量和参数的表达式。

通过

未发现具有不同数据类型的变量和参数的表达式。

_____

Check naming of ports in Stateflow charts (必需)
标识 Stateflow 端口和相关联信号之间的名称不匹配情况。

通过

Stateflow 端口和相关联信号之间不存在名称不匹配情况

_____

Check scoping of Stateflow data objects (必需)
标识具有局部作用域但其作用域不是限定在图级别或更低级别的 Stateflow 数据对象

通过

所有 Stateflow 数据对象都适当地限定了作用域。

_____

Check Stateflow charts for uniquely defined data objects (必需)
标识在图中的多个作用域内定义的局部数据标识符。

通过

未发现在多个作用域内定义的 Stateflow 数据标识符。

_____

Check usage of shift operations for Stateflow data (必需)
标识可能影响安全性的 Stateflow 移位运算的使用情形。

通过

不存在大于输入或输出类型位宽度的 Stateflow 移位运算。

_____

Check assignment operations in Stateflow charts (必需)
标识 Stateflow 对象中将整数和定点计算转换为更宽的数据类型的赋值运算。

通过
未发现将整数和定点计算转换为更宽数据类型的赋值运算。

_____

Check Stateflow charts for unary operators (必需)
标识 Stateflow 对象中应用于无符号数据类型的一元减法运算符。

通过
在 Stateflow 对象中未发现应用于无符号数据类型的一元减法运算。

📁 MATLAB ⛔0 ❌0 ⚠0 ☑0 ✅10 📄0

_____

✅ Check usage of standardized MATLAB function headers
标识 MATLAB 函数中标准化函数头的使用情形。

通过
未发现没有标准化函数头的 MATLAB Function 模块。

_____

Check for MATLAB Function interfaces with inherited properties (必需)
标识具有继承的复/实性或数据类型属性的输入、输出或参数的 MATLAB Function。

通过
未发现具有继承的复/实性或数据类型属性的 MATLAB Function 接口。

_____

Check MATLAB Function metrics (必需)
标识违反代码和复杂度度量的 MATLAB Function。

通过
未发现违反代码和复杂度度量的 MATLAB Function 模块。

_____

Check MATLAB Code Analyzer messages (必需)
检查 MATLAB 函数的 %#codegen 指令、MATLAB 代码分析器消息和申述消息 ID。

通过

28

未发现 MATLAB Function 模块具有代码分析器消息、缺失 %#codegen 指令或不当使用申述消息 ID。

_____

Check if/elseif/else patterns in MATLAB Function blocks (必需)
标识嵌入的 MATLAB 代码中没有适当 else 条件的 if/elseif/else 构型。

通过
未发现不适当的 if/elseif/else 构型。

_____

Check switch statements in MATLAB Function blocks (必需)
标识嵌入的 MATLAB 代码中使用不适当的 switch 语句。

通过
未发现使用不适当的 switch 语句。

_____

Check usage of relational operators in MATLAB Function blocks (必需)
标识 MATLAB Function 模块中对不同数据类型的操作数执行运算的关系运算符。

通过
未发现对不同数据类型的操作数执行运算的关系运算符。

_____

Check usage of equality operators in MATLAB Function blocks (必需)
标识 MATLAB Function 模块中对浮点操作数执行运算的相等运算符。

通过
未发现对浮点操作数执行运算的相等运算符。

_____

Check usage of logical operators and functions in MATLAB Function blocks (必需)
标识对数值数据类型的操作数执行运算的逻辑运算符和函数。

通过
未发现对数值数据类型的操作数执行运算的逻辑运算符或函数。

_____

Check type and size of condition expressions (必需)
标识并非逻辑标量的条件表达式。

通过

未发现并非逻辑标量的条件表达式。

📁 Naming 🚫0 ❌0 ⚠️0 ☑️0 ✅1 🗒0

📇 Check model file name (必需)

标识模型文件名中不合适的字符和长度问题。

通过

未发现模型文件名有问题。

📁 Code 🚫0 ❌0 ⚠️0 ☑️0 ✅1 🗒0

📇 Check for blocks not recommended for MISRA C:2012 (必需)
通过

📁 Model Metrics 🚫0 ❌0 ⚠️0 ☑️0 ✅1 🗒0

📁 *Count Metrics* 🚫*0* ❌*0* ⚠️*0* ☑️*0* ✅*1* 🗒*0*

📇 Subsystem depth metric (必需)

显示模型或子系统中子系统的深度。

通过

组件	子系统深度
..../vss_SaturationRequired	**8**
..../Subsystem/lib_ResettableUnitDelay_1	**8**
..../lib_Conversion_1	**8**
..../lib_Conversion_2	**8**

..../lib_Conversion_3	**8**
..../lib_Conversion_1	**8**
..../lib_Conversion_2	**8**
..../lib_Conversion_3	**8**
..../vss_LowPassFilter/lib_Conversion_1	**7**
..../vss_LowPassFilter/lib_Conversion_2	**7**
..../lib_CfgPar_6/vss_SaturationActivation	**7**
..../sss_SensAgCalib/lib_RateLim_2/Subsystem	**7**
..../lib_RateLim_2/sss_DecrementFlagTrigger	**7**
..../lib_RateLim_2/sss_IncrementFlagTrigger	**7**
..../vss_SaturationRequired	**7**
..../Subsystem/lib_ResettableUnitDelay_1	**7**
..../lib_Conversion_1	**7**
..../lib_Conversion_2	**7**
..../lib_Conversion_3	**7**
..../lib_Conversion_1	**7**
..../lib_Conversion_2	**7**
..../lib_Conversion_3	**7**
..../Subsystem/lib_ResettableUnitDelay_1	**7**
..../lib_Conversion_1	**7**
..../lib_Conversion_2	**7**
..../lib_Conversion_3	**7**
..../lib_Conversion_1	**7**
..../lib_Conversion_2	**7**
..../lib_Conversion_3	**7**
..../vss_SaturationRequired	**7**
..../vss_SaturationRequired	**7**

..../vss_SaturationRequired	**6**
..../vss_SaturationRequired	**6**
..../vss_SaturationRequired	**6**
..../vss_FilterVariant/vss_LowPassFilter	**6**
..../sss_SensAgCalib/lib_CfgPar_6	**6**
..../sss_SensAgCalib/lib_u16r15b0_2	**6**
..../sss_SensAgCalib/lib_RateLim_2	**6**
..../lib_CfgPar_2/vss_SaturationActivation	**6**
..../lib_RateLim_0/Subsystem	**6**
..../lib_RateLim_0/sss_DecrementFlagTrigger	**6**
..../lib_RateLim_0/sss_IncrementFlagTrigger	**6**
..../lib_RateLim_1/Subsystem	**6**
..../lib_RateLim_1/sss_DecrementFlagTrigger	**6**
..../lib_RateLim_1/sss_IncrementFlagTrigger	**6**
..../lib_CfgPar_0/vss_SaturationActivation	**6**
..../lib_CfgPar_1/vss_SaturationActivation	**6**
..../lib_CfgPar_3/vss_SaturationActivation	**5**
..../lib_CfgPar_5/vss_SaturationActivation	**5**
..../lib_CfgPar_4/vss_SaturationActivation	**5**
..../ias_Case2/lib_DynSat_2	**5**
..../ias_Case2/lib_Cnvr_2	**5**
..../lib_IirFilter_0/vss_FilterVariant	**5**
..../ias_NonZeroInput/lib_u16p15b0_1	**5**
..../ias_NonZeroInput/lib_Conversion_6	**5**
..../ias_ZeroInput/lib_Conversion_1	**5**
..../ens_SensAgCalibNeed/sss_SensAgCalib	**5**
..../ens_SensAgCalibNeed/lib_AgDvtLim_0	**5**

..../ias_NonZeroInput/lib_u16p15b0_1	**5**
..../ias_NonZeroInput/lib_Conversion_6	**5**
..../ias_ZeroInput/lib_Conversion_1	**5**
..../sss_SysSTMSpdConf/sas_SysStLmpHmeCs	**5**
..../sss_SysSTMSpdConf/sas_SysStDflCs	**5**
..../rss_RefElecSpdCalc/lib_Cnvr_0	**5**
..../rss_RefElecSpdCalc/lib_Cnvr_1	**5**
..../rss_RefElecSpdCalc/lib_DynSat_1	**5**
..../rss_RefElecSpdCalc/lib_CfgPar_2	**5**
..../rss_RefElecSpdCalc/lib_RateLim_0	**5**
..../rss_RefElecSpdCalc/lib_RateLim_1	**5**
..../rss_RefMechSpdCalc/lib_DynSat_0	**5**
..../rss_RefMechSpdCalc/lib_CfgPar_0	**5**
..../rss_RefMechSpdCalc/lib_CfgPar_1	**5**
..../sss_SysSTMSpdConf/sas_SysStRunCs	**5**
..../sss_SplyCurrEqvCurrLim/lib_CfgPar_3	**4**
..../sss_DrnRefTqLimCalc/lib_Cnvr_1	**4**
..../sss_DrnRefTqLimCalc/lib_Cnrv_2	**4**
..../sss_DrnRefTqLimCalc/lib_CfgPar_4	**4**
..../sss_RefTqLimConf/lib_CfgPar_5	**4**
..../sss_DrnRefTqLimCalc/lib_Cnvr_2	**4**
..../sss_SplyCurrEqvCurrLim/ias_Case1	**4**
..../sss_SplyCurrEqvCurrLim/ias_Case2	**4**
..../sss_DrnRefTqLimCalc/lib_DynSat_3	**4**
..../sss_SplyCurrEqvCurrLim/lib_Cnvr_2	**4**
..../sss_DrnRefTqLimCalc/lib_Cnvr_3	**4**
..../sss_SplyCurrEqvCurrLim/lib_Cnvr_3	**4**

..../sss_DrnRefTqLimCalc/lib_Cnvr_4	4
..../sss_SplyCurrEqvCurrLim/lib_IirFilter_0	4
..../sss_SplyCurrEqvCurrLim/lib_Conversion_1	4
..../lib_AmpCalc_0/ias_ZeroInput	4
..../lib_AmpCalc_0/ias_NonZeroInput	4
..../lib_MAF_1/lib_Conversion_1	4
..../lib_MAF_0/lib_Conversion_1	4
..../rss_SensAgCalibNeed/ens_SensAgCalibNeed	4
..../rss_ShOffFlgConfSens/ias_NoPosSCDCalib	4
..../rss_ShOffFlgConfSens/ias_PosSCDCalib	4
..../lib_RiAgSideLenCalc_0/ias_ZeroInput	4
..../lib_RiAgSideLenCalc_0/ias_NonZeroInput	4
..../rss_RefMechSpdCalc	4
..../rss_RefElecSpdCalc	4
..../rss_RefElecSpdCalcHybr/sss_SysSTMSpdConf	4
..../rss_RefTqLimCalc/sss_DrnRefTqLimCalc	3
..../rss_RefTqLimCalc/sss_RefTqLimConf	3
..../rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim	3
..../AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0	3
..../AST_SlwTask/rss_PhCurrCalc/lib_MAF_1	3
..../AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_1	3
..../AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_2	3
..../AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_4	3
..../AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_6	3
..../AST_SlwTask/rss_SplyCurrEst/lib_MAF_0	3
..../AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_3	3
..../AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_5	3

..../AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_7	**3**
..../AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_8	**3**
..../AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_9	**3**
..../AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_10	**3**
..../vrs_SensAgCalib/rss_SensAgCalibNeed	**3**
..../vrs_SlwTaskOut/sss_SlwTaskOutHybr	**3**
..../vrs_ShOffFlgConf/rss_ShOffFlgConfSens	**3**
..../rss_RefVltgLimCalc/lib_RiAgSideLenCalc_0	**3**
..../rss_RefVltgLimCalc/lib_Cnrv_1	**3**
..../rss_RefElecSpdCalcHybr	**3**
..../vrs_SlwTaskIn/sss_SlwTaskInHybr	**3**
..../rss_AgEvalModeCalcNeed	**3**
..../AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY	**3**
ASW_AST/AST_SlwTask/rss_RefTqLimCalc	**2**
ASW_AST/AST_SlwTask/rss_PhCurrCalc	**2**
ASW_AST/AST_SlwTask/rss_SplyCurrEst	**2**
ASW_AST/AST_SlwTask/vrs_SensAgCalib	**2**
ASW_AST/AST_SlwTask/vrs_SlwTaskOut	**2**
ASW_AST/AST_SlwTask/rss_RefVltgLimCalc	**2**
ASW_AST/AST_SlwTask/vrs_ShOffFlgConf	**2**
ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc	**2**
ASW_AST/AST_SlwTask/vrs_SlwTaskIn	**2**
ASW_AST/AST_SlwTask/vrs_AgEvalModeCalc	**2**
ASW_AST/AST_SlwTask/vrs_SysSTM	**2**
ASW_AST/AST_SlwTask	**1**
ASW_AST	**0**

∧ 更少

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📁 Modeling Standards for MAAB ⛔0 ❌0 ⚠3 ☑0 ✅48 ▤0

📁 *Naming Conventions* ⛔*0* ❌*0* ⚠*0* ☑*0* ✅*4* ▤*0*

_____

📑 Check file names (必需)
　检查模型文件名，确保名称符合建议的规范。

　通过
　所有文件都有正确的名称。

_____

✅ Check port block names
　通过

_____

📑 Check character usage in signal labels (必需)
　标识对 C 变量名称来说不正确的信号标签。

　通过
　所有信号标签都使用正确的字符。

_____

📑 Check Simulink bus signal names (必需)
　通过
　未发现具有不可使用的字符的 Simulink 总线信号

📁 *Model Architecture* ⛔*0* ❌*0* ⚠*1* ☑*0* ✅*2* ▤*0*

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⚠ Check for missing ports in Variant Subsystems
　检查可变子系统的输入/输出

　警告

以下可变子系统在可变子系统选择项中具有不同输入/输出数目:

可变子系统	可变子系统的选择项
ASW_AST/AST_SlwTask/vrs_SensAgCalib	
ASW_AST/AST_SlwTask/vrs_ShOffFlgConf	
ASW_AST/AST_SlwTask/vrs_SlwTaskOut	
ASW_AST/AST_SlwTask/vrs_SlwTaskOut	
ASW_AST/AST_SlwTask/vrs_SysSTM	
ASW_AST/AST_SlwTask/vrs_SysSTM	

建议采取的操作

请调整可变子系统及其选择项，使其具有相同的输入/输出数目。

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Check use of default variants (必需)
  na_0036: 默认变体

标识不使用默认变体的可变子系统。

  通过
  模型中的所有可变子系统都使用默认变体

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Check use of single variable variant conditionals (必需)
  标识使用多变量复合条件的可变子系统。

  通过
  未发现具有多变量复合条件的可变子系统

Model Configuration Options   ⛔0 ❌0 ⚠0 ☑0 ✅1 📄0

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 Check Implement logic signals as Boolean data (vs. double) (必需)

标识**将逻辑信号实现为布尔数据(而不是双精度数据)**是否处于选中状态。

通过

将逻辑信号实现为布尔数据(而不是双精度数据)处于选中状态。

📁 *Simulink* 🚫*0* ❌*0* ⚠*2* 🔲*0* ✅*13* 📄*0*

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 Check display for port blocks (必需)

通过

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 Check whether block names appear below blocks (必需)

通过

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 Check for matching port and signal names (必需)

标识端口名称和对应信号名称之间不匹配的情形。

通过

所有信号和端口名称都匹配。

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 Check for unconnected ports and signal lines (必需)

标识未连接的模块输入端口、输出端口和信号线。

通过

模型中的所有信号线和端口都已连接。

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⚠ Check for prohibited blocks in discrete controllers (必需)

标识离散控制器中不允许的模块。禁止的模块包括所有连续模块和一些源模块和信宿模块。

警告

以下离散控制器包含禁止的模块:

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- ASW_AST/AST_SlwTask
- ASW_AST/AST_SlwTask/Constant3
- ASW_AST/AST_SlwTask/From
- ASW_AST/AST_SlwTask/From1
- ASW_AST/AST_SlwTask/From10
- ASW_AST/AST_SlwTask/From11
- ASW_AST/AST_SlwTask/From12
- ASW_AST/AST_SlwTask/From13
- ASW_AST/AST_SlwTask/From14
- ASW_AST/AST_SlwTask/From15
- ASW_AST/AST_SlwTask/From16
- ASW_AST/AST_SlwTask/From17
- ASW_AST/AST_SlwTask/From18
- ASW_AST/AST_SlwTask/From19
- ASW_AST/AST_SlwTask/From2
- ASW_AST/AST_SlwTask/From20
- ASW_AST/AST_SlwTask/From21
- ASW_AST/AST_SlwTask/From22
- ASW_AST/AST_SlwTask/From23
- ASW_AST/AST_SlwTask/From26
- ASW_AST/AST_SlwTask/From27
- ASW_AST/AST_SlwTask/From29
- ASW_AST/AST_SlwTask/From3
- ASW_AST/AST_SlwTask/From30
- ASW_AST/AST_SlwTask/From32
- ASW_AST/AST_SlwTask/From35
- ASW_AST/AST_SlwTask/From36
- ASW_AST/AST_SlwTask/From37

- ASW_AST/AST_SlwTask/From38
- ASW_AST/AST_SlwTask/From4
- ASW_AST/AST_SlwTask/From40
- ASW_AST/AST_SlwTask/From41
- ASW_AST/AST_SlwTask/From42
- ASW_AST/AST_SlwTask/From43
- ASW_AST/AST_SlwTask/From44
- ASW_AST/AST_SlwTask/From45
- ASW_AST/AST_SlwTask/From46
- ASW_AST/AST_SlwTask/From47
- ASW_AST/AST_SlwTask/From48
- ASW_AST/AST_SlwTask/From49
- ASW_AST/AST_SlwTask/From5
- ASW_AST/AST_SlwTask/From52
- ASW_AST/AST_SlwTask/From53
- ASW_AST/AST_SlwTask/From54
- ASW_AST/AST_SlwTask/From55
- ASW_AST/AST_SlwTask/From56
- ASW_AST/AST_SlwTask/From57
- ASW_AST/AST_SlwTask/From58
- ASW_AST/AST_SlwTask/From59
- ASW_AST/AST_SlwTask/From6
- ASW_AST/AST_SlwTask/From60
- ASW_AST/AST_SlwTask/From61
- ASW_AST/AST_SlwTask/From62
- ASW_AST/AST_SlwTask/From63
- ASW_AST/AST_SlwTask/From64
- ASW_AST/AST_SlwTask/From67

- ASW_AST/AST_SlwTask/From7
- ASW_AST/AST_SlwTask/From8
- ASW_AST/AST_SlwTask/From9
- ASW_AST/AST_SlwTask/Goto
- ASW_AST/AST_SlwTask/Goto1
- ASW_AST/AST_SlwTask/Goto10
- ASW_AST/AST_SlwTask/Goto11
- ASW_AST/AST_SlwTask/Goto12
- ASW_AST/AST_SlwTask/Goto13
- ASW_AST/AST_SlwTask/Goto14
- ASW_AST/AST_SlwTask/Goto15
- ASW_AST/AST_SlwTask/Goto16
- ASW_AST/AST_SlwTask/Goto17
- ASW_AST/AST_SlwTask/Goto19
- ASW_AST/AST_SlwTask/Goto2
- ASW_AST/AST_SlwTask/Goto21
- ASW_AST/AST_SlwTask/Goto23
- ASW_AST/AST_SlwTask/Goto28
- ASW_AST/AST_SlwTask/Goto29
- ASW_AST/AST_SlwTask/Goto3
- ASW_AST/AST_SlwTask/Goto30
- ASW_AST/AST_SlwTask/Goto31
- ASW_AST/AST_SlwTask/Goto33
- ASW_AST/AST_SlwTask/Goto34
- ASW_AST/AST_SlwTask/Goto35
- ASW_AST/AST_SlwTask/Goto36
- ASW_AST/AST_SlwTask/Goto37
- ASW_AST/AST_SlwTask/Goto38

- ASW_AST/AST_SlwTask/Goto39
- ASW_AST/AST_SlwTask/Goto4
- ASW_AST/AST_SlwTask/Goto40
- ASW_AST/AST_SlwTask/Goto41
- ASW_AST/AST_SlwTask/Goto42
- ASW_AST/AST_SlwTask/Goto43
- ASW_AST/AST_SlwTask/Goto44
- ASW_AST/AST_SlwTask/Goto45
- ASW_AST/AST_SlwTask/Goto46
- ASW_AST/AST_SlwTask/Goto5
- ASW_AST/AST_SlwTask/Goto6
- ASW_AST/AST_SlwTask/Goto7
- ASW_AST/AST_SlwTask/Goto8
- ASW_AST/AST_SlwTask/Goto9
- ASW_AST/AST_SlwTask/busAST_SlwTaskIn
- ASW_AST/AST_SlwTask/busAST_SlwTaskOut
- ASW_AST/AST_SlwTask/rss_PhCurrCalc
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/Demux
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/From
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/From1
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/From17
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/From5
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/Goto24
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/Goto25
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/Goto4
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/Goto5
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/Saturation1
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/Terminator

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/AND

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/Compare To Constant

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- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/Compare To Constant/u

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/Compare To Constant/y

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/Compare To Constant1

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- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/Compare To Constant1/u

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/Compare To Constant1/y

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/From

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/From1

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/From2

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/From3

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/From5

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/From6

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/From7

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/Goto

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/Goto1

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/Goto2

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/Goto4

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/Goto5

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/If

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/Merge

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/Abs

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/Abs1

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- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/Constant3

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/DTC

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- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/Divide2

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/From

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/From1

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/From6

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/Gain

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/Goto1

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/Goto2

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/Goto3

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/Min

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/Min1

- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/Multiply

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- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_NonZeroInput/Selector1

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- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_ZeroInput
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_ZeroInput/Action Port
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_ZeroInput/Constant
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/ias_ZeroInput/lb_genHypt
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- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/lb_genFstCath
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/lb_genHypt
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_AmpCalc_0/lb_genSndCath
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_MAF_1
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_MAF_1/Data Type Duplicate
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_MAF_1/Divide
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_MAF_1/Sum of Elements
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_MAF_1/Tapped Delay
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_MAF_1/Vector Concatenate
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_MAF_1/Width
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_MAF_1/lb_genInp
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_MAF_1/lb_genOutp
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_MAF_1/lb_genOutpDelyVect
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_MAF_1/lib_Conversion_1

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- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_MAF_1/lib_Conversion_1/lb_genInp
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/lib_MAF_1/lib_Conversion_1/lb_genOutp
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/s16_ampActPhCurrAmp
- ASW_AST/AST_SlwTask/rss_PhCurrCalc/s16_iActDaxQaxCurr
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/From
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/From1
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/From11
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/From2
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/From3
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/From4
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/From5
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/From6
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/From7
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/From8
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/Goto
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/Goto1
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/Goto11
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/Goto16
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/Goto2
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/Goto4
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/Goto5
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/Goto6
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/Goto7
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/Saturation
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/Saturation1

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/Saturation4

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/Saturation5

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/s16_trqFaltRefTqLim

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/s16_trqRefTqLim

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/s16_uModulDaxQaxVltg

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/Cdbl_trqTqMaxVal

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/Constant

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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/Divide

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/From

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/From3

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/From4

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/From5

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/From6

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/From7

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/From9

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/Gain

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/Gain1

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/Goto

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/Goto1

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/Goto2

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/Goto3

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/Goto4

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/Goto5

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/Goto6

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/Min1

- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/Min2
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/Product1
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/Product2
-
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_CfgPar_4
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_Cnrv_2
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_Cnvr_1
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_Cnvr_2
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_Cnvr_3

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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_Cnvr_4
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_DynSat_3
-
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_DynSat_3/From
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_DynSat_3/From1
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_DynSat_3/From2
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_DynSat_3/From5
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_DynSat_3/From6
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_DynSat_3/From8
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_DynSat_3/From9
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_DynSat_3/Goto
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_DynSat_3/Goto1
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_DynSat_3/Goto2
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/lib_DynSat_3/Goto3
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/maxPolePairPerCurr
-
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/s16_trqDrnRefTqLim
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/u16_psiPermMagFlx
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_DrnRefTqLimCalc/u8_tPCBTempSubs
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_RefTqLimConf
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_RefTqLimConf/From
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_RefTqLimConf/From1
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_RefTqLimConf/From2
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_RefTqLimConf/Goto
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_RefTqLimConf/Goto1
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_RefTqLimConf/Goto2
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_RefTqLimConf/Min
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_RefTqLimConf/lib_CfgPar_5
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_RefTqLimConf/s16_trqDrnRefTrqLim
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_RefTqLimConf/s16_trqRefTqLim
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/Abs
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/Divide
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/From
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/From1
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/From10
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/From12
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/From16
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/From17
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/From2
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/From5

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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/From7
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/From8
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/From9
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/Gain3
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/Goto12
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/Goto2
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/Product
- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/Product1
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/ias_Case2
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/lib_CfgPar_3
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/sss_SplyCurrEqvCurrLim/u16_uSplyVltg
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- ASW_AST/AST_SlwTask/rss_RefTqLimCalc/u16_facSplyCurrComp
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- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/Constant2

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/From

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/From1

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/From2

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/From21

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/From22

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/From3

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/From4

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/From8

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/Goto

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/Goto1

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/Goto12

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/Goto2

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/Goto27

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/Goto5

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/Product

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/Product1

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/Saturation

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/Saturation1

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/lib_Cnrv_1

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/lib_Cnrv_1/Data Type Duplicate

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/lib_Cnrv_1/Data Type Conversion

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/lib_Cnrv_1/lb_genInp

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/lib_Cnrv_1/lb_genOutp

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/lib_RiAgSideLenCalc_0

- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/lib_RiAgSideLenCalc_0/AND

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- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/lib_RiAgSideLenCalc_0/From
- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/lib_RiAgSideLenCalc_0/From1
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- ASW_AST/AST_SlwTask/rss_RefVltgLimCalc/lib_RiAgSideLenCalc_0/Merge
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- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Abs
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- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Abs2
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- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Compare To Constant6/y
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- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Demux1

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- ASW_AST/AST_SlwTask/rss_SplyCurrEst/From9
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- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Gain2
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Gain3
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Gain4

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Goto1
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- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Saturation
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- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Saturation11
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Saturation12
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Saturation2

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Saturation3

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Saturation4

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Saturation6

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Saturation7

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Saturation9

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Switch

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/Terminator

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/enm_stMotoSt

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_1

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_1/Data Type Duplicate

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_1/Data Type Conversion

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_1/lb_genInp

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_1/lb_genOutp

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_10

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_10/Data Type Duplicate

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_10/Data Type Conversion

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_10/lb_genInp

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_10/lb_genOutp

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_2

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_2/Data Type Duplicate

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_2/Data Type Conversion

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_2/lb_genInp

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_2/lb_genOutp

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_3

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_3/Data Type Duplicate

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_3/Data Type Conversion

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- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_3/lb_genOutp

- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_4
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_4/Data Type Duplicate
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_4/Data Type Conversion
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_4/lb_genInp
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_4/lb_genOutp
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_5
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_5/Data Type Duplicate
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- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_Cnvr_6/lb_genOutp
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- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_MAF_0/Tapped Delay
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- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_MAF_0/lib_Conversion_1/lb_genInp
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/lib_MAF_0/lib_Conversion_1/lb_genOutp
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/s16_ampActPhCurrAmp
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/s16_iActDaxQaxCurr
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/s16_iEstSplyCurr
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/s16_nActElecSpd
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/s16_nRmpRefElecSpd
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/s16_pwrSplyElecPwr
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/s16_uModulDaxQaxVltg
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/u16_facSplyCurrComp
- ASW_AST/AST_SlwTask/rss_SplyCurrEst/u16_uSplyVltg
- ASW_AST/AST_SlwTask/vrs_AgEvalModeCalc
- ASW_AST/AST_SlwTask/vrs_AgEvalModeCalc/bol_flgPhiSensFaltRawFlg

- ASW_AST/AST_SlwTask/vrs_AgEvalModeCalc/enm_modeAgEvalMode
- ASW_AST/AST_SlwTask/vrs_AgEvalModeCalc/enm_stMotoSt
- ASW_AST/AST_SlwTask/vrs_AgEvalModeCalc/rss_AgEvalModeCalcNeed
- ASW_AST/AST_SlwTask/vrs_AgEvalModeCalc/rss_AgEvalModeCalcNeed/From
- ASW_AST/AST_SlwTask/vrs_AgEvalModeCalc/rss_AgEvalModeCalcNeed/From12
- ASW_AST/AST_SlwTask/vrs_AgEvalModeCalc/rss_AgEvalModeCalcNeed/From59
- ASW_AST/AST_SlwTask/vrs_AgEvalModeCalc/rss_AgEvalModeCalcNeed/Goto
- ASW_AST/AST_SlwTask/vrs_AgEvalModeCalc/rss_AgEvalModeCalcNeed/Goto1
- ASW_AST/AST_SlwTask/vrs_AgEvalModeCalc/rss_AgEvalModeCalcNeed/Goto35
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- ASW_AST/AST_SlwTask/vrs_AgEvalModeCalc/rss_AgEvalModeCalcNeed/enm_stMotoSt
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- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/bol_enPosSCDCalibEnFlg
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/bol_flgOvrTstCtlUDS2F
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/bol_flgSlepEn
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/busOBD_FaltSt
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/enm_stMotoSt
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/enm_stSysSt
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Bus Selector

- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From1
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From10
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From11
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From12
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From13
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From14
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From15
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From2
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From3
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From39
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From4
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From5
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From6
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From7
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From8
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/From9
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto1
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto10
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto11
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto12
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto13
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto14
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto15
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto2
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto3
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto32

- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto4
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto5
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto6
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto7
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto8
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Goto9
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- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Saturation2
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/Saturation3
-
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- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/bol_flgSlepEn
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/busOBD_FaltSt
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/enm_stMotoSt
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcHybr/enm_stSysSt
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- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/s16_nComRefMechSpd
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/s16_nFaltRefMechSpd
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/s16_nRefElecSpd
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/s16_nRmpRefElecSpd
- ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/u8_modeMCUOpMode
- ASW_AST/AST_SlwTask/vrs_SensAgCalib
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/bol_enPosSCDCalibEnFlg
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/bol_flgPosSCDCalibPermFail
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/bol_flgPosSCDCalibSucFlg
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/enm_stPosSCDCalibSt
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/enm_stSysSt
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed
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- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/From
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/From1
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/From2
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/From3
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/From4
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/From5
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/From6
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/From7
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/From8
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/Goto
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/Goto1

- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/Goto2

- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/Goto3

- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/Goto4

- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/Goto5

- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/Goto6

- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/Goto7

- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/Goto8

- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/Logical Operator

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- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/enm_stPosSCDCalibSt

- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/enm_stSysSt

- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/ens_SensAgCalibNeed

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- ASW_AST/AST_SlwTask/vrs_SensAgCalib/rss_SensAgCalibNeed/u16_ADSensMechAgDvt
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- ASW_AST/AST_SlwTask/vrs_SensAgCalib/s16_trqPosSCDCalibRefTq
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/u16_ADSensMechAgDvt
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/u16_phiPosSCDCalibElecAg
- ASW_AST/AST_SlwTask/vrs_SensAgCalib/u16_phiSensActElecAg
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/bol_enPosSCDCalibEnFlg
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/bol_flgShOffFlg
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/enm_stSysSt
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens
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- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/Constant
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/Constant1
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/From
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/From1
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/From3
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/From4
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/From5
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/From6
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/Goto
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/Goto1
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/Goto2
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/Goto3
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/Goto4
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/If
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/Merge
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/Switch
-
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/bol_flgShOffFlg
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/enm_stSysSt
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/ias_NoPosSCDCalib
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- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/ias_PosSCDCalib
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- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSens/u8_stMaxFaltLvl
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/u8_stMaxFaltLvl
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/bol_enPosSCDCalibEnFlg
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/bol_flgOvrTstCtlUDS2F
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/bol_flgPhiSensFaltRawFlg
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/bol_flgSlepEn
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/busAST_SlwTaskIn
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/busOBD_FaltSt
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/enm_stMotoSt
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/s16_iActDaxQaxCurr
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/s16_nActElecSpd
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/s16_nComRefMechSpd
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/s16_nFaltRefMechSpd
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/s16_trqFaltRefTqLim
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/s16_uModulDaxQaxVltg
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/s16_uRefDaxVltg
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn1
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn17
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn18
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn19
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn2
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn20

- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn21
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn24
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn25
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn26
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn27
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn3
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn33
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn34
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn4
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn5
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn6
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn7
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/BusElementIn8
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/bol_enPosSCDCalibEnFlg
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/bol_flgOvrTstCtlUDS2F
-
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/bol_flgSlepEn
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/busOBD_FaltSt
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/enm_stMotoSt
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/s16_iActDaxQaxCurr
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/s16_nActElecSpd
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/s16_nComRefMechSpd
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/s16_nFaltRefMechSpd
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/s16_trqFaltRefTqLim
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/s16_uModulDaxQaxVltg
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/s16_uRefDaxVltg
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/u16_ampMaxNrmVltgAmp
- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/u16_phiSensActElecAg

- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/u16_uSplyVltg

- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/u8_FrzFrmSaveFlg

- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/u8_modeMCUOpMode

- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/u8_stMaxFaltLvl

- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/sss_SlwTaskInHybr/u8_tPCBTempSubs

- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/u16_ampMaxNrmVltgAmp

- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/u16_phiSensActElecAg

- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/u16_uSplyVltg

- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/u8_FrzFrmSaveFlg

- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/u8_modeMCUOpMode

- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/u8_stMaxFaltLvl

- ASW_AST/AST_SlwTask/vrs_SlwTaskIn/u8_tPCBTempSubs

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/bol_flgPosSCDCalibPermFail

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/bol_flgPosSCDCalibSucFlg

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/bol_flgShOffFlg

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/busAST_SlwTaskOut

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/enm_modeAgEvalMode

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/enm_stPosSCDCalibSt

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/enm_stSysSt

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/s16_ampActPhCurrAmp

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/s16_iEstSplyCurr

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/s16_nRefElecSpd

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/s16_nRmpRefElecSpd

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/s16_pwrSplyElecPwr

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/s16_trqPosSCDCalibRefTq

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/s16_trqRefTqLim

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/s16_uRefDaxVltgLim

- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/s16_uRefQaxVltgLim
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr/Bus Creator1
-
-
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr/bol_flgShOffFlg
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr/busAST_SlwTaskOut
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr/enm_modeAgEvalMode
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr/enm_stPosSCDCalibSt
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr/enm_stSysSt
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr/s16_ampActPhCurrAmp
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr/s16_iEstSplyCurr
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr/s16_nRefElecSpd
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr/s16_nRmpRefElecSpd
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr/s16_pwrSplyElecPwr
-
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr/s16_trqRefTqLim
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr/s16_uRefDaxVltgLim
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr/s16_uRefQaxVltgLim
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr/u16_ADSensMechAgDvt
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/sss_SlwTaskOutHybr/u16_ampMaxVltgAmp
-
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/u16_ADSensMechAgDvt
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/u16_ampMaxVltgAmp
- ASW_AST/AST_SlwTask/vrs_SlwTaskOut/u16_phiPosSCDCalibElecAg
- ASW_AST/AST_SlwTask/vrs_SysSTM
- ASW_AST/AST_SlwTask/vrs_SysSTM/bol_flgSlepEn
- ASW_AST/AST_SlwTask/vrs_SysSTM/busOBD_FaltSt

- ASW_AST/AST_SlwTask/vrs_SysSTM/enm_stSysSt
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/Bus Selector
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/From
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/From2
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/From20
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/From27
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/From39
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/From4
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/From5
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/From6
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/From7
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/Goto
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/Goto15
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/Goto2
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/Goto3
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/Goto32
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/Goto4
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/Goto47
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/Goto5
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/Goto8
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/Logical Operator
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/Saturation2
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/bol_flgSlepEn
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/busOBD_FaltSt
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/enm_stSysSt
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/s16_nActElecSpd
- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/sfc_SysSTM

- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/sfc_SysSTM/ Demux

- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/sfc_SysSTM/ SFunction

- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/sfc_SysSTM/ Terminator

-

- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/sfc_SysSTM/bol_flgSlepEn

- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/sfc_SysSTM/enmSysStas

- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/sfc_SysSTM/s16_nActElecSpd

- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/sfc_SysSTM/u8_modeMCUOpMode

- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/sfc_SysSTM/u8_stMaxFaltLvl

- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/u8_modeMCUOpMode

- ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/u8_stMaxFaltLvl

- ASW_AST/AST_SlwTask/vrs_SysSTM/s16_nActElecSpd

- ASW_AST/AST_SlwTask/vrs_SysSTM/u8_FrzFrmSaveFlg

- ASW_AST/AST_SlwTask/vrs_SysSTM/u8_modeMCUOpMode

- ASW_AST/AST_SlwTask/vrs_SysSTM/u8_stMaxFaltLvl

- ASW_AST/busAST_SlwTaskIn

- ASW_AST/busAST_SlwTaskOut

∧ 更少

建议采取的操作

请用等效的离散模块替换上述模块。

_____

Check for prohibited sink blocks (必需)
通过

_____

Check scope of From and Goto blocks (必需)
标识 From 和 Goto 模块的不正确作用域设置。对于信号流，From 和 Goto 模块必须使用局部作用域。控制流可以使用全局作用域。

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通过

所有 From 和 Goto 模块都正确使用。

_____

⚠ Check usage of Switch blocks (必需)

标识未使用布尔信号开关条件(输入 2)的模块。

警告

以下 Switch 模块对开关条件使用了非布尔信号:

-
-
-
-
-
-
-
-
-
- ASW_AST/AST_SlwTask/vrs_ShOffFlgConf/rss_ShOffFlgConfSenl/Switch
-
-
-
-

∧ 更少

建议采取的操作

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将输入条件转换为布尔信号。

___

Check usage of Relational Operator blocks (必需)

标识其第一个(上)输入值连接到常量的 Relational Operator 模块。

通过

具有常量输入值的所有 Relational Operator 模块都正确配置。

___

Check orientation of Subsystem blocks (必需)

检查方向不正确的子系统。具有正确方向的模块的左边为输入，右边为输出。

通过

所有子系统模块都使用正确的方向。

___

Check fundamental logical and numerical operations

标识逻辑和数值运算的使用。

通过

未发现具有布尔输入的数值运算模块。
未发现具有非布尔输入的逻辑运算模块。

___

Check usage of merge blocks (必需)

na_0032: 标识 Simulink 中 Merge 模块的使用情形

通过

未发现输入不一致的 Merge 模块

___

Check logical expressions in If blocks (必需)

检查 If 模块中的逻辑表达式内是否使用了复杂的主表达式

通过

If 模块中的逻辑表达式很简单

___

Check usage of enumerated values (必需)

标识模型中使用的没有默认值设定的枚举类。

通过

未发现没有默认值设定的枚举类。

_____

Check usage of non-compliant blocks (必需)
标识根据配置未列在符合条件的模块列表中的模块。

通过

未发现违规。

Stateflow ⛔0 ❌0 ⚠0 ☑0 ✅21 📄0

_____

Check usage of exclusive and default states in state machines (必需)
标识不正确地使用或定义互斥和默认状态的 Stateflow 图和子状态。

_____

检查 Stateflow 图中是否存在互斥状态
标识具有孤单互斥(OR)状态的 Stateflow 图。

通过

Stateflow 图没有孤单互斥(OR)状态。

_____

检查 Stateflow 图中是否存在未定义的默认状态
标识未定义默认状态的 Stateflow 图。

通过

每个 Stateflow 图都定义一个默认状态。

_____

检查指定为默认状态的多个状态
在 Stateflow 层次结构的根级，应只将一个状态指定为默认状态。

通过

图的根级只指定了一个默认状态。

_____

检查具有孤单 OR 状态的子状态
配置为 OR 的状态应始终隶属于一组状态。

通过

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未检测到孤单 OR 状态。

_____

检查未定义默认状态的子状态

在 Stateflow 层次结构的每个级别，都应指定一个默认状态。

通过

所有子状态都指定了默认状态。

_____

检查定义了多个默认状态的子状态

在 Stateflow 层次结构的每个级别，应只将一个状态指定为默认状态。

通过

图的所有级别都只指定了一个默认状态。

_____

Check transition orientations in flow charts (必需)

标识 Stateflow 流程图中绘制不正确的转移。

通过

流程图中的所有 Stateflow 转移都正确绘制。

_____

Check entry formatting in State blocks in Stateflow charts

标识状态中 entry 动作(en)、during 动作(du)和 exit 动作(ex)条目之间缺少换行符。标识语句中分号(;)后缺少换行符。

通过

找到的所有状态入口语句都具有正确的格式。

_____

Check return value assignments in Stateflow graphical functions (必需)

标识 Stateflow 图中其返回值出现多次赋值的图形函数。

通过

未发现返回值出现多次赋值。

_____

Check default transition placement in Stateflow charts

标识没有默认转移或默认状态不是最顶部状态的所有状态分组。

通过

未发现 Stateflow 图中存在违反默认转移放置规范的 Stateflow 图和状态。

Check for Strong Data Typing with Simulink I/O (必需)
检查标注的输入和输出信号是否为强定型。

通过

不存在清除**对 Simulink I/O 使用强数据定型**的 Stateflow 图。

Check Stateflow data objects with local scope (必需)
标识在状态机级定义的 Stateflow 数据上设置的作用域值。

通过

状态机级的所有 Stateflow 数据都已按照规范进行定义。

Check usage of return values from Stateflow graphical functions (必需)
标识对条件表达式中使用的图形函数的调用。

通过

未发现包含对图形函数的调用的条件表达式。

Check for MATLAB expressions in Stateflow charts (必需)
标识不适合在 Stateflow 模块中用于代码生成的 MATLAB 表达式。

通过

未发现使用不适合代码生成的 MATLAB 表达式的 Stateflow 对象。

Check for pointers in Stateflow charts (必需)
标识对自定义代码变量执行的指针运算。
注意: 此检查仅适用于使用 C 语言作为动作语言的 Stateflow 图。

通过

未发现指针运算。

Check for event broadcasts in Stateflow charts (必需)
标识 Stateflow 中的无向事件广播。

通过
未发现无方向事件广播实例。

_____

Check transition actions in Stateflow charts (必需)
标识转移动作之间缺失换行符的情形。

通过
所有转移动作的格式都正确。

_____

Check for bitwise operations in Stateflow charts (必需)
标识 Stateflow 中运算符的使用。

通过
未发现运算符使用不正确的 Stateflow 模块。

_____

Check usage of unary minus operations in Stateflow charts (必需)
标识 Stateflow 对象中应用于无符号整数的一元减法运算。

通过
未发现应用于 Stateflow 对象中无符号整数的一元减法运算。

_____

Check for comparison operations in Stateflow charts (必需)
标识 Stateflow 对象中应用于不同数据类型的比较运算。

通过

未发现应用于不同数据类型的比较运算。

注意

非确定性情况: 模型顾问无法确定以下包含比较运算的表达式中的数据类型。

要使模型顾问能够确定数据类型，请考虑对下面列出的表达式进行显式类型转换。

比较运算	状态或转移
[**u16_ctSysStInitWait Cnt >= Gu16_ctSysStInitWait Cnt_C** &&...	ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_CRRC/sfc_SysSTM
u8_modeMCUOpMod e ==uint8(0)]	ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_CRRC/sfc_SysSTM
(**abs(s16_nActElecSpd) < abs(Gs16_nShOffActEl ecSpd_C)**)]	ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/sfc_SysSTM
(**abs(s16_nActElecSpd) < abs(Gs16_nShOffActEl ecSpd_C)**)]	ASW_AST/AST_SlwTask/vrs_SysSTM/rss_SysSTM_JEELY/sfc_SysSTM
[**u16_ctAlgnStEndCnt >= Gu16_ctAlgnStEndCnt _C**]	ASW_AST/AST_SlwTask/vrs_RefElecSpdCalc/rss_RefElecSpdCalcSenl/rss_Ref ElecSpdCalc/sfc_OpnLpRefElecSpdConf

_____

Check usage of floating-point expressions in Stateflow charts (必需)
标识在涉及浮点变量或常量的表达式中的相等性或不相等性运算(==、~=、!=)。

通过

未发现任何相等或不相等运算表达式的至少一侧是浮点变量或常量。

Check for names of Stateflow ports and associated signals (必需)

标识 Stateflow 端口和相关联信号之间的名称不匹配情况。

通过

Stateflow 端口和相关联信号之间不存在名称不匹配情况。

Check nested states in Stateflow charts (必需)
检查 Stateflow 图中状态的嵌套级别。

通过

未发现深度嵌套的状态。

Check use of Simulink in Stateflow charts (必需)
na_0039: Chart 模块中 Simulink 函数的限制

检查在 Stateflow 中使用的 Simulink 函数内嵌套的 Stateflow 图。

通过

在 Stateflow 中使用的 Simulink 函数中未发现嵌套的 Stateflow 图。

Check number of Stateflow states per container (必需)
标识可查看状态数超过阈值的 Stateflow 容器。

通过

未发现状态数超过最大限制的 Stateflow 容器

Check reuse of Variables within a Stateflow scope (必需)
jc_0491: Stateflow 数据的重用
一个 Stateflow 变量在单个 Stateflow 作用域内不应有多个含义(用法)。

通过

在给定 Stateflow 作用域内，所有变量都只有一种用法。

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📁 *MATLAB Functions*   ⛔0 ❌0 ⚠0 🗳0 ✅7 ▤0

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📋 Check input and output settings of MATLAB Functions (必需)

标识具有继承的复/实性、数据类型或大小属性的输入、输出或参数的 MATLAB Function。

通过
在模型或子系统中未发现 MATLAB Function。

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📋 Check MATLAB Function metrics (必需)

标识违反复杂度限制的 MATLAB Function。

通过

未发现违反度量的 MATLAB Function。

为模型或子系统确定了以下度量。

图例:

- LoC : 代码行总数
- ELoC : 有效代码行数
- CLoC : 代码注释行数
- DC : 注释密度
- CYC : 圈复杂度

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名称	值
每个函数的最大有效代码行数	60
最小注释密度	0.2
每个函数的最大圈复杂度	15

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Check MATLAB code for global variables (必需)

检查 MATLAB 代码中的全局变量

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检查在 MATLAB Function 模块中使用的 MATLAB 代码中是否存在全局变量

通过
未发现 MATLAB Function 模块

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检查 Stateflow 图中定义的 MATLAB 函数中是否存在全局变量

通过
未发现 Stateflow 图中定义的 MATLAB 函数

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检查调用的 MATLAB 函数中是否存在全局变量

通过
未发现外部 MATLAB 函数

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Check the number of function calls in MATLAB Function blocks (必需)
检查 MATLAB Function 模块中的函数调用次数是否小于 3。

通过

MATLAB Function 模块中的函数调用次数小于 3。

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✅ Check usage of restricted variable names

标识 MATLAB Function 模块中保留关键字的使用情形。

通过

变量名称与保留关键字没有冲突

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✅ Check usage of character vector inside MATLAB Function block

标识 MATLAB Function 模块中字符串的使用情形。

通过

在 MATLAB Function 模块中未发现字符向量

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✅ Check usage of recommended patterns for Switch/Case statements

标识 Switch/Case 语句中非常变量的使用情形。

通过

非常变量不用作 Switch/Case 参数

📁 Modeling Standards for MISRA C:2012 ⛔0 ❌0 ⚠0 ☑0 ✅11 📄0

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📑✅ *Check for blocks not recommended for C/C++ production code deployment (必需)*
通过

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📑✅ *Check for blocks not recommended for MISRA C:2012 (必需)*
通过

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📑✅ *Check for unsupported block names (必需)*
通过

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📑✅ *Check usage of Assignment blocks (必需)*
通过

Check for switch case expressions without a default case (必需)
标识没有默认 case 的 switch case 表达式。

通过

所有 switch case 表达式都有默认 case。

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Check for missing error ports in AUTOSAR receiver interfaces (必需)
标识没有匹配的错误端口的 AUTOSAR 接收方接口端口。

通过

模型未配置为 AUTOSAR 目标。

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Check for bitwise operations on signed integers (必需)
标识有符号整数的按位运算。

通过

未发现有符号整数的按位运算。

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Check for recursive function calls (必需)
标识递归函数调用。

通过

未发现递归函数调用。

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Check for equality and inequality operations on floating-point values (必需)
标识浮点值的相等和不相等运算。

通过

未发现浮点值的相等或不相等运算。

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Check for missing const qualifiers in model functions (必需)
标识模型函数中缺失的常量限定符。

通过

模型未使用自定义的模型函数。

Check bus object names that are used as bus element names (必需)

标识用作总线元素名称的总线对象名称。

通过

未发现用作总线元素名称的总线对象名称。