









UL62368-1 AS/NZS62368-1 TPTC004 IEC62368-

### Feature

- · Width only 52.5mm (3SU)
- 4:1 ultra wide input range
- -40~+85°C wide working temperature
- No minimum load required
- DC output adjustable (±10%)
- Cooling by free air convection
- · Can be installed on DIN rail TS-35/7.5 or 15
- Protections: Short circuit / Overload / Over voltage / Input reverse polarity / Input under voltage protection
- 4KVdc I/O isolation(Reinforced isolation)
- 3 years warranty











## Applications

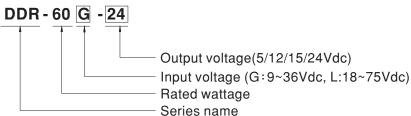
- · Industrial control system
- · Semi-conductor fabrication equipment
- Factory automation
- · Electro-mechanical
- · Wireless network
- · Telecom or datacom system

# Description

DDR-60 series is a 60W DIN Rail type DC-DC converter with main features including DIN rail-type easy installation, ultra slim width (52.5mm), 4:1 ultra wide input voltage,  $-40 \sim +85^{\circ}$ C wide operating temperature, 4KVdc I/O isolation, adjustable output voltage (± 10%) and full protective functions...etc.

This series has two input options:  $9\sim36V/18\sim75V$  and various output options: 5V/12V/15V/24V and can be used for industrial control, security control, communication system and other fields. Suitable applications are DC buck/boost regulator, increasing system insulation level and voltage drop compensation along cable...etc.







MODEL		DDR-60G-5	DDR-60G-12	DDR-60G-15	DDR-60G-24	DDR-60L-5	DDR-60L-12	DDR-60L-15	DDR-60L-2	
	DC VOLTAGE	5V	12V	15V	24V	5V	12V	15V	24V	
ОИТРИТ	RATED CURRENT	10.8A	5A	4A	2.5A	12A	5A	4A	2.5A	
	CURRENT RANGE	0 ~ 10.8A	0 ~ 5A	0 ~ 4A	0 ~ 2.5A	0 ~ 12A	0 ~ 5A	0 ~ 4A	0 ~ 2.5A	
	RATED POWER	54W	60W	60W	60W	60W	60W	60W	60W	
	RIPPLE & NOISE (max.) Note.2	60mVp-p	75mVp-p	75mVp-p	100mVp-p	60mVp-p	75mVp-p	75mVp-p	100mVp-p	
	VOLTAGE ADJ. RANGE	4.5 ~ 5.5V	9 ~ 13.2V	13.5 ~ 16.5V	21.6 ~ 28V	4.5 ~ 5.5V	9 ~ 13.2V	13.5 ~ 16.5V	21.6 ~ 28V	
	VOLTAGE TOLERANCE Note.3		±2.0%	±2.0%	±2.0%	±2.0%	±2.0%	±2.0%	±2.0%	
	LINE REGULATION	±0.5%	±0.5%	±0.5%	±0.5%	±0.5%	±0.5%	±0.5%	±0.5%	
	LOAD REGULATION	±1.5%	±0.5%	±0.5%	±0.5%	±1.5%	±0.5%	±0.5%	±0.5%	
	SETUP, RISE TIME	120ms, 85ms a	full load							
	HOLD UP TIME (Typ.)	G-type: 5ms@24Vdc input				L-type: 10ms@48Vdc input				
	EXTERNAL CAPACITANCE LOAD (Max.)	6800 µF	4700 µF	3300 µF	2200 µF	6800 μF	4700 µF	3300 µF	2200 µF	
	VOLTAGE RANGE Note.4	9 ~ 36Vdc				18 ~ 75Vdc				
	EFFICIENCY (Typ.)	87.5%	91%	91%	91%	87.5%	91%	92%	92%	
NPUT	DC CURRENT (Typ.)	3A /24Vdc	3A /24Vdc				1.5A/48Vdc			
	INRUSH CURRENT (Typ.)	20A /24Vdc				20A/48Vdc				
PROTECTION	(717	105 ~ 135% rated output power								
	OVERLOAD				overs automatical	ly after fault co	ndition is removed			
	OVER VOLTAGE	5.75 ~ 7V	13.8 ~ 16.2V	17.25 ~ 20.25V	28.8 ~ 34V	5.75 ~ 7V	13.8 ~ 16.2V	17.25 ~ 20.25V	28.8 ~ 34V	
						10	10.0 10.24	17.20 20.201	20.0 341	
	REVERSE POLARITY	Protection type: Shut down o/p voltage, re-power on to recover  By internal MOSFET, no damage, recovers automatically after fault condition removed								
		24Vin (G-type):Power ON≥9V , OFF≤8.5V								
	UNDER VOLTAGE LOCKOUT	48Vin (L-type):Power ON≥18V , OFF≤17V								
	WORKING TEMP.	-40 ~ +85°C (Refer to "Derating Curve")								
	WORKING TEMP. WORKING HUMIDITY	5 ~ 95% RH non-condensing								
NVIDONMENT		3 ~ 95% KH non-condensing -40 ~ +85°C, 5 ~ 95% RH non-condensing								
ENVIRONMENT	STORAGE TEMP., HUMIDITY									
	TEMP. COEFFICIENT	±0.03%/°C (0~60°C)  Component 10~500Hz, 2G 10min /1cycle, 60min, each along X, V, 7 avec; Mounting; Compliance to IEC60068, 2, 6								
	VIBRATION ALTITUDE	Component: 10 ~ 500Hz, 2G 10min./1cycle, 60min. each along X, Y, Z axes; Mounting: Compliance to IEC60068-2-6								
	OPERATING ALTITUDE	5000 meters								
	SAFETY STANDARDS	IEC 62368-1, UL 62368-1, AS/NZS 62368-1, EAC TP TC 004 approved								
	WITHSTAND VOLTAGE	I/P-O/P:4KVdc I/P-O/P>100M Ohms / 500Vdc / 25°C / 70% RH								
	ISOLATION RESISTANCE		Onms / 500Vdc			1	ant Lavel / Nata			
	EMC EMISSION	Parameter			tandard		Test Level / Note			
SAFETY &		Conducted					Class A			
		Radiated			S EN/EN55032		Class A for 1m I/O cable , Class B for 30cm I/O ca			
MC		Voltage Flicker BS EN/EN61000-3-3								
(Note 5)		BS EN/EN55024 , BS EN/EN61000-6-2(BS EN/EN50082-2)								
	EMC IMMUNITY	Parameter			tandard		Test Level / Note			
		ESD			3S EN/EN61000-4-2		Level 3, 8KV air ; Level 3, 6KV contact; criteria A			
		Radiated		В	S EN/EN61000-4-	3 L	Level 3, 10V/m ; criteria A			
		EFT / Burst		В	S EN/EN61000-4-	4 L	Level 3, 2KV ; criteria A			
		Surge		В	3S EN/EN61000-4-5		Level 3, 1KV/Line-Line; criteria A			
		Conducted		В	S EN/EN61000-4-6		Level 3, 10V; criteria A			
		Magnetic Field BS EN/EN61000-4-8 Level 4, 30A/m; c					iteria A			
	MTBF	611K hrs min. MIL-HDBK-217F ( $25^{\circ}$ C)								
THERS	DIMENSION	52.5*90*54.5mm (W*H*D)								
	PACKING	216g; 60pcs/14Kg/0.97CUFT								
NOTE		216g; 60pcs/1 lly mentioned a ed at 20MHz of tolerance, line nder low input v	4Kg/0.97CUFT re measured at bandwidth by u regulation and le voltage. Please of	sing a 12" twist toad regulation. Theck the dera	ted pair-wire term	ninated with a or	0.1 μf & 47 μf pa	rallel capacitor.		
	directives. For guidance on (as available on http://www. 6. The ambient temperature d	how to perform meanwell.com erating of 3.5°C	these EMC test	sts, please refe	r to "EMI testing o	of component posts	power supplies."	·		



G-type: 9 L-type: 18

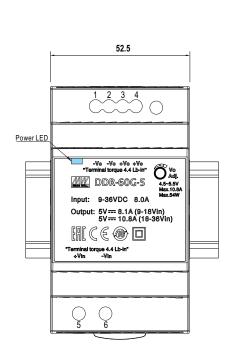
#### ■ Block Diagram fosc: 130KHz RECTIFIERS REVERSE POWER EMI -O +Vo POLARITY **FILTER** SWITCHING -O -Vo FILTER DETECTION O.L.P. UNDER **PWM** CIRCUIT CONTROL VOLTAGE LOCKOUT O.V.P. ■ Derating Curve DDR-60G-12/15/24 **DDR-60G-5** 100 100 18Vdc input 85 80 75 > 18Vdc input 70 60 ≤18Vdc input (%) <sub>35</sub> LOAD (%) 50 ≤18Vdc input 35 -40 -30 -20 -10 70 80 85 -40 -30 -20 -10 10 20 30 40 10 20 30 40 0 70 80 85 0 (VERTICAL) (VERTICAL) **AMBIENT TEMPERATURE (°C)** AMBIENT TEMPERATURE (°C) DDR-60L-12/15/24 **DDR-60L-5** 100 100 90 80 > 36Vdc input -70 70 60 ≤36Vdc input (%) 50 35 50 LOAD (%) 35 70 80 85 (VERTICAL) -40 -30 -20 -10 10 20 30 40 50 70 80 85 -40 -30 -20 -10 0 10 20 30 40 50 (VERTICAL) AMBIENT TEMPERATURE (°C) AMBIENT TEMPERATURE (°C) ■ Output derating VS input voltage 100 90 85 75 DDR-60L-12/15/24 60 DDR-60L-5 DDR-60G-12/15/24 LOAD (%) DDR-60G-5 40

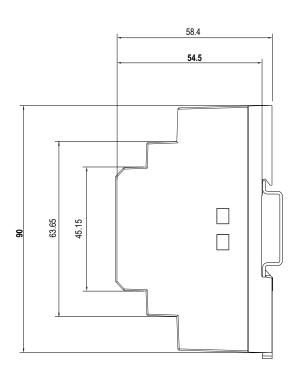
36 75

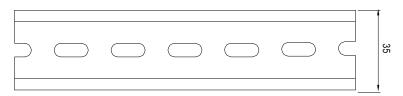
**INPUT VOLTAGE** 



### ■ Mechanical Specification







ADMISSIBLE DIN-RAIL:TS35/7.5 OR TS35/15

Pin No.	Assignment		
1,2	DC output	-Vo	
3,4	DC output	+Vo	
5	DC input	+Vin	
6	DC input	-Vin	

### **■** Installation Manual

Please refer to : http://www.meanwell.com/manual.html