Register file 32 bit wide Register 32 JUST registes 18to B4 bit arch are 64 bit wide Each 32 bit entity stored her is a word Question How many addresses can erode prepresent 232 = 210 x 210 x 2 x 2 = 1K x1K x1K x 4 Then your address space is to GRB

If you assume each address to

refer to a 4-bite entity

& you have 4 billion unique addresses.