



bneq

se 'slt' → (set on less than) not itself a branch instruction
 (*) sets a certain value based on a comparison of 2 registers.

eg: `slt $t0, $f1, $f2`
 Set \$t0 to 1 if $\$f1 < \$f2$

can be used with a follow up branch instruction which checks result of slt instruction, & proceeds accordingly

2nd kind: Unconditional branch:
 jr → jump to address in a register.