Instruction Formats (Have Instr. format slide)

✗ 2 Broad classes of instructions

R-type

✗ last 6 bits denotes the variation in adding (adding a byte, 2 bytes, or a word)

✗ (the 5 bits shift is used for operation instructions like,

  sll
  srl

Ⓧ by which/where bits are shifted to the left or right.

Ⓧ These bits tell us how much this shift is.

I-type

  e.g. immediate,
    lw, sw

  Have Fii   5 bits → 1st reg. operand
             5 bits → next reg. operand
            16 bits → stores the
                      offsets.