

32 bits 32 b

4 Bytes 4B

usually

> 1 line of C will converted to multiple lines of ~~it instructions~~ assembly instructions.

Skip slide ~~17~~ on discussion of IS design
slide ~~18~~ - 21

> There may be different ways
you may generate compiler
assembly code,
some may be better than
others.

(use the term
temporary location)

Slide #5, #6

[No discussion on floating point
operations.]

$$f = (g + h) - (i + j)$$

Compiler is
more likely
to generate
the code on

the left (although temporaries are used)

[Due the design of the PL & compiler but
it all depends]