P1.3 TEST PLAN

LED AND PB CONSOLE USING TCP (TELNET)

VERSION I

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1 - TPP VERSION HISTORY

Version #	Implemented By	Revision Date	Approved By	Approval Date	Reason	Mark
1	Richard Constantine	10/03/2017				

2 - INTRODUCTION

2.1 PURPOSE OF THE TEST PLAN TEMPLATE DOCUMENT

The purpose of this test plan document is to test the TCP/IP capabilities of the MX7cK development board by connecting to the newly added TCP server via Telnet. This can be accomplished by connecting the board to another host computer via the Ethernet connector, editing the provided ETH795.h file with the MAC and IP addresses with the information printed on the MX7 device and connecting to port 7777 using the Telnet client. The server should also allow the user to control the LEDs and read the pushbuttons remotely over the Telnet client.

3 - LED & PB CONSOLE OVER TCP

3.1 TEST PLAN AND CASES

Item to Test	Test Description	Test Date	Responsibility
CONSOLE CONTROL USING TCP CONNECTION VIA TELNET	Using the console provided by Telnet, the user should be able to connect to the MX7 board via a TCP connection and to supply commands that allows for control of the LEDs and reading of the PBs remotely.	Oct 3, 2017	Richard Constantine

3.2 TEST PROCEDURE

Instr	uction	P/ F
1.	Ensure the PC being used has an up-to-date version of Windows (Windows 7 or later) along with the MPLAB X IDE (currently version is 4.01), and XC32 compiler (currently version 1.44), and the PIC32 Legacy Peripheral Libraries (ensuring to install the libraries within the xc32/v1.44 folder). These tools can be found at http://www.microchip.com/mplab/ along with more detailed instructions in the reference material [1].	
2.	Also, ensure your computer is has the Telnet client software downloaded, installed and activated — this step may depend on your Version of Windows. Try searching the local host PC for the telnet software. If the Telnet console cannot be located, try enabling it by browsing to Start > Control Panel, Programs and Features Turn Windows features on or off. In the Windows Features dialog box, check the Telnet Client and click OK.	
3.	Connect the J3 Power Select (DBG jumper just above the Ethernet port) of the MX7cK board if it is not already connected. Then connect the MX7cK board via the J15 Debug USB connector (located just below the Ethernet connector) via the provided USB OTG cable. Power on the device using the power	

	switch in the lower left corner – LEDs should light up which confirms that the device is receiving power.	
4.	IF ALREADY COMPLETE, MOVE TO STEP 4. Go to the Google OneDrive URL sent via email. This should take you a folder called Richard Constantine - 7686561 - ECE 3740 - Assignment 2. Download and extract all files within Constantine_Richard_P1.3.zip, and place its contents in a directory called C:\Users\ <yourusername>\ECE3740\MPLABXProjects\Constantine_Richard_P1\P1.3.</yourusername>	
5.	Start the MPLAB X IDE by clicking on MPLAB X in the taskbar (as explained by Step 1 of [1]) or find the MPLAB X IDE by going to Windows File Explorer, browsing to the folder or drive where MPLABX is installed, using Windows search (in the top right corner of the window) for "IDE" then scrolling until mplab.exe can be located and opened.	
6.	Open the XC32-PIC32_ETH_SK_ETH795 project by selecting File->Open Project within the MPLAB X IDE (top left corner of the window), and then browsing to the newly extracted directory: C:\Users\ <yourusername>\ECE3740\MPLABXProjects\Constantine_Richard_PI\P1.3\v4\TCPIPStack\TCPIP\Demo App\ then selecting and opening the project XC32-PIC32_ETH_SK_ETH795.X.</yourusername>	
7.	If no files are displayed beneath the XC32-PIC32_ETH_SK_ETH795 project in the Projects tab (top left of the window), click the '+' icon to the left of the XC32-PIC32_ETH_SK_ETH795 project. This should display the files located in that folder such as Header Files, Important Files, etc	
8.	Click the '+' icon to left of the Header Files (located in the projects tab of the MPLABX IDE) which should also display the files such as MainDemo.h, TCPIP ETH795.h, etc., located within that folder. Open the file TCPIP ETH795.h file within the Header Files folder by right-clicking the file and selecting 'Open' – this should reveal the C code comprising that file.	
9.	Within the TCPIP ETH795.h code, locate 'Application Options' (located at line 57 of the code). Ensure that every define statement located under 'Application Options' is commented out EXCEPT lines 67, 74, 84 and 96: • #define STACK_USE_ICMP_SERVER // Ping transmission capability • #define STACK_USE_DHCP_SERVER // Single host DHCP server • #define STACK_USE_ANNOUNCE // Microchip Embedded Ethernet Device Discoverer • #define STACK_USE_TCP_LED_PB_CONSOLE_SERVER //***ADDED***	
10.	Find the MAC address printed on the back on the back your (the tester's) MX7cK board - the MAC address consists of 12 hexadecimal digits in the form UU-VV-WW-XX-YY-ZZ. Record this for the later steps.	
11.	Within the TCPIP ETH795.h code, locate 'Network Addressing Options' (at line 144). The MAC and IP information is currently configured for my (Richard Constantine's) device which has a MAC address of 00-18-3E-01-AD-37. These settings should be altered to match the tester's MAC info. First, find the host name on line 155: • #define MY_DEFAULT_HOST_NAME "mx37" And change it to read: • #define MY_DEFAULT_HOST_NAME "mxZZ"	
	Where ZZ represents the last 2 digits of the MAC address printed on the back of your (the tester's) board. For example, using the previous MAC address of OO-18-3E-O1-AD-37, we find ZZ = 37. Follow these instructions using the MAC address printed on the back of your board.	

```
12.
       Still within the TCPIP ETH795.h code, ensure that lines 164-169 match your (the tester) device's MAC
       address by editing lines 164-169 to match your MAC address. Using a MAC address of UU-VV-WW-XX-
       YY-ZZ, these lines would look like:
                #define MY DEFAULT MAC BYTE1
                                                       (OxUU) // MAC address printed on back of board
                #define MY DEFAULT MAC BYTE2
                                                       (0xVV) //
                #define MY DEFAULT MAC BYTE3
                                                       (\square_X WW) //
                #define MY DEFAULT MAC BYTE4
                                                       (\square_XXX) //
                #define MY DEFAULT MAC BYTES
                                                       (\square_X YY) //
                #define MY DEFAULT MAC BYTE6
                                                       (0xZZ) //
       For the previous example MAC address 00-18-3E-01-AD-37, these lines would look like:
                #define MY DEFAULT MAC BYTE1
                                                       (0x00) // MAC address printed on back of board
                #define MY DEFAULT MAC BYTE2
                                                       (0x18) //
                #define MY DEFAULT MAC BYTE3
                                                       (0x3E) //
                #define MY DEFAULT MAC BYTE4
                                                       (0x01) //
                #define MY DEFAULT MAC BYTES
                                                       (OxAD) //
                #define MY DEFAULT MAC BYTE6
                                                       (0x37) //
       Do cha
       The IP address should also be updated to reflect the new MAC address. Confirm that lines 171-173 of
13.
       the TCPIP ETH795.h code match the following:
                #define MY DEFAULT IP ADDR BYTE1
                                                         (192ul)
                #define MY DEFAULT IP ADDR BYTE2
                                                         (168<sub>u</sub>l)
                #define MY DEFAULT IP ADDR BYTE3
                                                         (lul)
       Find the line 174 and change it read:

    #define MY DEFAULT IP ADDR BYTE4

                                                         (0xZZuI)
      Where ZZ represent the last 2 digits of the your (the tester) device's MAC address – same idea as
      step 11.
14.
       Confirm that the default mask located on lines 176-179 of the TCPIP ETH795.h code are defined as:
                #define MY DEFAULT MASK BYTE1
                                                        (255ul)
                #define MY DEFAULT MASK BYTE2
                                                        (255ul)
                #define MY DEFAULT MASK BYTE3
                                                        (255<sub>ul</sub>)
                #define MY DEFAULT MASK BYTE4
                                                        Next, find the default gate and default primary DNS located on lines 181-189:
15.
                #define MY DEFAULT GATE BYTE1
                                                       (192ul)
                #define MY DEFAULT GATE BYTE2
                                                       (168<sub>u</sub>l)
                #define MY DEFAULT GATE BYTE3
                                                       (lul)
                #define MY DEFAULT GATE BYTE4
                                                       (55ul)
                #define MY DEFAULT PRIMARY DNS BYTE1
                                                                         (192ul)
                #define MY DEFAULT PRIMARY DNS BYTE2
                                                                         (168ul)
                #define MY DEFAULT PRIMARY DNS BYTE3
                                                                         (lul)
                #define MY DEFAULT PRIMARY DNS BYTE4
                                                                         (55<sub>ul</sub>)
       Using the tester's MAC address UU-VV-WW-XX-YY-ZZ, convert the last 2 digits of the MAC address
       (ZZ) of your (the tester's) local device from hexadecimal to decimal. Say this decimal number is
       represented by \Phi, then the lines (181-189) should then be changed to the following:
                #define MY DEFAULT GATE BYTE1
                                                       (192uI)
```

	 #define MY_DEFAULT_GATE_BYTE2 (168ul) #define MY_DEFAULT_GATE_BYTE3 (1ul) #define MY_DEFAULT_GATE_BYTE4 (Φ ul) #define MY_DEFAULT_PRIMARY_DNS_BYTE1 (192ul) #define MY_DEFAULT_PRIMARY_DNS_BYTE2 (168ul) #define MY_DEFAULT_PRIMARY_DNS_BYTE3 (1ul) #define MY_DEFAULT_PRIMARY_DNS_BYTE4 (Φ ul) In the previous example, using a MAC address of OD-18-3E-O1-AD-37: (37)_{Hex} = (55)_{Decimal} Therefore Φ = 55 and the effective IP address is given by 192.168.1.55. Now, calculate Φ for your local device and update lines 184 and 189 using this value. 				
16.	To test the device, one end of an Ethernet cable should be connected to the MX7cK board via the Ethernet Port (just above the J15 Debug USB connector), while the other end of the cable should be connected to the PC running Windows and the MPLAB X IDE.				
17.	Select the XC32-PIC32_ETH_SK_ETH795 project by right-clicking on the XC32-PIC32_ETH_SK_ETH795 project name in the Projects tab of the MPLAB X IDE and build the project by selecting Build. After many seconds, the message "BUILD SUCCESSFUL" should display in the Output window.				
18.	Run the project by right-clicking on the XC32-PIC32_ETH_SK_ETH795 project name in the Projects tab of the MPLAB X IDE and selecting Run. After many seconds, the message "Programming/Verify complete" should display in the output window. LED1 (on the MX7cK board) should be blinking which confirms the program is running.				
19.	Next, open Windows Command Prompt on the host PC running Windows by clicking Start and using the OS to search for 'cmd'. Select/open the Command Prompt from this search window.				
20.	Using Windows Command Prompt, open a connection by entering the command "telnet 192.168.1. Φ 7777", where Φ is the last two digits of the tester's IP address. This should display the LED SERVER MENU which provides a list of possible commands. Typing "1" through "8" allows for manipulation of the LEDs according to their description (i.e. pressing "1" will turn LED1 on, pressing "2" will turn LED1 off, etc.). Typing "a", "s", or "d" reads the status of pushbuttons 1, 2 and 3 respectively. Confirm functionality of all listed commands.				

4 - TEST PLAN TEMPLATE APPROVAL

Required Signatures:

The undersigned acknowledge they have reviewed the P1.1.1 Test Plan Template document and agree with the approach it presents. Any changes to this Requirements Definition will be coordinated with and approved by the undersigned or their designated representatives.

• TA - Kaiser	Nahiyan		
Signature:		Date:	
Print Name:		-	
Title:		-	
Role:		-	

REFERENCES

