**TOSHIBA** TD62706P-H/FA-H

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TD62706P-H, TD62706FA-H

# 6ch HIGH-VOLTAGE SOURCE-CURRENT DRIVER

The TD62706P-H is and TD62706FA-H are comprised of six source current Transistor Array.

This driver is specifically designed for fluorescent display applications.

For proper operation, the substrate (SUB) must be connected to the most negative voltage.

### **FEATURES**

Package Type

P-H Type : DIP16PIN FA-H Type: SSOP16PIN

High Output Voltage

 $: V_{CC} - V_{OUT} = 60V (MIN)$ 

Output Current (Single Output)

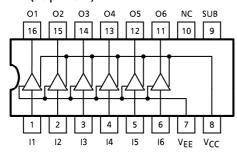
:  $I_{OUT} = -50mA$  (MAX)

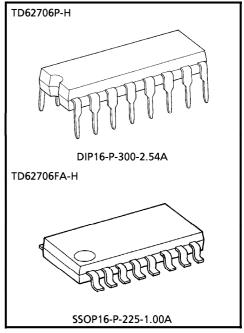
Input Compatible with Various Types of Logic

:  $R_{IN} = 10k\Omega$ 

Wide operating temperature range. :  $T_{opr} = -40 \sim 105$ °C

### PIN ASSIGNMENT (Top view)





Weight

DIP16-P-300-2.54A : 1.11g (Typ.) SSOP16-P-225-1.00A : 0.14g (Typ.)

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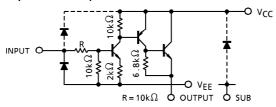
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# **SCHEMATICS** (Each driver)



(Note) The input and output parasitic diodes cannot be used as clamp diodes.

# **MAXIMUM RATINGS** (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Power Supply Voltage	V <sub>CC</sub> -V <sub>EE</sub>	30	V	
Fower supply voltage	V <sub>CC</sub> -V <sub>SUB</sub>	60	V	
Output Voltage	V <sub>CC</sub> -V <sub>OUT</sub>	60	V	
Input Voltage	V <sub>IN</sub> -V <sub>EE</sub>	VCC-VEE	V	
Output Current	IOUT	<b>–</b> 50	mA / ch	
Input Current	IN	± 10	mA	
Bower Dissipation	P <sub>D</sub> (Note 1)	1.0	w	
Power Dissipation	P <sub>D</sub> (Note 2)	0.78	] **	
Operating Temperature	T <sub>opr</sub>	<b>- 40∼105</b>	°C	
Storage Temperature	T <sub>stg</sub>	<b>- 55∼150</b>	°C	

(Note 1) TD62706P-H : Delated above 25°C in the proportion of 8.0mw/°C. (Note 2) TD62706FA-H : On Glass Epoxy PCB (  $50.\times50\times1.6$ mm Cu 40% ).

Delated above 25°C in the proportion of 6.2mw/°C.

# **RECOMMENDED OPERATING CONDITIONS** ( $Ta = -40 \sim 85$ °C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Power Supply Voltage	Vcc	V <sub>EE</sub> = 0V	4.5		25	V
Tower Supply Voltage	$V_{SUB}$	V <sub>CC</sub> = 0V	VOUT		<b>–</b> 55	
Output Voltage	Vout	V <sub>CC</sub> = 0V	0		<b>–</b> 55	<
Output Current	IOUT	_	0	_	- 40	mA / ch
Input Voltage	$V_{IN}$	$V_{EE} = 0V, V_{CC} = 25V$	0	_	7	٧
Device Dissination	D_	TD62706P-H	_	_	0.52	w
Power Dissipation	$P_{D}$	TD62706FA-H when mounting	_	_	0.4	] **

# **ELECTRICAL CHARACTERISTICS** (Ta = 25°C)

CHARACT	ERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Input	"H" Level	VIH	1	V <sub>EE</sub> = 0V	2.2	_	_	V
Voltage	"L" Level	V <sub>IL</sub>	'	V <sub>EE</sub> = 0V	_	_	0.8	ľ
Input	"H" Level	lН	2	$V_{EE} = 0V, V_{IN} = 2.4V$	_	0.12	0.18	mΑ
Current	"L" Level	IIL		$V_{EE} = V_{IN} = 0V, V_{CC} = 25V$	_	_	± 1	μΑ
Output Leak	age	lasy	3	$V_{EE} = 0V$ , $V_{CC} = 25V$			- 100	
Current		ICEX	3	VIN = VIL MAX., VOUT = -30V		_	- 100	$\mu$ A
Collector-Emi	tter	Var ( ı)	4	$V_{EE} = 0V$ , $V_{CC} = V_{CC}$ MIN.			Vcc	٧
Saturation V	oltage	VCE (sat)	-	V <sub>IN</sub> = V <sub>IH</sub> M <sub>IN</sub> ., I <sub>OUT</sub> = -40mA		_	- 2.5	٧
Supply Curre	nt	lee (ON)	1	$V_{EE} = 0V$ , $V_{CC} = 25V$			25	mΑ
(Output On)		ICC (ON)	<b>'</b>	VIN = VIH MAX., OUTPUT = OPEN			23	IIIA
Turn-On Dela	эу	ton	5	$R_L = 1.4k\Omega$	_	0.2	_	,,,,
Turn-Off Del	ay	tOFF	]	C <sub>L</sub> = 15pF	_	1.5	_	$\mu$ s

# **RECOMMENDED OPERATING CONDITION** (Ta = $-40 \sim 105$ °C)

CHARACTERISTIC	SYMBOL	TEST CONDITION		MIN	TYP.	MAX	UNIT
Power Supply Voltage	Vcc	V <sub>EE</sub> = 0V		4.5	_	25	V
L Supply Voltage	$V_{SUB}$	V <sub>CC</sub> = 0V		VOUT		- 55	\ \ \
Output Voltage	Vout	V <sub>CC</sub> = 0V		0	_	- 55	V
	IOUT	2 Circuits	TD62706P-H	0	_	- 40	
		Parallel ON	TD62706FA-H	0	_	- 40	
Output Current		4 Circuits	TD62706P-H	0	_	- 37	  m
Output Current		Parallel ON	TD62706FA-H	0	_	- 30	mA / ch
		6 Circuits	TD62706P-H	0	_	<b>–</b> 15	
		Parallel ON	TD62706FA-H	0	_	- 25	
Input Voltage	VIN	$V_{EE} = 0V$ , $V_{CC} = 25V$		0	_	7	V
Bower Dissipation	P.S.	TD62706P-H		_	_	0.36	w
Power Dissipation	PD	TD62706FA-H when mounting		_		0.28	] <b>vv</b>

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# **ELECTRICAL CHARACTERISTICS** (Ta = 105°C) : TD62706P-H

CHARAC	TERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Input	"H" Level	V <sub>IH</sub>	1	V <sub>EE</sub> = 0V	1.8	_	_	V
Voltage	"L" Level	VIL	] '	V <sub>EE</sub> = 0V	_	_	0.6	v
Input	"H" Level	ΙΗ	2	$V_{EE} = 0V, V_{IN} = 2.4V$	_	0.12	0.18	mΑ
Current	"L" Level	IIL	]	$V_{EE} = V_{IN} = 0V, V_{CC} = 25V$	_	_	± 10	μΑ
Output Leal	kage	1	3	$V_{EE} = 0V$ , $V_{CC} = 25V$			- 300	
Current		ICEX	3	V <sub>IN</sub> = V <sub>IL</sub> MAX., V <sub>OUT</sub> = -30V	-	_	- 300	$\mu$ A
Collector-Em	itter	\/a= / · ·>	4	V <sub>EE</sub> = 0V, V <sub>CC</sub> = V <sub>CC</sub> MIN.			Vcc	V
Saturation V	oltage/	VCE (sat)	4	VIN = VIH MIN., IOUT = -40mA	_	_	- 2.5	V
Supply Curre	ent	laa (23.1)	1	$V_{EE} = 0V$ , $V_{CC} = 25V$			25	mΑ
(Output On)	1	ICC (ON)		VIN = VIH MAX., OUTPUT = OPEN	_	_	25	ША
Turn-On Del	ay	ton	5	$R_L = 1.4k\Omega$ , Single circuit	_	0.4		,,,5
Turn-Off De	lay	tOFF	]	C <sub>L</sub> = 15pF	_	3.0		$\mu$ s

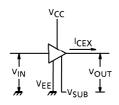
# **ELECTRICAL CHARACTERISTICS** (Ta = 105°C) : TD62706FA-H

CHARACT	ERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Input	"H" Level	V <sub>IH</sub>	1	V <sub>EE</sub> = 0V	1.8	_	_	V
Voltage	"L" Level	V <sub>IL</sub>	'	V <sub>EE</sub> = 0V	_	_	0.6	v
Input	"H" Level	ΊΗ	2	$V_{EE} = 0V, V_{IN} = 2.4V$	_	0.12	0.18	mΑ
Current	"L" Level	IJL		$V_{EE} = V_{IN} = 0V$ , $V_{CC} = 25V$	_	_	± 10	$\mu$ A
Output Leak	age		3	$V_{EE} = 0V$ , $V_{CC} = 25V$			- 300	μΑ
Current		ICEX	า	V <sub>IN</sub> = V <sub>IL</sub> MAX., V <sub>OUT</sub> = -30V	_	_	- 300	$\mu$ A
Collector-Emi	tter	V65 ( )	4	$V_{EE} = V_{SUB} = 0V$ , $V_{CC} = V_{CC}$ MIN.			$V_{CC}$	V
Saturation Vo	oltage	VCE (sat)	Ť	VIN = VIH MIN., IOUT = -25mA		_	- 1.1	V
Supply Curre	nt	lee (ON)	1	$V_{EE} = V_{SUB} = 0V$ , $V_{CC} = 16V$			10	mΑ
(Output On)		ICC (ON)	•	VIN = VIH MAX., OUTPUT = OPEN	_	_	2	ША
Turn-On Dela	ау	ton	5	$R_L = 1.4k\Omega$ , Single circuit	_	0.4	_	,,,5
Turn-Off Del	ay	tOFF	,	C <sub>L</sub> = 15pF	_	3.0	_	$\mu$ s

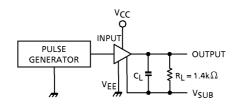
# **TEST CIRCUIT**

1. V<sub>IH</sub>, V<sub>IL</sub>, I<sub>CC</sub>

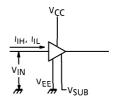
3. I<sub>CEX</sub>



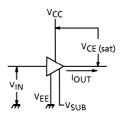
5. ton, toff



C<sub>L</sub> = 15pF (Includes probe and jig capacitance) 2. I<sub>IH</sub>, I<sub>IL</sub>



4. VCE (sat)



Input condition

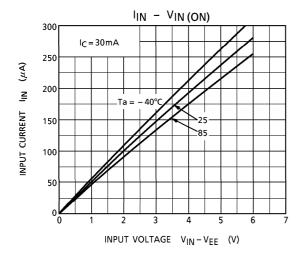
	V <sub>IN</sub>	V <sub>CC</sub>	V <sub>SUB</sub>
TD62706	0 – 3V	25V	- 30

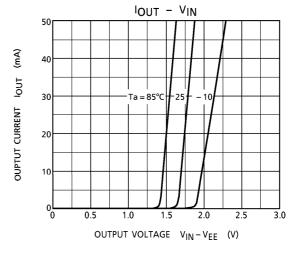
V<sub>IN</sub>: Pulse Width 50 μs Duty Cycle 50%

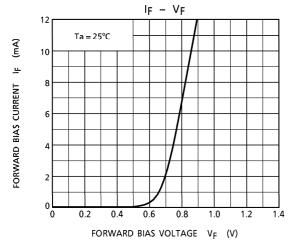
> $t_r \le 5ns$  $t_f \le 10ns$

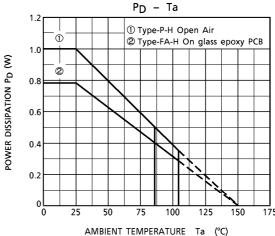
# PRECAUTIONS for USING

Utmost care is necessary in the design of the output line,  $V_{CC}$  and GND (SUB,  $V_{EE}$ ) line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.



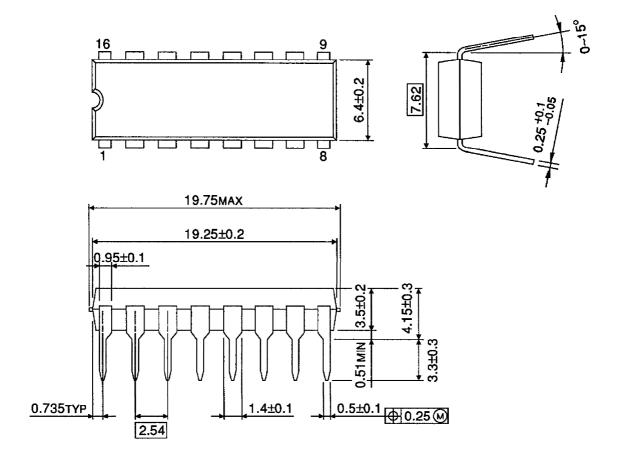






# OUTLINE DRAWING DIP16-P-300-2.54A

Unit: mm



Weight: 1.11g (Typ.)

# OUTLINE DRAWING SSOP16-P-225-1.00A Unit:mm 0.6TYP 8.7MAX 8.2±0.2 7.0±5-1.00A Unit:mm 0.525±0.2

Weight: 0.14g (Typ.)