



# FlingOS

Teaching students on their level

# Background

- Primary and secondary schools
  - National Curriculum
  - Microbit
  - Raspberry Pi
- High-level languages:
  - Python,
  - Java,
  - JavaScript,
  - C#
- University modules
  - Computer architecture : 1<sup>st</sup> year
  - Concurrency : 2<sup>nd</sup> year

# Historical approach

- Teach C
- Then computer architecture
- Then (some) operating systems
- Then concurrency
- Problem!
  - Harder to go from High-level (e.g. C#) to low-level (C/ASM) than other way around
  - But: Students are coming to University with prior high-level knowledge

# New approach

- Bridge the gap between high and low level
- Use familiar languages
- Parallel teaching of C and computer architecture
  - Better understanding e.g. pointers

	Module 1	Module 2	Module 3
Year 1	C	Architecture	Algorithms
Year 2	Go / CSP	Concurrency	Language Eng.
Year 3		Operating Systems	HPC

# FlingOS

The educational operating system



# FlingOS

## Familiar

- C#
- Object oriented

## Simple

- Un-optimised
- Traceable

## Documented

- Inline
- Articles
- Video tutorials



# Example

## Computer Architecture

- Compiler
  - Trace C# to IL
  - Trace IL to ASM

0 references | 0 authors | 0 changes

```
public static int Demo1()  
{  
    int x = 1;  
    int y = 2;  
    int z = x + y;  
    return z;  
}
```

```
1  .method public hidebysig static  
2  int32 Demo1 () cil managed  
3  {  
4      // Method begins at RVA 0x64fc  
5      // Code size 15 (0xf)  
6      .maxstack 2  
7      .locals init (  
8          [0] int32 x,  
9          [1] int32 y,  
10         [2] int32 z,  
11         [3] int32 CS$1$0000  
12     )  
13  
14     IL_0000: nop  
15     IL_0001: ldc.i4.1  
16     IL_0002: stloc.0  
17     IL_0003: ldc.i4.2  
18     IL_0004: stloc.1  
19     IL_0005: ldloc.0  
20     IL_0006: ldloc.1  
21     IL_0007: add  
22     IL_0008: stloc.2  
23     IL_0009: ldloc.2  
24     IL_000a: stloc.3  
25     IL_000b: br.s IL_000d  
26  
27     IL_000d: ldloc.3  
28     IL_000e: ret  
29 } // end of method Kernel::Demo1  
30
```

```
1  BITS 32  
2  SECTION .text  
3  global method_System_Int32_RETEND_Kernel_Kernel_DECLEND_Demo1_NAMEEND____:function  
4  method_System_Int32_RETEND_Kernel_Kernel_DECLEND_Demo1_NAMEEND____:  
5  ;.IL_00 -- -- Offset: FFFFFFFF  
6  push dword EBP  
7  mov dword EBP, ESP  
8  push dword 0  
9  push dword 0  
10 push dword 0  
11 push dword 0  
12 ;.IL_01 -- nop -- Offset: 00  
13 .IL_01_Debug:  
14 nop  
15 ;.IL_02 -- ldc.i4.1 -- Offset: 01  
16 push dword 0x00000001  
17 ;.IL_03 -- stloc.0 -- Offset: 02  
18 pop dword [EBP-4]  
19 ;.IL_04 -- ldc.i4.2 -- Offset: 03  
20 push dword 0x00000002  
21 ;.IL_05 -- stloc.1 -- Offset: 04  
22 pop dword [EBP-8]  
23 ;.IL_06 -- ldloc.0 -- Offset: 05  
24 push dword [EBP-4]  
25 ;.IL_07 -- ldloc.1 -- Offset: 06  
26 push dword [EBP-8]  
27 ;.IL_08 -- add -- Offset: 07  
28 pop dword EBX  
29 pop dword EAX  
30 add EAX, EBX  
31 push dword EAX  
32 ;.IL_09 -- stloc.2 -- Offset: 08  
33 pop dword [EBP-12]  
34 ;.IL_0A -- ldloc.2 -- Offset: 09  
35 push dword [EBP-12]  
36 ;.IL_0B -- stloc.3 -- Offset: 0A  
37 pop dword [EBP-16]  
38 ;.IL_0C -- br.s -- Offset: 0B  
39 ;No jump insert - pointless 0 distance jump  
40 ;.IL_0D -- ldloc.3 -- Offset: 0D  
41 push dword [EBP-16]  
42 ;.IL_0E -- -- Offset: 0E  
43 pop dword EAX  
44 mov dword [EBP+8], EAX  
45 add ESP, 16  
46 pop dword EBP  
47 ;.IL_0F -- ret -- Offset: FFFFFFFF  
48 ret
```

# Future

- Basic OS is ready!
- Now it's time to draw it all together...
- Starter kit
  - Target : A-level / 1<sup>st</sup> year university
  - Aim : Basic operating system (concurrency)
  - Platforms : Virtual machine, simple h/w platform
  - Price-point : £40 to £50
- How?
  - Extended articles and videos
  - Kit sold online





## FlingOS

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