Tutorial Continuous Delivery z/OS

1) Criar tabelas: AGNALDO.BECOME.JOBS(XMLCRETB)

MYCUST MYCUST HIST INTEGER ID INTEGER VARCHAR 4 20 TRANS NAME VARCHAR MSG_TYPE MSG_TXT CITY VARCHAR XML STREET VARCHAR LAST_UPDATED
DB2_GENERATED_DOCI TIMESTMP BIGINT 10 AST_UPDATED TIMESTMP

2) **Populate MYCUST:** AGNALDO.BECOME.JOBS(XMLINSRT)

MYCUST

*****	******	******	Top of	f Data ****	******	******
ID	TRANS	MSG_TYPE	MSG_TX	KT		
1000	1000	address	×ml</td <td>version="1.</td> <td>0" encoding=</td> <td>="IBM037"?></td>	version="1.	0" encoding=	="IBM 0 37"?>
1002	1002	address	×ml</th <th>version="1.</th> <th>0" encoding=</th> <th>="IBM037"?></th>	version="1.	0" encoding=	="IBM 0 37"?>
1003	1003	address	×ml</th <th>version="1.</th> <th>0" encoding=</th> <th>="IBM037"?></th>	version="1.	0" encoding=	="IBM037"?>
1004	1004	address	xml</th <th>version="1.</th> <th>0" encoding=</th> <th>="IBM037"?></th>	version="1.	0" encoding=	="IBM037"?>
******	******	*****	Bottom	of Data ***	*****	******

MYCUST_HIST is empty

3) **Test MERGE:** AGNALDO.BECOME.JOBS(XMLMERGE) – Notice data from source MYCUST will be casted into MYCUST HIST

****	******	**** Top of [)ata *********	******	
ID	NAME	CITY	STREET	LAST_UPDATED	
1000	Kathy Smith	Toronto	5 Rosewood	2020-06-03-10	
1002	Jim Noodle	Markham	25 EastCreek	2020-06-03-10	
1003	Robert Shoemaker	Aurora	1596 Baseline	2020-06-03-10	
1004	Kathy Smith	Nova Odessa	Rec. Fazenda, 1	2020-06-03-10	

4) **Test SQL NATIVE PROCEDURE to exploit MERGE,** AGNALDO.BECOME.JOBS(XMLPROC). We would receive the following errors as per each scenario.

		APPLCOMPAT			
Senario	ZPARM	PLAN/PACKAGE	DYNAMIC SET	TAKEN	SQLCODE
1	V11R1	V11R1	None	V11R1	-4743
2	V11R1	V12R1M500	None	V11R1	-4743
3	V11R1	V12R1M500	V12R1M500	V12R1M500	0
4	V12R1M500	V11R1	None	V12R1M500	0
5	V12R1M500	V12R1M500	V12R1M500	V12R1M500	0

Test Scenario: Run program to create new Stored Procedure that exploit new SQL MERGE capability available with FL 500 with Db2 V12.

Observations: Db2 Admin does not override FL default

V12R1 is the same as V12R1M500 V11R1 is the same as V12R1M100