Milestone 1 *Dates?*

- Worked on part of the instruction set/format table [1hr]
- Reviewed example assembly programs for errors [1 hr]
- Worked on the machine translation table for relprime [1hr]
- Working on fibonacci to demonstrate recursive programs using our language [2.5hrs]

- Few to no design decisions as Ethan finished most of the work by himself.

- Work for M2 to be decided, as most of us were active on teams, we only had a single meeting to finalize the design document before the 5:00 p.m. Tuesday deadline.

Milestone 2
- Thursday, January 11:
  - Met with ethan to understand the processor hardware logic that he had implemented in turing complete as well as how to run the assembler in python that he had built. [1.5hrs]
  - Discussed workload split for milestone 2. [30 minutes]
  - Wenzi and I were to split the rtl insrtuction breakdown while Emma and Ethan were to work on jump + return and the rtl component list. [30 minutes]
- Saturday, January 13:
  - Wenzi and I met and discussed homework 13 and assigned the exact workload split for milestone 2. [3 hrs]
- Sunday, January 14:
  - Ethan commited the initial push for the quartus files and discussed the rough breakdown of how we would work through the verilog + unit testing on quartus on teams. [1hr]
  - I finished the first pass of the rtl instructions [2 hrs]
- Monday, January 15: *what was wrong?*
  - Made incremental changes to correct rtl instructions. [15 mins]
  - Met with Emma to finalize the component table + rtl instruction table for M2 [1hr]