# HD14040B

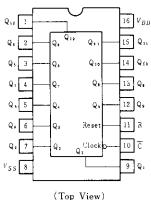
#### 12-bit Binary Counter

The HD14040B 12-stages binary counter is designed with an input wave shaping circuit and 12-stages of ripple-carry binary counter. The device advances the count on the negative-going edge of the clock pulse. Applications include time delay circuits, counter controls, and frequency-dividing circuits.

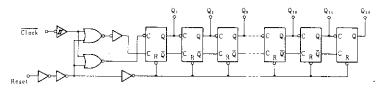
#### FEATURES

- Fully Static Operation
- Quiescent Current = 5nA/pkg typ. @5V
- Supply Voltage Range = 3 to 18V
- Capable of Driving One Low-power Schottky TTL Load Over the Rated Temperature Range
- Common Reset Line
- 13MHz Typical Counting Rate @15V
- Pin-for-Pin Replacement for CD4040B and MC14040B

#### ■ PIN ARRANGEMENT



#### **■LOGIC DIAGRAM**

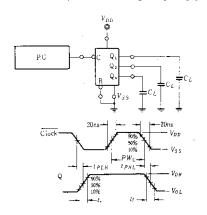


#### **■ TRUTH TABLE**

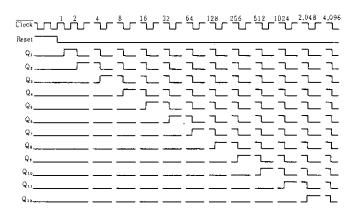
$\overline{\overline{C}}$	Reset	Outputs State
	0	No Change
	0	Advance to next state
×	1	All Outputs are low

x-Don't Care

#### ■ SWITCHING TIME TEST CIRCUIT



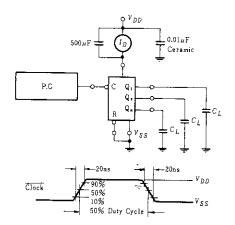
#### **■TIMING DIAGRM**



#### ■ ELECTRICAL CHARACTERISTICS

Characteristic	Symbol			−40°C		<b>25°</b> ℃			<b>85</b> ℃			
Onaracteristic	J. J	$V_{DD}(V)$	Test Conditions	min	max	min	typ	max	min	max	Unit	
	Vol	5.0		_	0.05	_	0	0.05	_	0.05	4	
		10	$V_{in} = V_{DD}$ or 0		0.05	-	0	0.05	_ :	0.05		
Output Voltage		15		_	0. <b>0</b> 5	_	0	0.05	-	0.05		
Output voltage		5.0	$V_{in}=$ 0 or $V_{DD}$	4.95	-	4.95	5.0	_	4.95	-	v	
	Von	10		9.95		9.95	10		9.95	-		
		15		14.95	_	14.95	15	_	14.95	-		
**		5.0	$V_{out}=$ 4.5 or 0.5V		1.5	_	2.25	1.5	-	1.5	v	
	$V_{IL}$	10	$V_{out} = 9.0 \text{ or } 1.0 \text{V}$	_	3.0		4.50	3.0	_	3.0		
Input Voltage		15	$V_{eut}=13.5$ or $1.5\mathrm{V}$	-	4.0	_	6.75	4.0	_	4.0		
input voltage		5.0	$V_{out} = 0.5 \text{ or } 4.5 \text{V}$	3.5	_	3.5	2.75	-	3.5	-	ν	
	$V_{IH}$	10	$V_{out} = 1.0 \text{ or } 9.0 \text{V}$	7.0	_	7.0	5.50		7.0			
		15	$V_{out} = 1.5 \text{ or } 13.5 \text{V}$	11.0	-	11.0	8.25	_	11.0			
		5.0	Voh = 2.5  V	-1.0		-0.8	-1.7		-0.6	-		
	Іон	5.0	$V_{OH} = 4.6 \text{ V}$	-0.2	_	-0.16	-0.36	*****	-0.12	_ ]	m A	
		10	$V_{OH} = 9.5 \text{ V}$	-0.5	_	-0.4	-0.9	_	-0.3	- :		
Output Drive Current		15	$V_{OH}=13.5\mathrm{V}$	-1.4	-	-1.2	-3.5	_	-1.0	_ 1		
	IoL	5.0	$V_{OL} = 0.4 \text{ V}$	0.52	-	0.44	0.88	_	0.36	- ]	mA	
		10	$V_{OL} = 0.5 \text{ V}$	1.3	-	1.1	2.25	_	0.9	<u>-</u>		
		15	$V_{OL} = 1.5 \text{ V}$	3.6	-	3.0	8.8		2.4	-		
Input Current	Iin	15		_	±0.3	_	~0.00001	±0.3	_	±1.0	μA	
Input Capacitance	Cin	-	$V_{in} = 0$			_	5.0	7.5	-		рF	
Quiescent Current	$I_{DD}$	5.0	7ama Si1	_	20	-	0.005	20	_	150		
		10	Zero Signal, per Package		40	_	0.010	40	-	300	- 1 '	
		15		_	80	-	0.015	80	_	600		
<del> </del>	* I <sub>T</sub>	5.0	Dynamic + $I_{DD}$ , $C_L = 50$ pF		-	_	0.42	_	-	-	μΑ	
Total Supply Current*		10	f = 1  kHz,		-		0.85	_	_	_		
		15	per Gate	_		. –	1.43	-	_	_		

### ■POWER DISSIPATION TEST CIRCUIT AND WAVEFORM



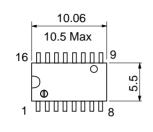
<sup>\*</sup> To calculate total supply current at frequency other than 1kHz.  $@V_{DD} = 5.0 \text{ V} & I_T = (0.42 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 10 \text{ V} & I_T = (0.85 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_T = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V_{DD} = 15 \text{ V} & I_{T} = (1.43 \mu \text{A/kHz}) f + I_{DD} & @V$ 

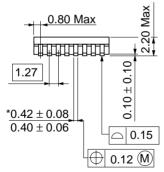
## ■SWITCHING CHARACTERISTICS ( $C_{t}$ =50pF, Ta=25 $^{\circ}$ C)

Characteristic		Symbol	$V_{DD}(V)$	min	typ	max	Unit
Output Rise Time		t,	5.0	_	180	400	
			10		90	200	ns
			15		65	160	
Output Fall Time		t ;	5.0	_	100	200	
			10	_	50	100	ns
			15	-	37	80	
		tplн,	5.0	_	400	1050	
	Clock-to-Q1		10		170	420	ns ns
	16		15	<u> </u>	120	320	
		tPHL	5.0	_	2.5	7.5	
Propagation	Clock-to-Q12		10	_	0.9	2.7	μs
Delay Time	1		15	_	0.5	2.1	
			5.0		570	1620	
	Reset-to-Qn	tрнi	10		215	600	ns
			-15		170	450	i
Clock Pulse Width			5.0	385	140	_	
		$PW_C$	10	150	55	-	ns
			15	115	38	_	]
Clock Frequency		***************************************	5.0	_	3.5	1.5	
		PRF	10	_	9.0	3.5	MHz
			15	_	13	4.5	]
Clock Pulse Rise and Fall Time			5.0	-			
		$t_{\tau}, t_f$	10	- - -	į		
			15				
Reset Pulse Width		$PW_R$	5.0	960	320	_	
			10	360	120	-	ns
			15	270	80	_	1

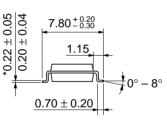
Unit: mm 19.20 20.00 Max 16 7.40 Max 6.30 1.3 1.11 Max 7.62 5.06 Max 2.54 Min 0.51 Min  $0.25^{+0.13}_{-0.05}$  $0.48 \pm 0.10$  $2.54\pm0.25$  $0^{\circ} - 15^{\circ}$ Hitachi Code DP-16 **JEDEC** Conforms EIAJ Conforms Weight (reference value) 1.07 g

Unit: mm





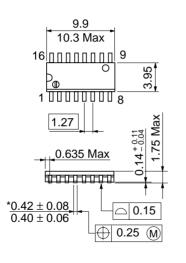


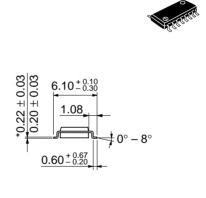


Hitachi Code	FP-16DA
JEDEC	_
EIAJ	Conforms
Weight (reference value)	0.24 g

\*Dimension including the plating thickness
Base material dimension

Unit: mm





\*Dimension including the plating thickness
Base material dimension

Hitachi Code	FP-16DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.15 g

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