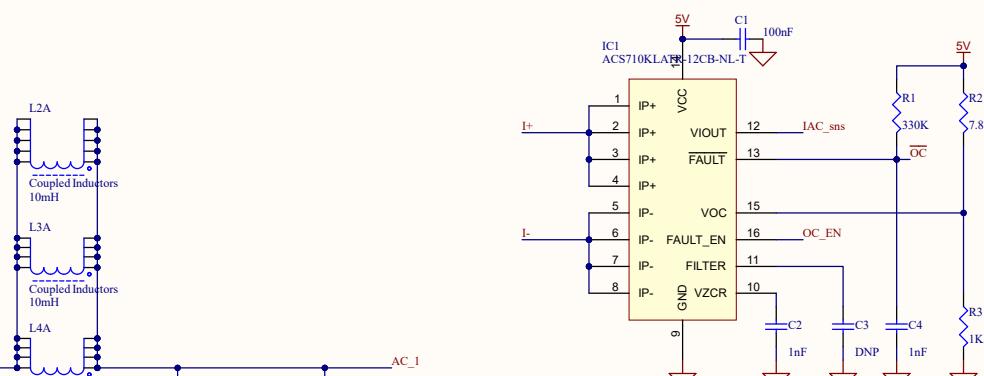


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REVISION	DESCRIPTION	DATE	APPROVED

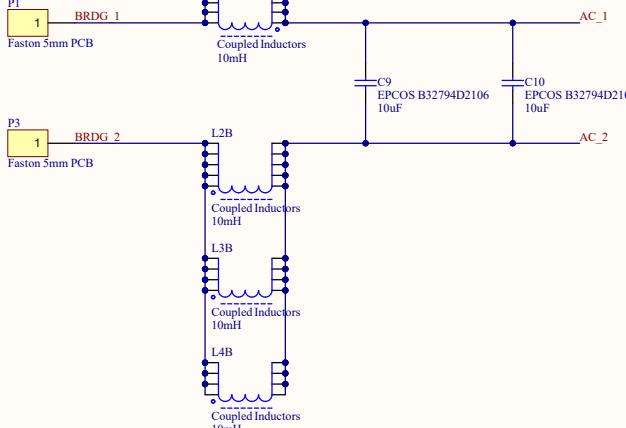
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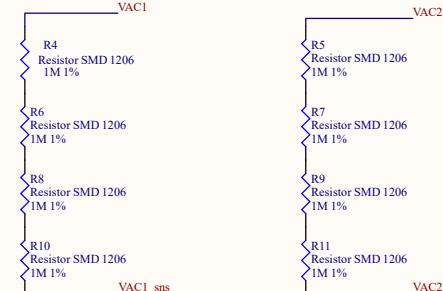
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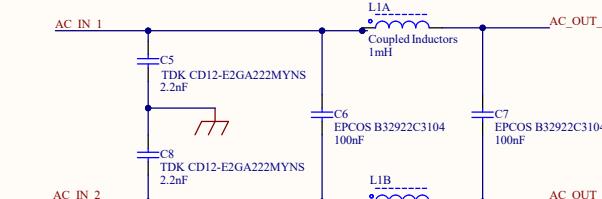
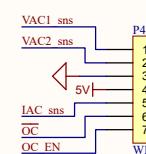


C

C



AC 2                  I-  
AC 1                  AC IN 1  
AC IN 2                  I+



APPROVALS	DATE	PROJECT	Altium	•
ENG:	*			
DSN:	*	PROJECT REVISION:	Not in version control	
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ASSY DWG:		SIZE	CAGE CODE	DWG NO.
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PCB DWG:		REV		
SCALE:		FILE NAME	AC_Output_schematic.SchDoc	SHEET * OF *