

Document ID	Type	Object Type	Status	Task ID	Section ID
DC_M-01	Header	Hardware Test			
DC_M-01	Definition	The activity of the system begins here, when the hardware plug along with the data is set up the system after the power control mode.	Proposed	1	
DC_M-02	Comments	Check status is ok when the system is started.	Proposed	1	
DC_M-03	Comments	1. Check the hardware Plug Pin Foots from the sea for the validation of the system control.	Proposed	1	
DC_M-04	Comments	2. The information from the DC/DC memory block is stored in the DC/DC memory block at level 1.	Proposed	1	
DC_M-05	Requirements	3. Along with the hardware plug the speed data from the sea also obtained which is stored in the DC/DC memory block 1 for further usage.	Proposed	1	
DC_M-07	Header	Control Mode			
DC_M-07	Definition	The Control mode sets up whether the system is in mode mode or in normal mode.	Proposed	2	
DC_M-08	Comments	The system will set a normal mode with the Control On.	Proposed	2	
DC_M-09	Comments	1. The Control mode sets up whether the system is in mode mode or in normal mode.	Proposed	2	
DC_M-10	Comments	2. Based on the data from the DC/DC memory block the speed data is stored in the DC/DC memory block 1 for further usage.	Proposed	2	
DC_M-11	Comments	3. The speed data from the DC/DC memory block is stored in the DC/DC memory block 1 for further usage.	Proposed	2	
DC_M-12	Header	Run Range Control (MCC)			
DC_M-12	Definition	This is a timer which gives information and any detection or detection in the run is identified and required to the system.	Proposed	3	
DC_M-13	Comments	Control Release: After a Range sensor are used for the purpose.	Proposed	3	
DC_M-14	Comments	1. The Range sensor is used for the purpose of the system.	Proposed	3	
DC_M-15	Comments	2. The Range sensor is used for the purpose of the system.	Proposed	3	
DC_M-16	Comments	3. The Range sensor is used for the purpose of the system.	Proposed	3	
DC_M-17	Requirements	4. The Range sensor is used for the purpose of the system.	Proposed	3	
DC_M-18	Comments	5. The Range sensor is used for the purpose of the system.	Proposed	3	
DC_M-19	Header	Control Mode			
DC_M-19	Definition	The activity is a system speed based on the actual speed and Run based on the speed is maintained continuously, until there is a change from the sea.	Proposed	4	
DC_M-20	Comments	Used frequency, during the speed definition.	Proposed	4	
DC_M-21	Comments	1. During the actual speed of the system from the actual speed sensor through the CMC bus.	Proposed	4	
DC_M-22	Comments	2. The data obtained from the actual speed sensor is stored in the DC/DC memory block 1.	Proposed	4	
DC_M-23	Comments	3. The data obtained from the actual speed sensor is stored in the DC/DC memory block 1.	Proposed	4	
DC_M-24	Comments	4. The data obtained from the actual speed sensor is stored in the DC/DC memory block 1.	Proposed	4	
DC_M-25	Comments	5. The data obtained from the actual speed sensor is stored in the DC/DC memory block 1.	Proposed	4	
DC_M-26	Comments	6. The data obtained from the actual speed sensor is stored in the DC/DC memory block 1.	Proposed	4	
DC_M-27	Comments	7. The data obtained from the actual speed sensor is stored in the DC/DC memory block 1.	Proposed	4	