

Project Proposal - EEC289Q

Parallel Recursive Spoke Darts for Delaunay/Voronoi Meshing

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Abstract

We propose to implement the Recursive Spoke Darts (RSD) algorithm; a local hyper-plane sampling method for 3D Delaunay and Voronoi meshing presented in [4]. The algorithm has two overlapping objectives. Through reliance on simple local operations (recursive line-hyperplane trimming), the authors claims to present the first exact Delaunay/Voronoi meshing algorithm that breaks the curse of dimensionality by improving the scalability through eliminating communication between compute entities (processors, threads, blocks, etc). However, the algorithm was only implemented on MPI settings where it improves over the state-of-the-art. The improvement comes in terms of speedup and the ability to construct the mesh exactly in higher dimensions where other methods give approximate solution, prohibitively slow or fail completely. In this project, we would like to scratch the surface of this algorithm when mapped into the GPU by confining ourselves to 3D space. This will give a concert idea on the challenges of implementing it on higher dimensions. The algorithm presents many interesting sub-problems due to the inherent irregularity of the problem which troubles the memory accessing pattern, load balance and control flow.

Motivation

Delaunay Triangulation (DT) of a point set has important applications in many fields including data visualization and imaging, terrain modeling, finite element mesh generation, surface reconstruction, and structural networking for arbitrary point sets. This is due to its nice geometric properties and desirable qualities. For example, in \mathbb{R}^3 , DT minimizes the maximum radius of the minimum containment sphere of the tetrahedra which guarantees the convergence of the finite element solution. Accelerating the computation of DT is a necessity with the rapid increase in the problem size from thousands of points to millions and even billions [8].

There has been a decent amount of work devoted to parallelize the DT computation most of which is an adaption of one of the serial algorithms. Examples include parallelize Dwyer's divide and conquer algorithm [5, 3, 6, 8], incremental/point insertion algorithm [2, 1] and flipping algorithms [7]. Even though all these implementations have achieved good speedup over the serial counterparts, we hypothesize that in order to further accelerate the computation, one should start with an algorithm that is designed for parallel execution. RSD has the potential to be the first DT algorithm that is deigned with parallel mindset. RSD abandons the dependence on the empty sphere principle which gives it the foundation needed for scalable consistent meshing. Additionally, the simple operations the RSD algorithm relies on make it more favorable for GPU settings.

Milestones

- **Week 1 (26 February - 4 March):** Preparation of input point clouds, k-d tree for nearest neighbor search queries, initial implementation using one thread, and validating the output.
- **Week 2 (5 March - 11 March):** Extending the one thread implementation to multiple blocks and multiple threads.
- **Week 3 (12 March - 18 March):** Optimization of memory accesses and caching into shared memory, comparing against other parallel algorithms for 3D Delaunay meshing.

Deliverables

We promise, to the best of our capacity, to deliver the followings through this project

- Exploring and reporting the challenges of implementing the RSD algorithm in 3D
- Reporting our experience with solving these challenges
- Valid and robust implementation of the RSD algorithm in 3D on the GPU using CUDA
- Comparison against the available code of [2] to get a taste on where we stand.

References

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