**Memory Bus Width over Time** 10,000 -**Memory Bus type** 1T-SRAM O DDR O DDR2 O DDR3 A/OA /O 00 O DDR3L O DDR4 O DRAM **@** 0 0 0 0 2,000 EDO VRAM O FPM O GDDR2 O GDDR3 1,000 O GDDR4 O GDDR5 O GDDR5X A AAA O GDDR6 O GDDR6X O HBM O HBM2 O HBM2E 200 Memory Bus width (bit) ○ HBM3 O LPDDR4 O ALD CORDA ACCESSOR AS ACCESSOR OF THE CORDA ACCESSOR OF THE CORD O LPDDR4X O LPDDR4x 100 0 0 O LPDDR5 O SDR 0 0 A 00 00 0 000 A A SGRAM O XDR Vendor • • **A A A** Δ Δ 0 O AMD ☐ Intel ▲ NVIDIA 20 10 0 2 -1988 1990 1992 1996 1998 2000 2002 2004 2006 2008 2010 2012 2014 2016 2018 2020 2024 1986 1994 2022 Launch