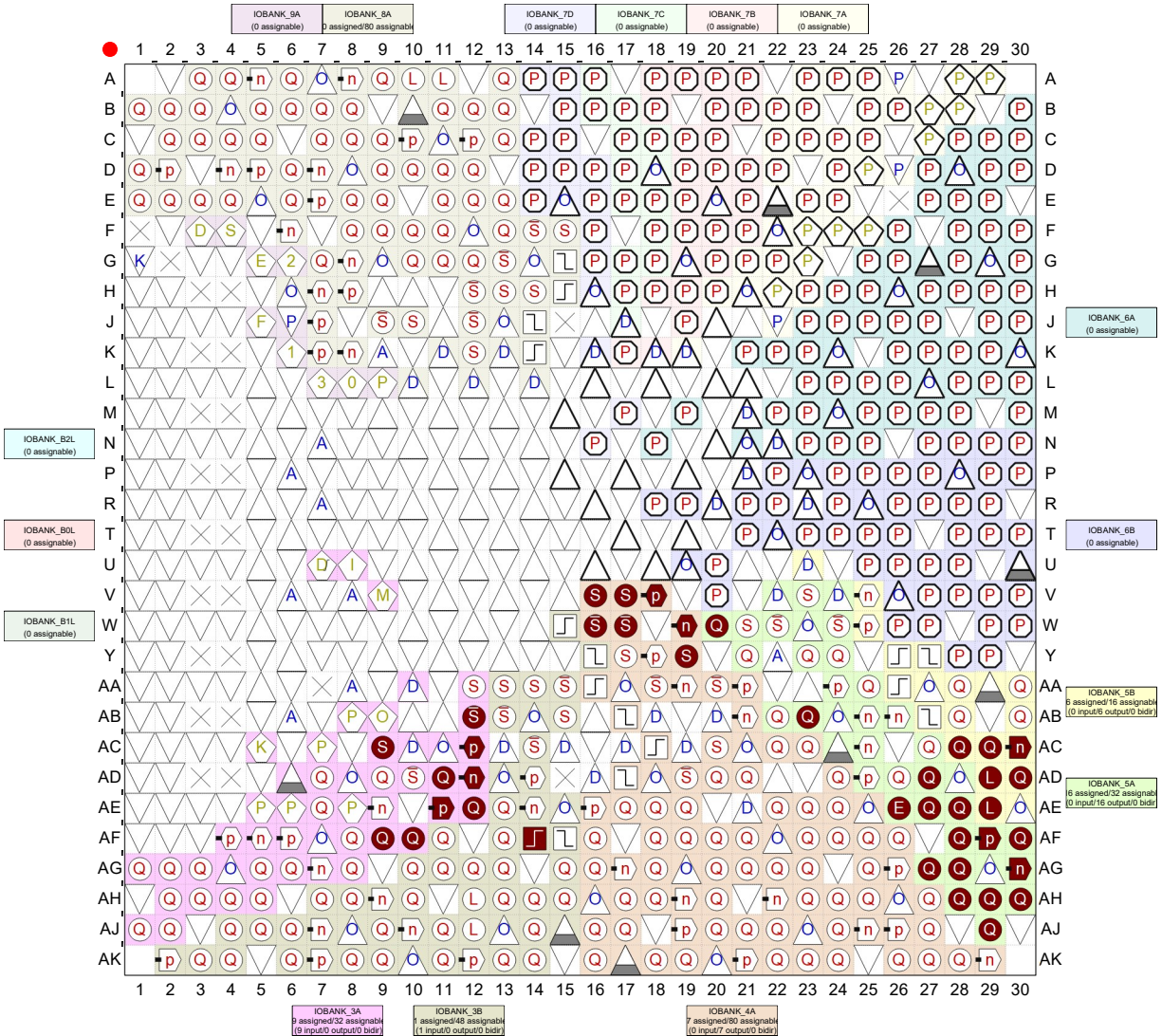










































Cyclone V - 5CSEMA5F31C6



Legend:

- | | | | | | | | |
|---|----------------|---|---------------------------|---|-----------------------------|---|--------------|
|  | User I/O |  | User assigned I/O |  | Fitter assigned I/O |  | Unbonded pad |
|  | Reserved pin |  | DEV_OE |  | DIFF_n |  | DIFF_p |
|  | DIFF_n output |  | DIFF_p output |  | DQ |  | DQS |
|  | DQSB |  | Hard processor system pin |  | CLK_n |  | CLK_p |
|  | Other PLL |  | MSEL0 |  | MSEL1 |  | MSEL2 |
|  | MSEL3 |  | CONF_DONE |  | DCLK |  | nCE |
|  | nCONFIG |  | TDI |  | TCK |  | TMS |
|  | TDO |  | nSTATUS |  | Other dedicated programming |  | VREF |
|  | VCCP/VCCR/VCCT |  | VCCA |  | VCCIO |  | VCCPD |
|  | GND |  | GND for programming |  | RREF |  | No connect |