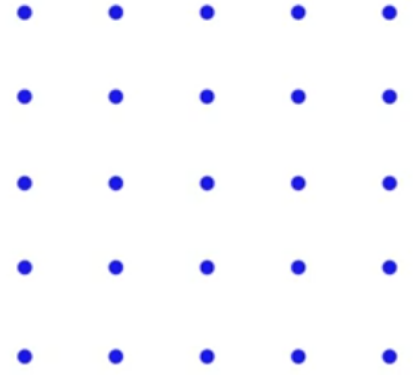


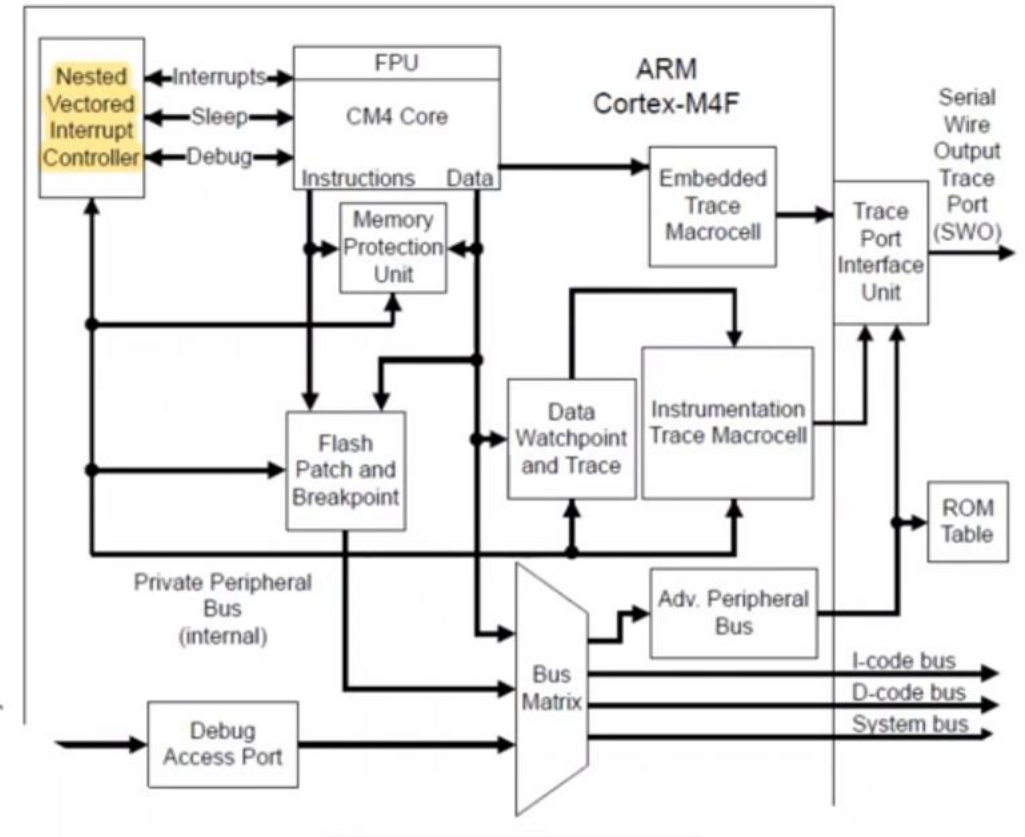
# NVIC : NESTED VECTOR INTERRUPTS CONTROLLER



MUHAMMAD ELZEINY

# FEATURES

- 78 interrupts.
- A programmable priority level of 0-7 for each interrupt.
- Low-latency exception and interrupt handling.
- Level and pulse detection of interrupt signals.
- Grouping of priority values into group priority and sub-priority fields.
- Interrupt tail-chaining and late arriving mechanism.
- An external Non-maskable interrupt (NMI).



# PRIORITY GROUPING


- The NVIC can assign each interrupt to Group and sub-group
- If the pending\active interrupts have **different Group priority** → The higher will serve\preempt first.
- If the pending interrupts have **different sub-Group priority** → the higher one will be served first (but can't preempt the active one)
- If the pending interrupts have the **same Group and sub-group priority** → the first ordered in vector table will be served first (but can't preempt the active one)


Interrupt	Group	Sub-Group
Int0	0	0
Int1	1	0
Int2	0	0
Int3	0	1

## PRIORITY GROUPING

- The NVIC can assign each interrupt to Group and sub-group
- If the pending\active interrupts have **different Group priority** → The higher will serve\preempt first.
- If the pending interrupts have **different sub-Group priority** → the higher one will be served first (but can't preempt the active one)
- If the pending interrupts have the **same Group and sub-group priority** → the first ordered in vector table will be served first (but can't preempt the active one)

Interrupt	Group	Sub-Group
Int0	0	0
Int1	1	0
Int2	0	0
Int3	0	1

Group  $0 \rightarrow 7$   G

Sub Group  $0 \rightarrow 7$   S

[PRI-Register]  $\Delta$  Grap (X)  
SubGrap (Y)



	B2	B1	B0	G	S
G	x	x	x	0 → 7	0
	x	x	y	0 → 3	0 → 1
	x	y	y	0 → 1	0 → 3



# PRIORITY GROUPING

- The NVIC can assign each interrupt to Group and sub-group
- If the pending\active interrupts have **different Group priority** → The higher will serve\preempt first.
- If the pending interrupts have **different sub-Group priority** → the higher one will be served first (but can't preempt the active one)
- If the pending interrupts have the **same Group and sub-group priority** → the first ordered in vector table will be served first (but can't preempt the active one)

Interrupt	Group	Sub-Group
Int0	0	0
Int1	1	0
Int2	0	0
Int3	0	1

Group 0 → 7       G  
 SubGroup 0 → 7       S

[PRI-Register]


Group (X)  
 SubGroup (Y)


	B2	B1	B0	G	S
Group	X	X	X	0 → 7	0
SubGroup	X	X	Y	0 → 3	0 → 1
	X	Y	Y	0 → 1	0 → 3
	Y	Y	Y		

## PRIORITY GROUPING

- The NVIC can assign each interrupt to Group and sub-group
- If the pending\active interrupts have **different Group priority** → The higher will serve\preempt first.
- If the pending interrupts have **different sub-Group priority** → the higher one will be served first (but can't preempt the active one)
- If the pending interrupts have the **same Group and sub-group priority** → the first ordered in vector table will be served first (but can't preempt the active one)

Interrupt	Group	Sub-Group
Int0	0	0
Int1	1	0
Int2	0	0
Int3	0	1

Group  $0 \rightarrow 7$   G

Sub Group  $0 \rightarrow 7$   S

[PRI-Register]       $\mathcal{L}$  Grap (X)  
SubGrap (Y)

	B2	B1	B0	G	S
0	X	X	X	0 → 7	0
1	X	X	Y	0 → 3	0 → 1
2	X	Y	Y	0 → 1	0 → 3
3	Y	Y	Y		

no preemption



# PRIORITY GROUPING

- The NVIC can assign each interrupt to Group and sub-group
- If the pending\active interrupts have **different Group priority** → The higher will serve\preempt first.
- If the pending interrupts have **different sub-Group priority** → the higher one will be served first (but can't preempt the active one)
- If the pending interrupts have the **same Group and sub-group priority** → the first ordered in vector table will be served first (but can't preempt the active one)

SCB  
APINT

NVIC

Interrupt	Group	Sub-Group
Int0	0	0
Int1	1	0
Int2	0	0
Int3	0	1

Group 0 → 7



SubGroup 0 → 7



[PRI-Register]

Group (X)  
SubGroup (Y)

B2 B1 B0



	B2	B1	B0	G	S
→	X	X	X	0 → 7	0
	X	X	Y	0 → 3	0 → 1
	X	Y	Y	0 → 1	0 → 3
	Y	Y	Y		

no preemption

## REGISTERS DESCRIPTIONS

Objective	Register Name	Module
Enable\Disable All Interrupts	PRIMSK	Core
Enable\Disable All Exceptions (Except NMI)	FAULTMSK	Core
Define the minimum priority for exception processing.	BASEPRI	Core
Trigger System-Interrupts \Faults by SW	INTCTRL	SCB
Trigger any Interrupt by SW	SWTRIG	NVIC
Enable\Disable NVIC gate for each Interrupt.	ENx	NVIC
Enable\Disable SCB gate for each System-Interrupts \ Faults.	SYSHNDCTRL	SCB
Configure Priority for Interrupts	PRIx	NVIC
Configure Priority for System-Interrupts \ Faults.	SYSPRIx	SCB
Indicate the cause of faults	FAULTSTAT \ HFAULTSTAT	SCB
Priority grouping control	<b>APINT</b>	SCB
Indicate the offset of the vector table base address	VTABLE	SCB
The Current Active Exception	PSR	Core




## REGISTERS DESCRIPTIONS

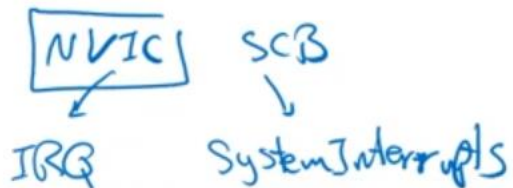
Objective	Register Name	Module
Enable\Disable All Interrupts	PRIMSK	Core
Enable\Disable All Exceptions (Except NMI)	FAULTMSK	Core
Define the minimum priority for exception processing.	BASEPRI	Core
Trigger System-Interrupts \Faults by SW	INTCTRL	SCB
Trigger any Interrupt by SW	SWTRIG	NVIC
Enable\Disable NVIC gate for each Interrupt.	ENx	NVIC
Enable\Disable SCB gate for each System-Interrupts \ Faults.	SYSHNDCTRL	SCB
Configure Priority for Interrupts	PRIx	NVIC
Configure Priority for System-Interrupts \ Faults.	SYSPRIx	SCB
Indicate the cause of faults	FAULTSTAT \ HFAULTSTAT	SCB
Priority grouping control	<b>APINT</b>	SCB
Indicate the offset of the vector table base address	VTABLE	SCB
The Current Active Exception	PSR	Core

### Interrupt Enabling Gates



## REGISTERS DESCRIPTIONS

Objective	Register Name	Module
Enable\Disable All Interrupts	PRIMSK	Core ✓
Enable\Disable All Exceptions (Except NMI)	FAULTMSK	Core ✓
Define the <u>minimum priority</u> for exception processing.	BASEPRI	Core ✓
Trigger <u>System</u> -Interrupts \Faults by SW	(INTCTRL) ← {	SCB
Trigger any Interrupt by SW	SWTRIG	NVIC
Enable\Disable NVIC gate for each Interrupt.	(ENx) ←	NVIC ✓
Enable\Disable SCB gate for each System-Interrupts \ Faults.	(SYSHNDCTRL)	SCB ✓
Configure Priority for Interrupts 	(PRIx) ←	NVIC
Configure Priority for System-Interrupts \ Faults.	(SYSPRIx) ←	SCB
Indicate the cause of faults	FAULTSTAT \ HFAULTSTAT	SCB
Priority grouping control	APINT <sup>xxx</sup> <sub>yyy</sub>	SCB
Indicate the offset of the vector table base address	VTABLE	SCB
The Current Active Exception	PSR	Core


  
 NVIC → IRQ
   
 SCB → System Interrupts

### Interrupt Enabling Gates



# #TASK – INTCTRL DRIVER

## API: Types

- IntCtrl\_InterruptType

## API: Functions

- void IntCtrl\_init(void)

## Configuration

- InterruptPeripheralGates
- InterruptGroup Priority
- InterruptSub-Group Priority



# #TASK – INTCTRL DRIVER

## API: Types

- IntCtrl\_InterruptType

## API: Functions

- void IntCtrl\_init(void)

## Configuration

- InterruptPeripheralGates
- InterruptGroup Priority
- InterruptSub-Group Priority

## Dynamic Code (Configuration)

- IntCtrl\_Cfg.h
- IntCtrl\_Lcfg.c

## Static Code

- **IntCtrl.c**
- IntCtrl.h
- IntCtrl\_Types.h

# #TASK – INTCTRL DRIVER

## API: Types

- IntCtrl\_InterruptType

## API: Functions

- void IntCtrl\_init(void)

## Configuration

- InterruptPeripheralGates
- InterruptGroup Priority
- InterruptSub-Group Priority

## Dynamic Code (Configuration)

- IntCtrl\_Cfg.h
- IntCtrl\_Lcfg.c

## Static Code

- **IntCtrl.c**
- IntCtrl.h
- IntCtrl\_Types.h

