

Virtual Machine

COP-3402 Systems Software

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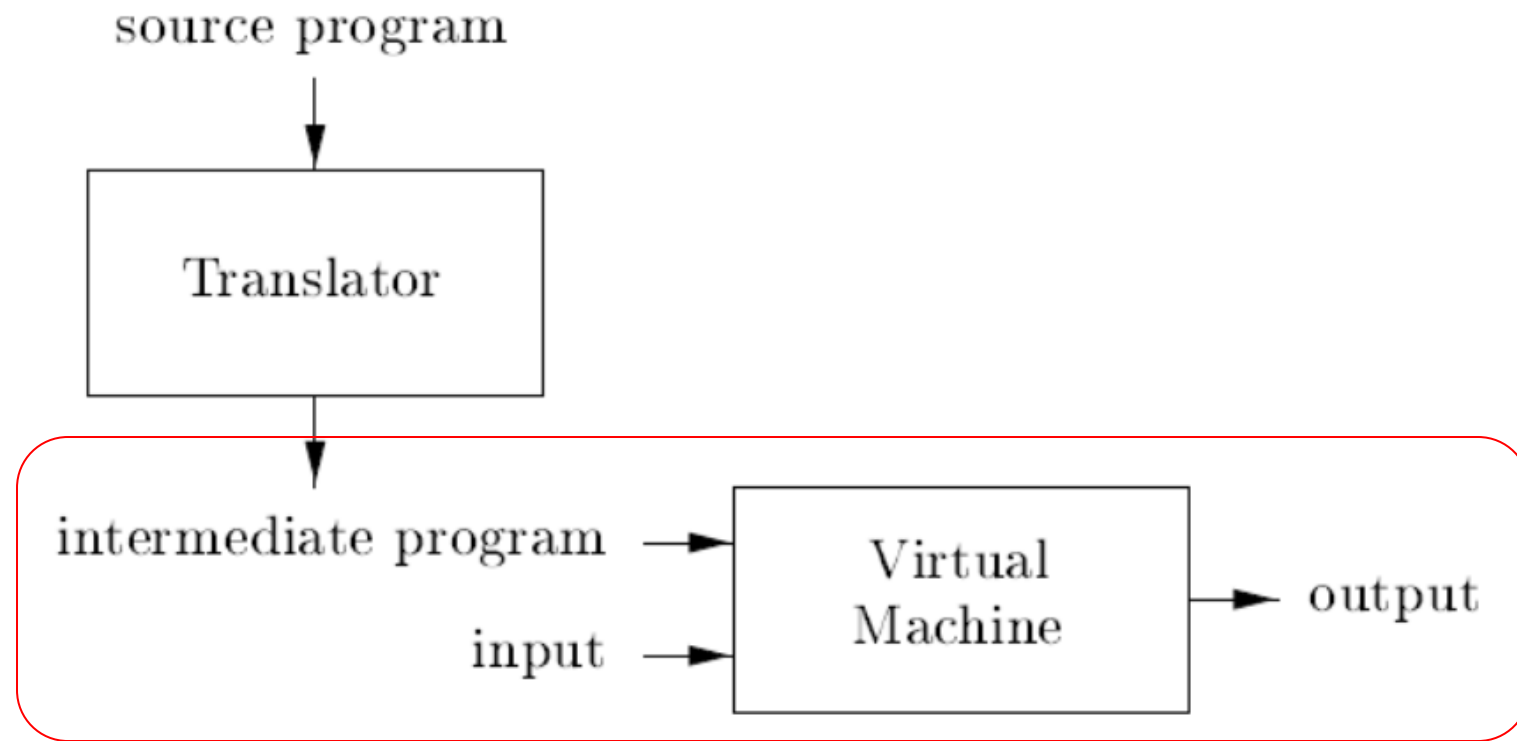


Figure 1.4: A hybrid compiler

Core Idea: Simulate Physical Hardware