

Memory Instructions

<i>HEXADECIMAL CODE</i>	<i>DESCRIPTION</i>
1XXX	ADD (add memory word to AC)
2XXX	LDA (load memory word to AC)
4XXX	BUN (Branch unconditionally)
5XXX	SUB (subtract memory word from AC)

Register Instructions

<i>HEXADECIMAL CODE</i>	<i>DESCRIPTION</i>
7020	INC (increment AC)
7004	SZA (skip next instruction if AC zero)