

Formal Verification (Equivalence checking)

Description:

Equivalence checking using basic miter circuit (XOR Gate)

Circuit 2 is an enhanced version of circuit 1 where the critical path delay is improved

In circuit 1 the critical path has 8 gates

In circuit 2 the critical path has 6 gates only

Circuit 3 is an xor gate (miter circuit)

Operation idea:

- 1- Optimizing the logic functions of both circuits.
- 2- Supplying the same inputs in both circuits.
- 3- The miter circuit output should be zero if they are equivalent.

Tool:

Logic Friday

