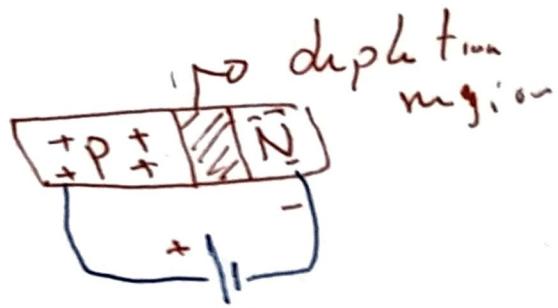
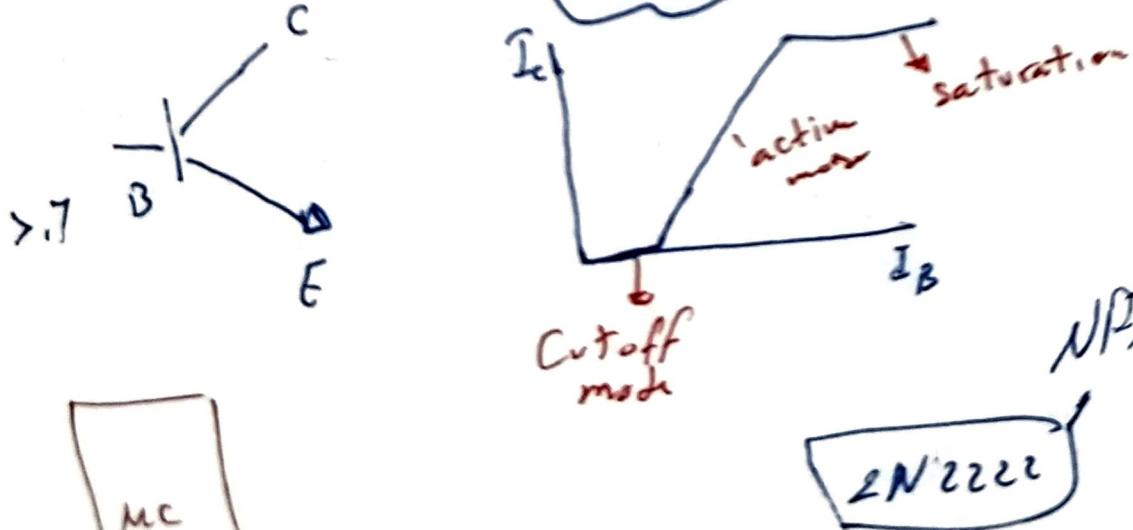
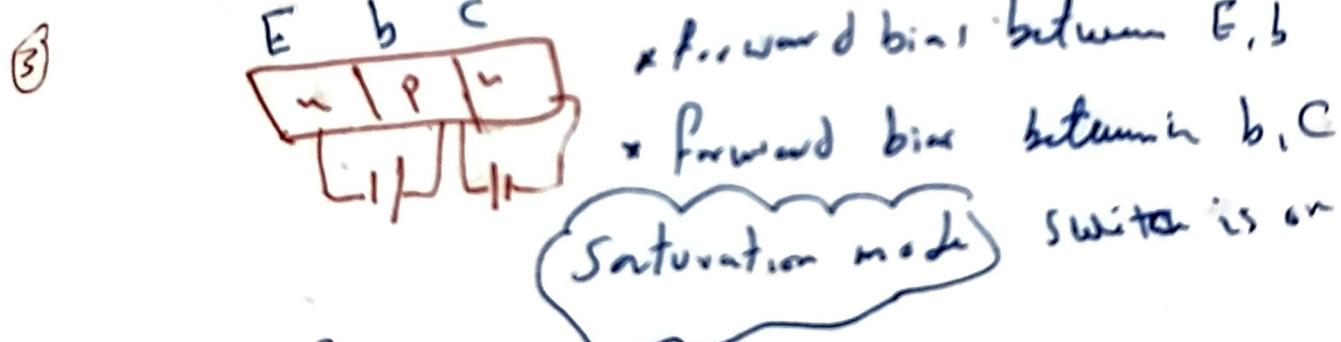
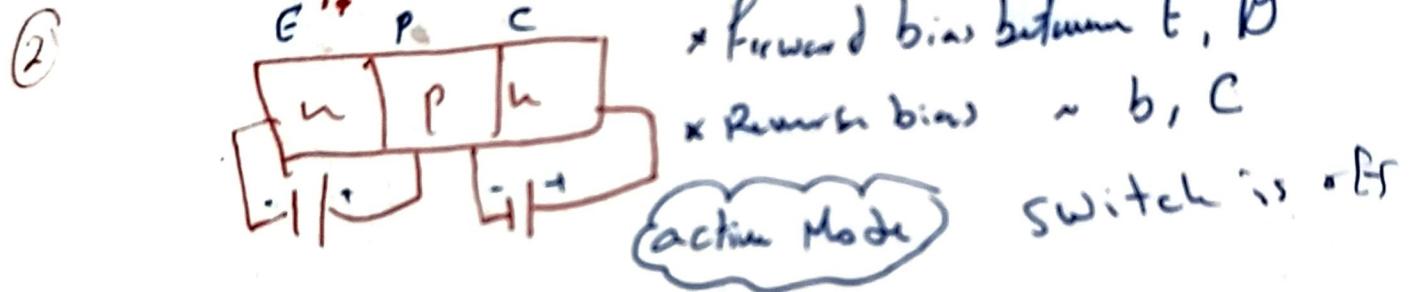
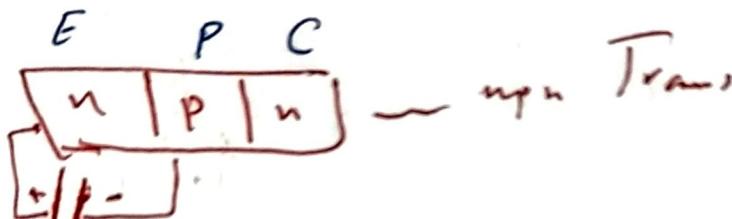


- Switches
- ① Mechanical Switch
  - ② Electrical Switch

### ① Transistor P-n Junction



### ① npn Transistor

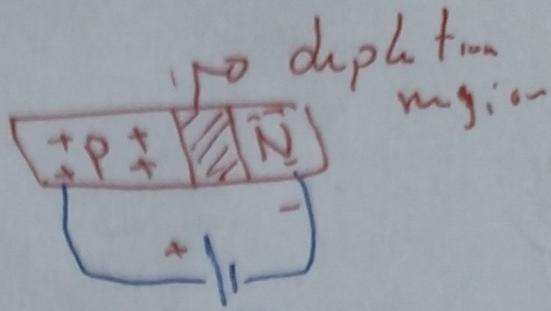


NPN Trans  
 $I_C$

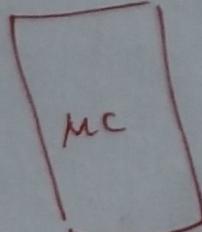
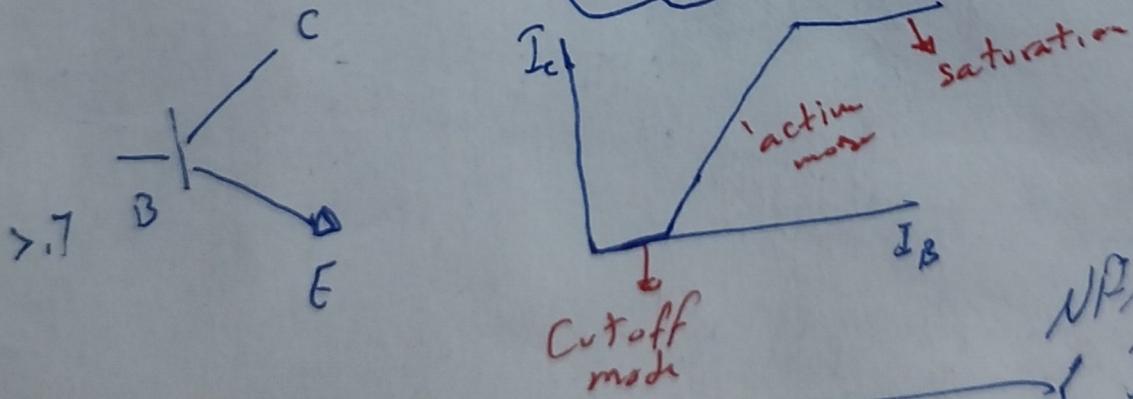
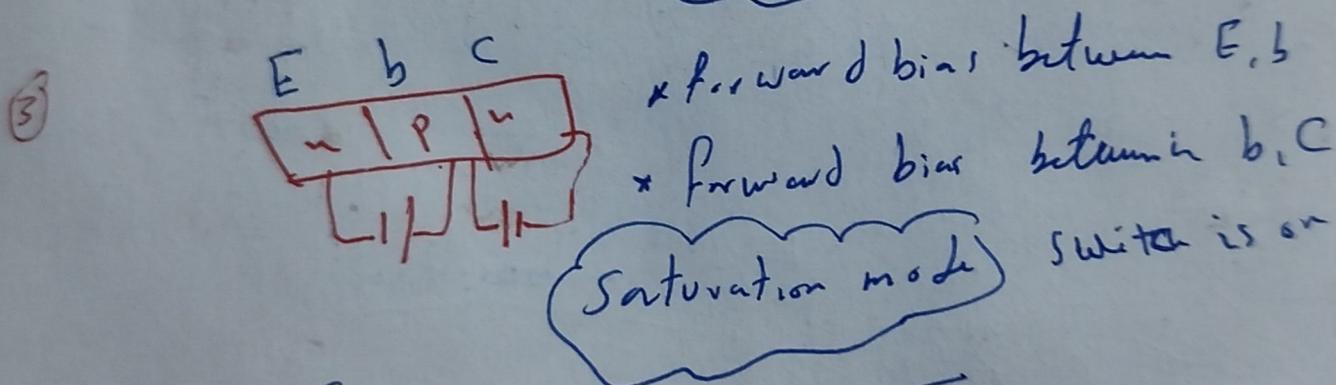
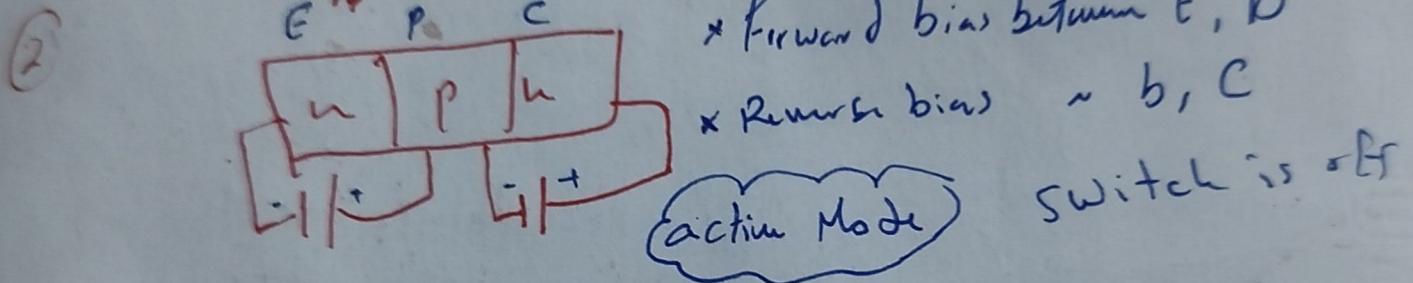
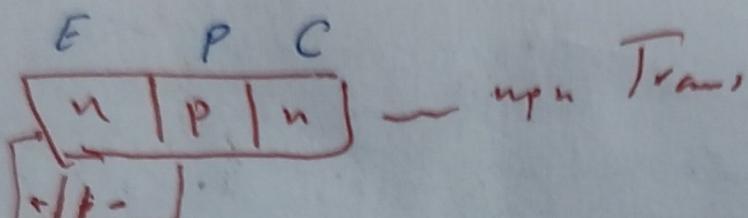
2N2222

- Switches
- ① Mechanical Switch
  - ② Electrical Switch

① Transistor P-n Junction



② npn Transistor



NPN Trans

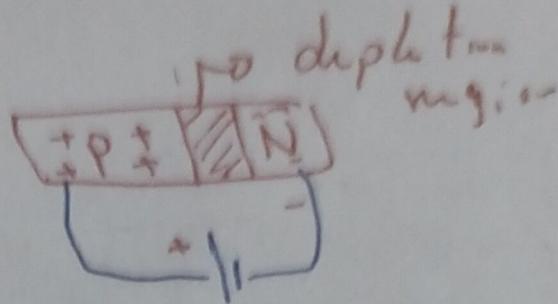
I<sub>c</sub>

2N2222

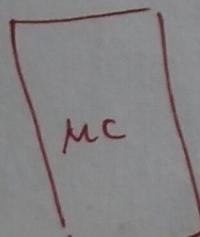
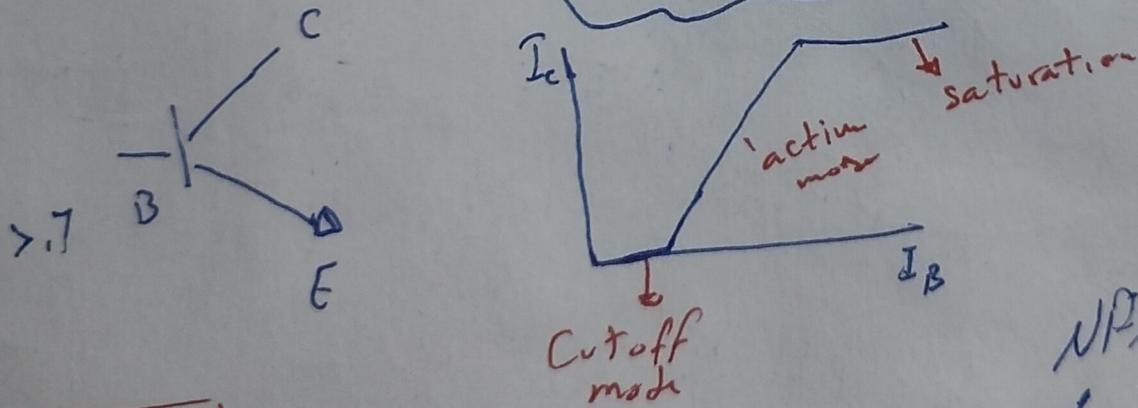
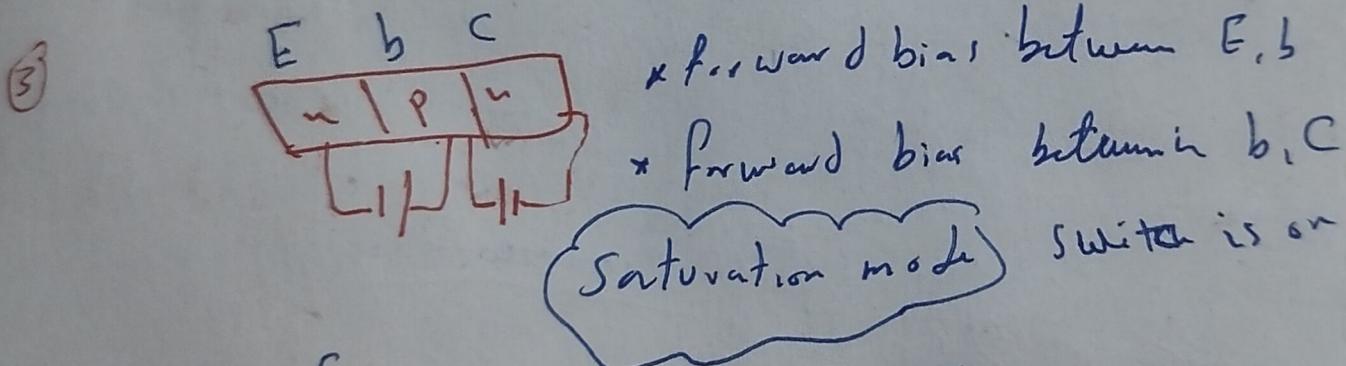
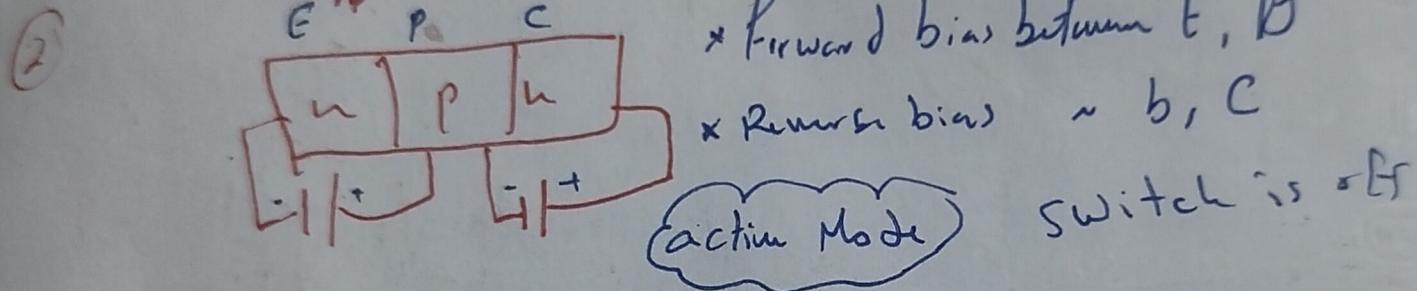
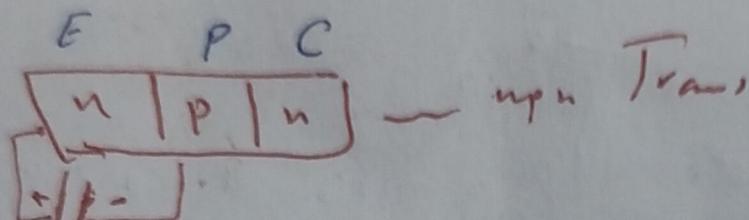
①

- Switches
- ① Mechanical Switch
  - ② Electrical Switch

① Transistor P-n Junction



② n-p-n Transistor

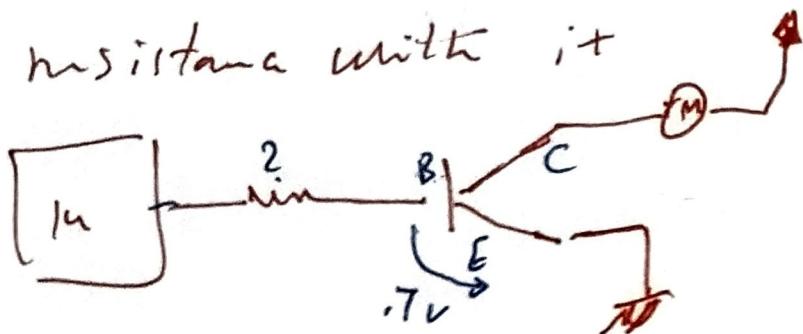


NPN Trans  
 $I_C$

2N2222

①

The 2N2222 only need 15mA to be in saturation mode so we need to add resistance with it



$$R_{\text{val}} = \frac{\Delta V}{I} = \frac{5 - 1.7}{15 \text{ mA}} = 0$$

i 5v is volt from your AC

*(Rvk)* never use AC as driving circuit use it as Control Circuit

### Brushed DC Motor

Stator → magnet

Rotator → coil

advantages

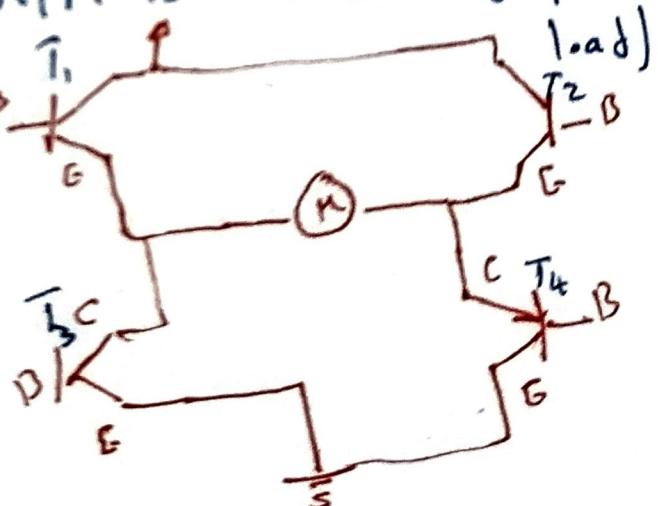
- ① Small size
- ② Simple control
- ③ low power cons

disadvantages

- ① Control by H.W (change rotation dir)
- ② RPM is not stable (depends on load)

### H-bridge & Transistor NPN

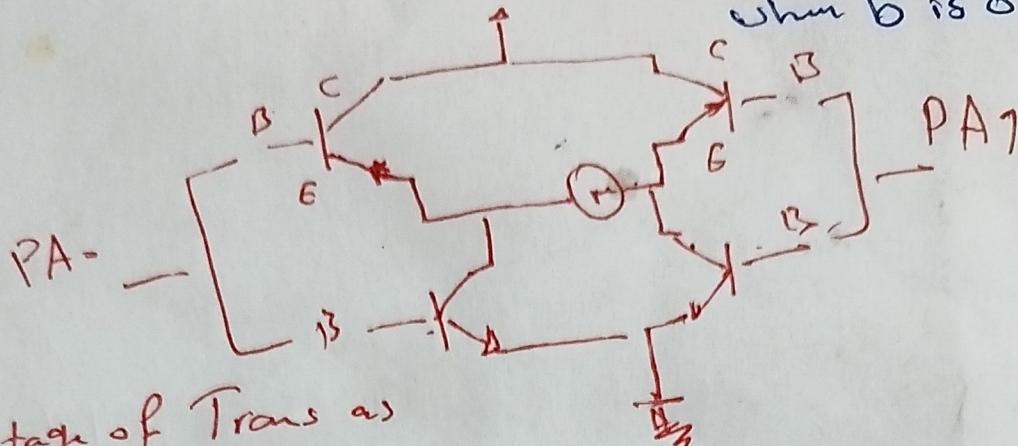
T <sub>1</sub>	T <sub>2</sub>	T <sub>3</sub>	T <sub>4</sub>	Motor
0	0	0	0	stop
1	0	0	1	rotate ↗
0	1	1	0	rotate ↑
1	1	0	0	stop
0	0	1	1	stop
1	0	1	0	short circuit
0	1	1	1	stop



This circuit has a problem  
That operator may enable or disable 2 Transistors that are reverse each other  
To solve it

H-bridge - 4 Trans      n-p-n & p-n-p

PNP only work when b is 0



disadvantage of Trans as switch

- ① Can't use in switching for loads which need very high power
- ② No isolation between power Circuit & Control unit

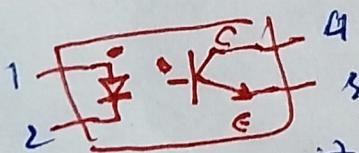
opto Coupler

advantages

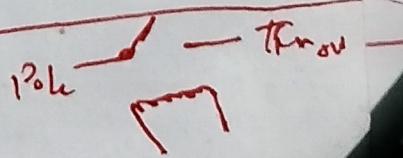
- ① Complete isolation between Control Circuit & Load circuit
- ② Low power consumption
- ③ High switch speed

disadvantages

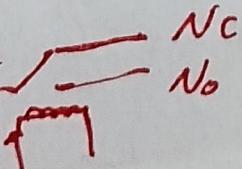
- ④ High Cost Compared to Transistor
- ⑤ Can't use in Switching for high power loads



Relay ① Single Pole Single Throw (SPST)



② SPDT

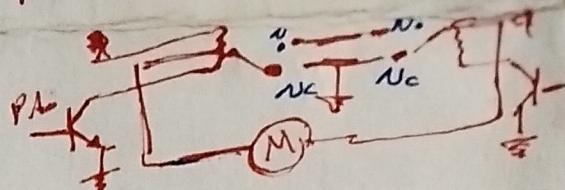


③ DPST

④ DPDT

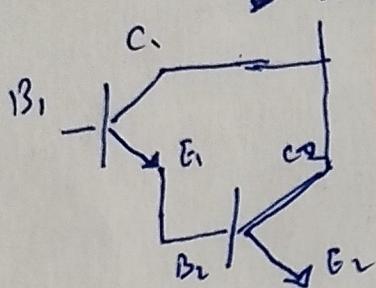
③

Hbridge using 2 Relay



use H-bridge

Darlington Pair Type of Connection used to make more Transistor handle more Current Than usual



For one Transistor  
•  $I_C = \text{Amplification Factor} \times I_B$   
( $\beta$  or hFE)

Assume  $I_B = 20 \mu A$     $\beta = 100$

max load can be handled by Transistor

$$I_C = 100 \times 20 = 2 A$$

•  $I_E = I_C + I_B$

For Tr1

$$-I_E = I_B + I_{C1} \Rightarrow I_E \approx I_{C1}$$

$I_{C1} = \beta_1 I_B$  for ①

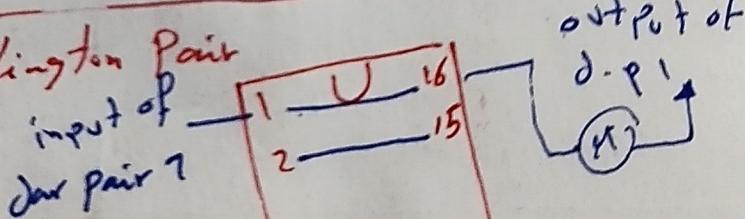
For Tr2

$$I_{C2} = \beta_2 I_{B2} \rightarrow ③ \quad I_{B2} = I_{E1} - ④$$

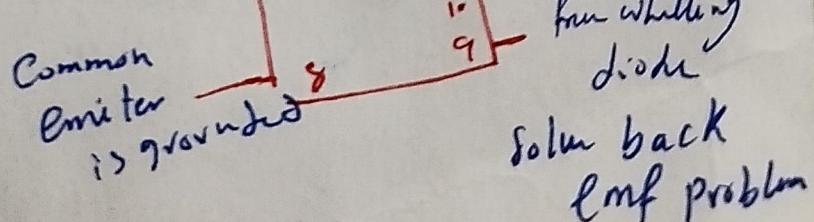
$$I_{C2} = \beta_2 I_{E1} \rightarrow ⑤ \quad I_{C2} = \beta_2 I_{C1} \rightarrow ⑥$$

$$I_{C2} = \beta_2 (\beta_1 I_B)$$

With 2003 → have 7 Darlington Pair  
You can use it with 7 loads



This Connection is  
Used for Stepper  
Motor



Can be connected to  
Vcc or left free

④



## how to calculate angle per step

Step Variation

From dataset

$$\text{stride angle } 5.625 / 64 = 0.08789^\circ$$

$$\text{angle per half step} = 0.08789^\circ$$

$$\sim \sim \text{full step} = 0.08789 \times 2 = 0.1757^\circ$$

Number of steps to have full cycle  $360^\circ = \frac{360^\circ}{0.1757^\circ} = 2098 \text{ step}$

Number of iteration of for loop  $= \frac{2098}{4} = 512$

$$360 \rightarrow 2048$$

Xdeg  $\rightarrow ?$

$$\text{No. of steps} = \frac{(\text{deg required} \times 2098)}{360}$$

$$\text{No. of iteration} = \frac{\text{No. of steps}}{4} = \boxed{?}$$

## Interrupt used in a lot of peripheral like ADC

any peripheral that generate interrupt must have:

① PIF (Peripheral Interrupt + Flag)    ② PIE (Peripheral interrupt enable)

### Interrupt types

① Maskable interrupt Can ignore (ADC - Timer - var)  
② Non Maskable  $\rightarrow$  Can't ignore by processor  
(Reset button) ANR

## Int H.W Circuit ① Fixed vector table / priority

any peripheral is connected to PIC

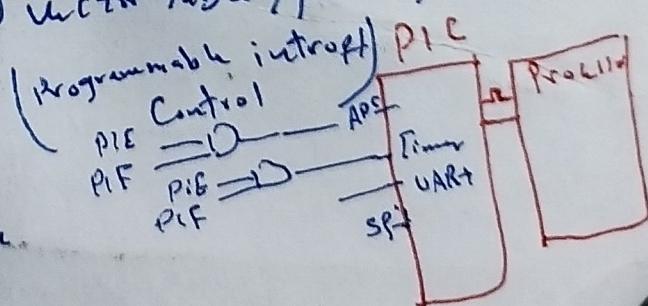
by 2 wires PIF, PIE

which are ~~acted~~ input to

an OR gate and output of gate

is connected to PLC

GIE  $\rightarrow$  global interrupt enable



①