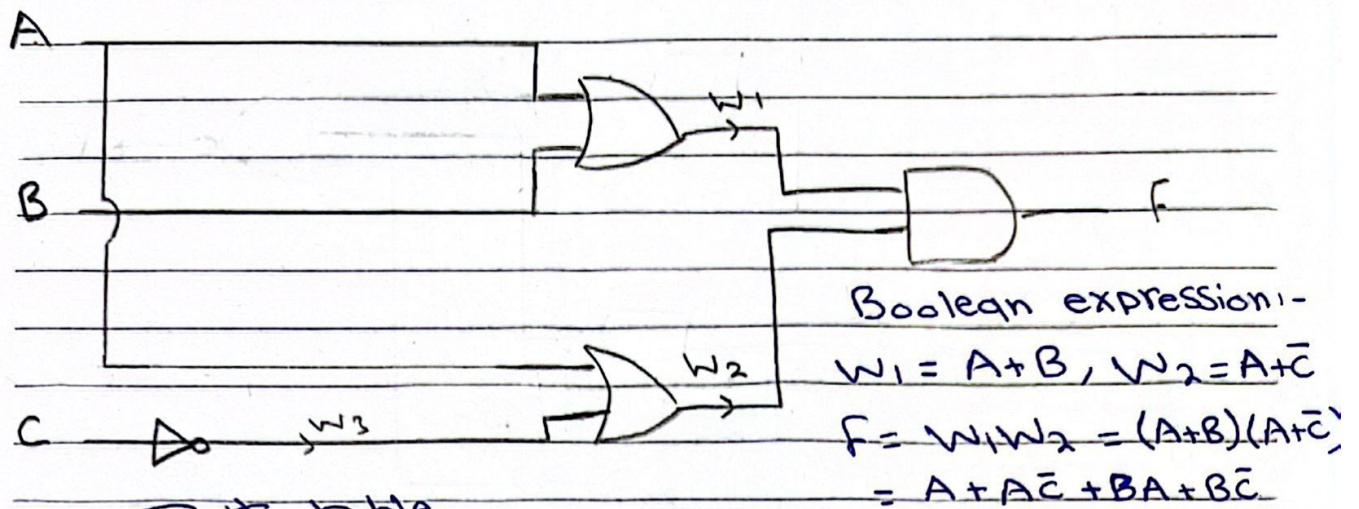


# Circuit Using And, OR Gates



Truth table

A	B	C	$W_1$	$W_2$	F
0	0	0	0	1	0
0	0	1	0	0	0
0	1	0	1	1	1
0	1	1	1	0	0
1	0	0	1	1	1
1	0	1	1	0	0
1	1	0	1	1	1
1	1	1	1	0	0



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x 1/2 (p10) v1.0

YASSIN