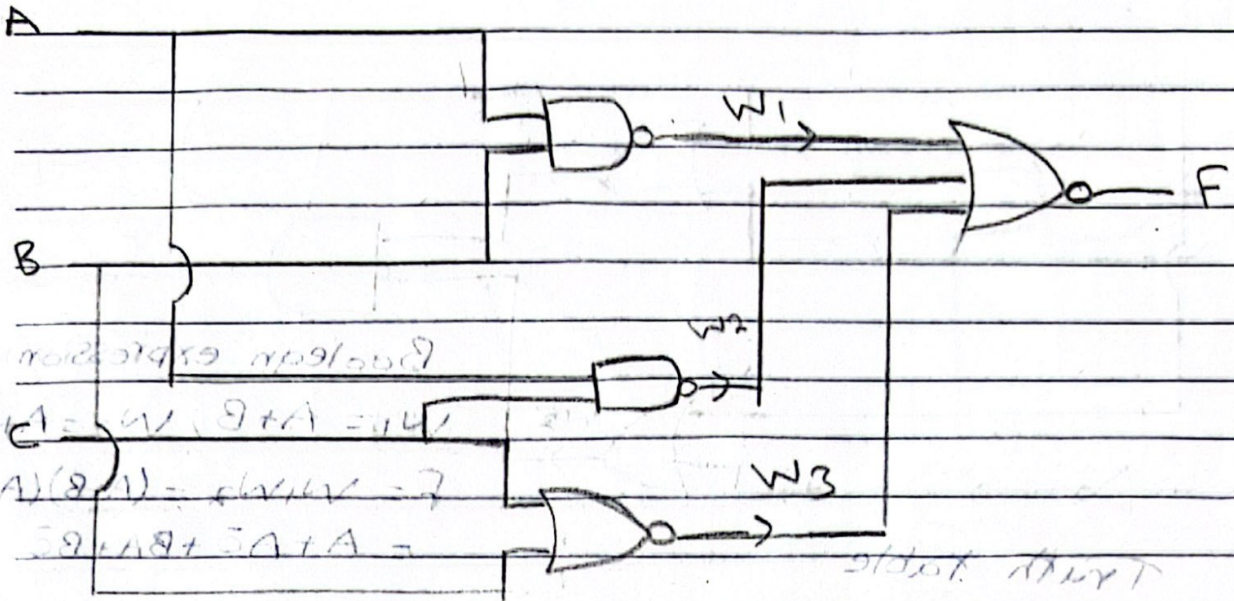


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Date:-----

# Circuit using NAND, NOR Gates



A	B	C	W <sub>1</sub>	W <sub>2</sub>	W <sub>3</sub>	F	A
0	0	0	1	1	1	0	0
0	0	1	1	0	0	0	0
0	0	1	1	1	0	0	0
0	1	0	1	1	0	0	0
0	1	1	1	0	1	0	0
0	1	1	1	1	0	0	0
1	0	0	1	1	1	0	0
1	0	1	1	0	0	0	0
1	0	1	1	1	0	0	0
1	1	0	0	1	0	0	0
1	1	1	0	0	0	1	1

Boolean expression:  $W_1 = \overline{AB}$ ,  $W_2 = \overline{AC}$ ,  $W_3 = \overline{B+C}$ ,  $F = \overline{W_1 + W_2 + W_3}$

$$F = \overline{(\overline{AB} \cdot \overline{AC} \cdot \overline{B+C})}$$



No:-----

Date:-----

3

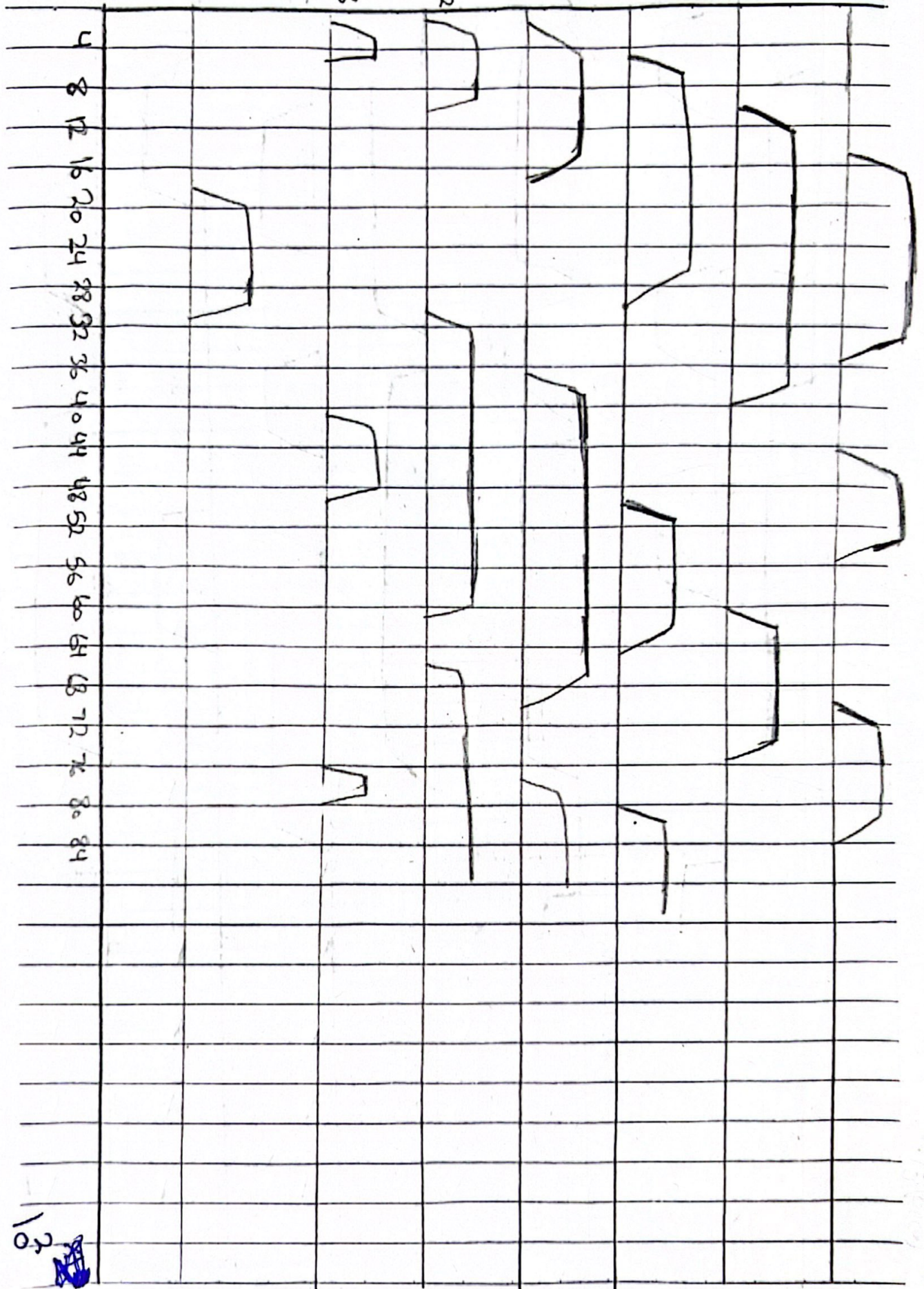
2

1

0

3

2



10  
pic

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