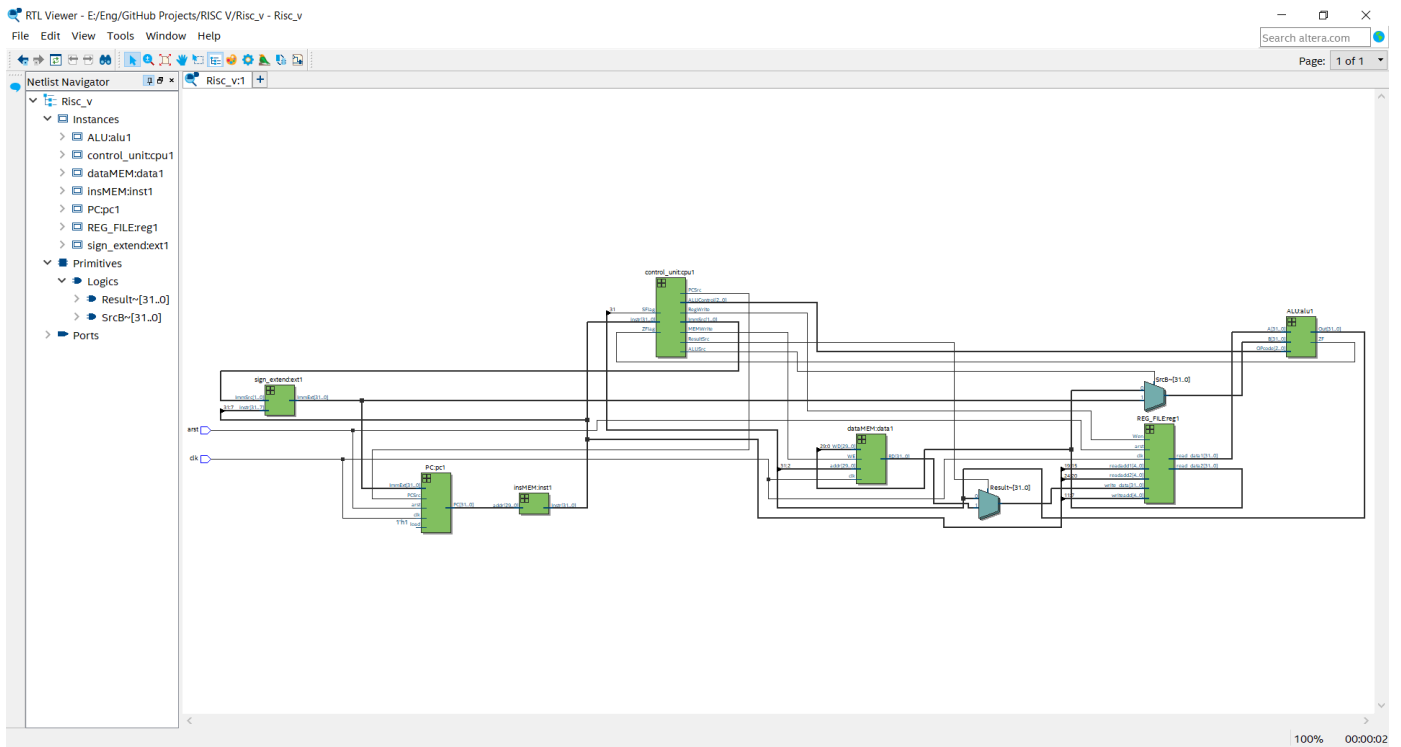
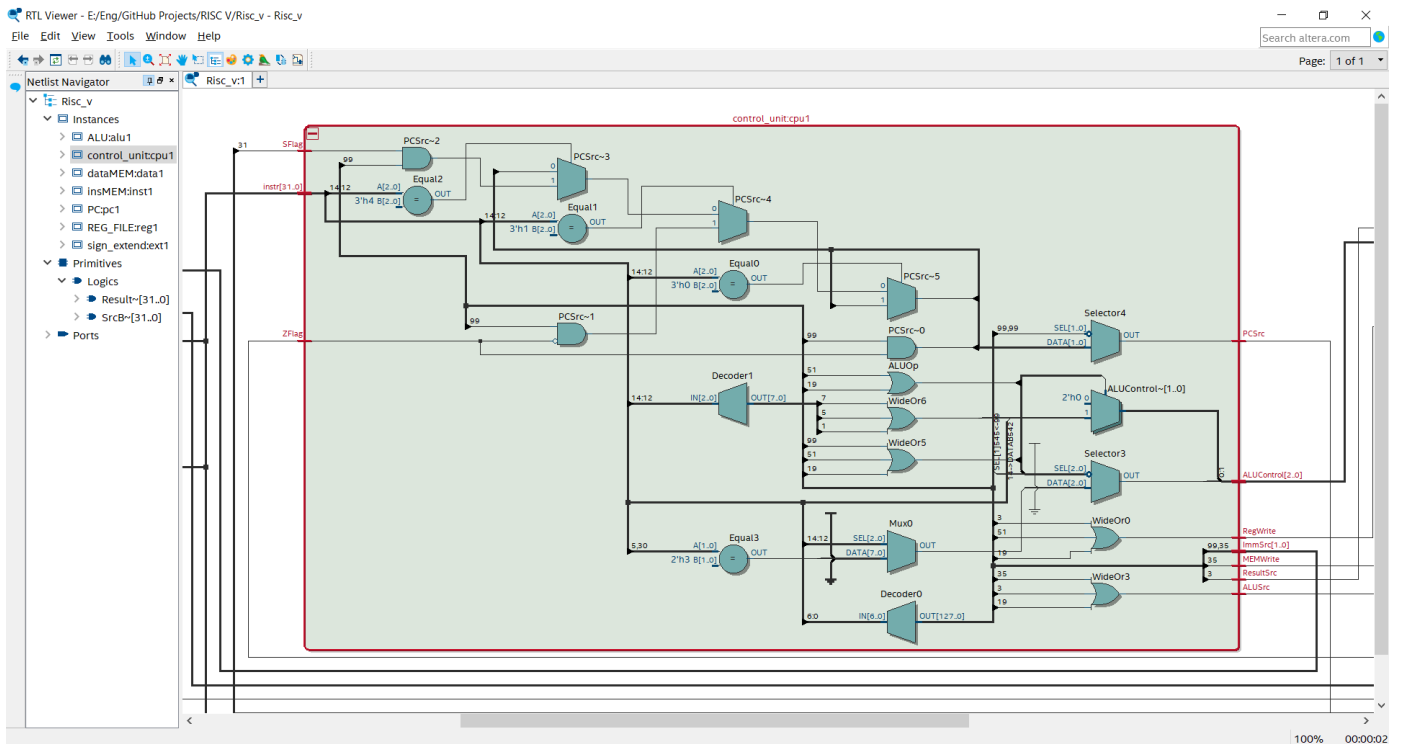


## RTL Viewer:

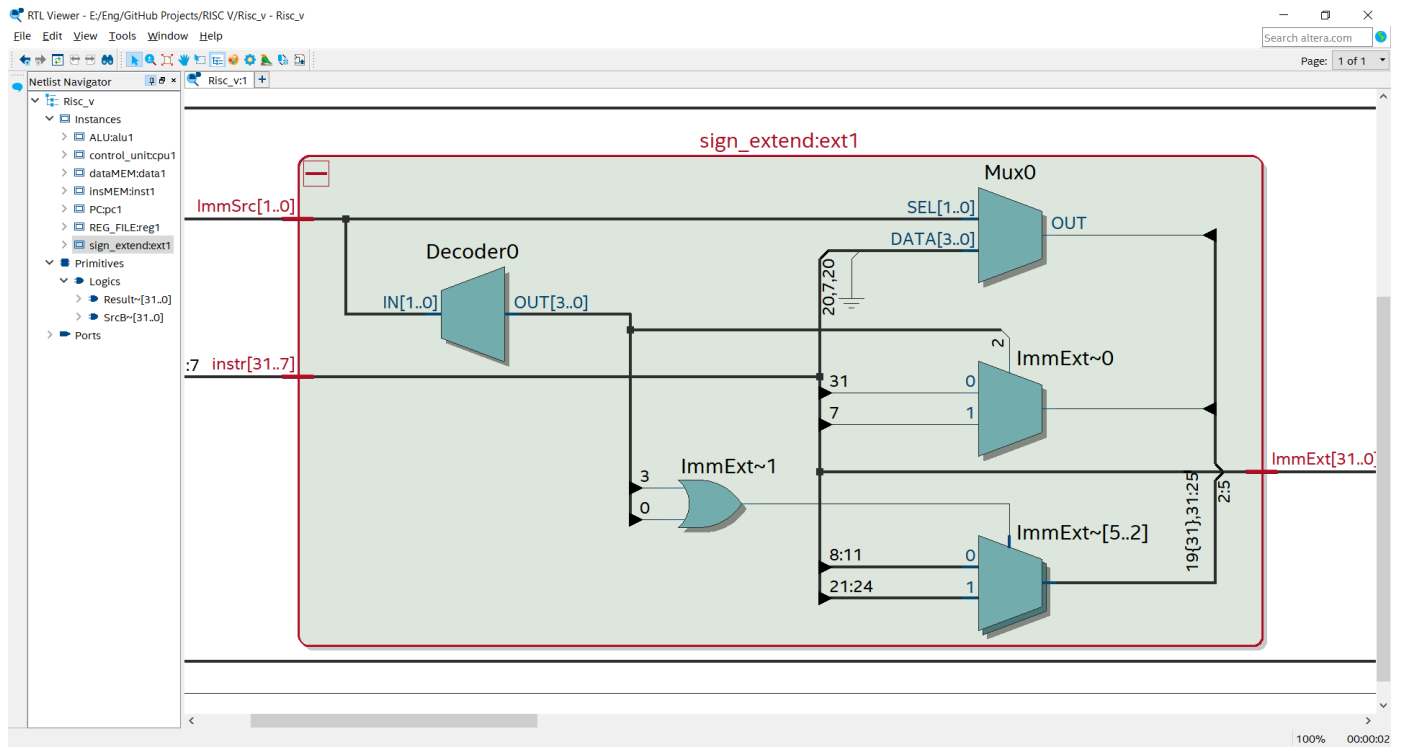
### 1- Risc\_V Processor:



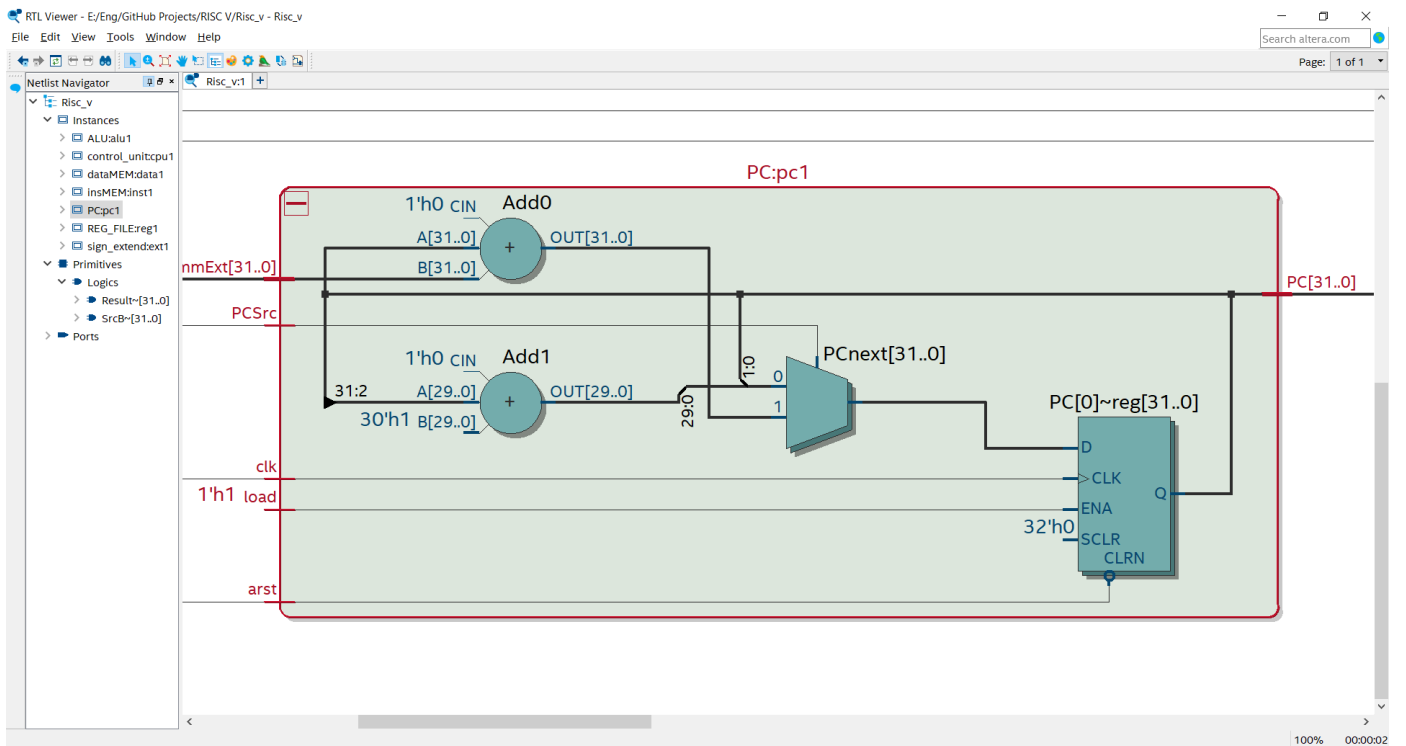
### 2- Control Unit:



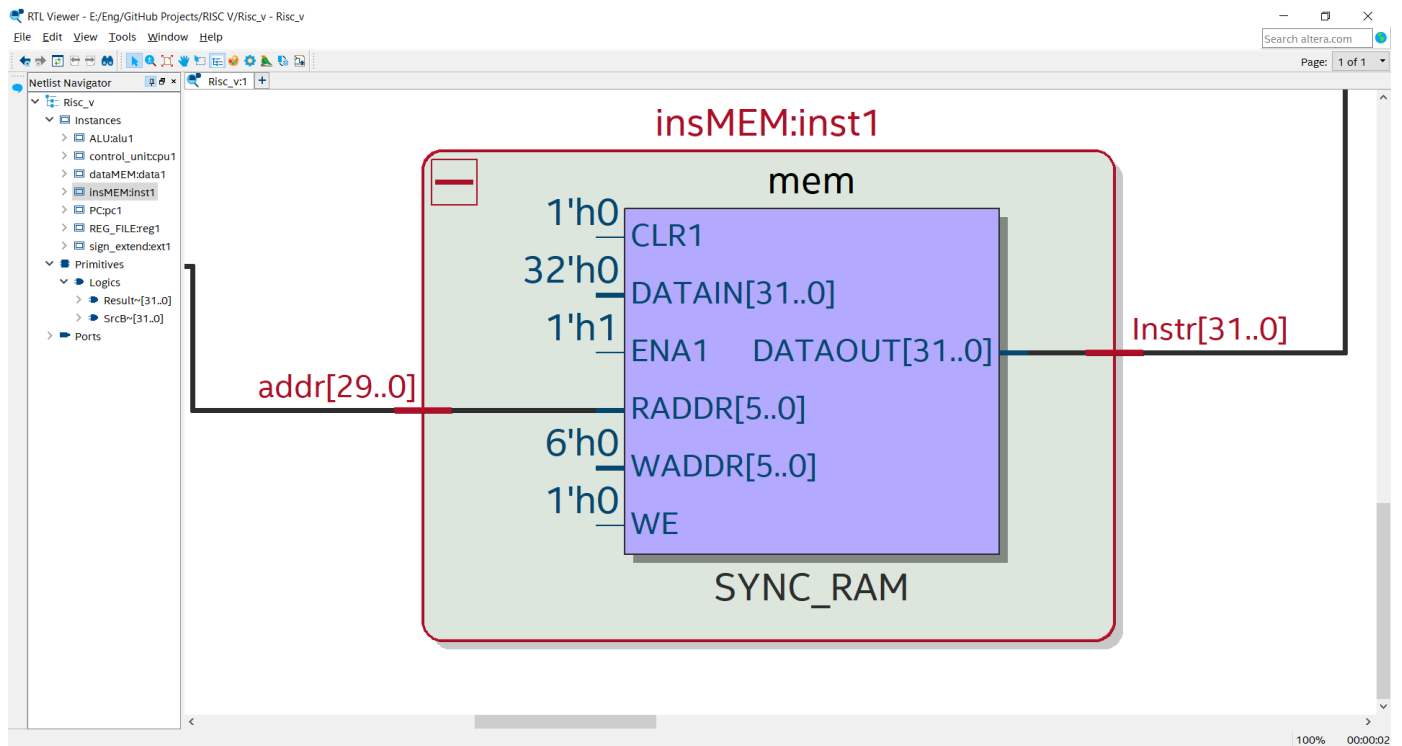
### 3- Sign Extend:



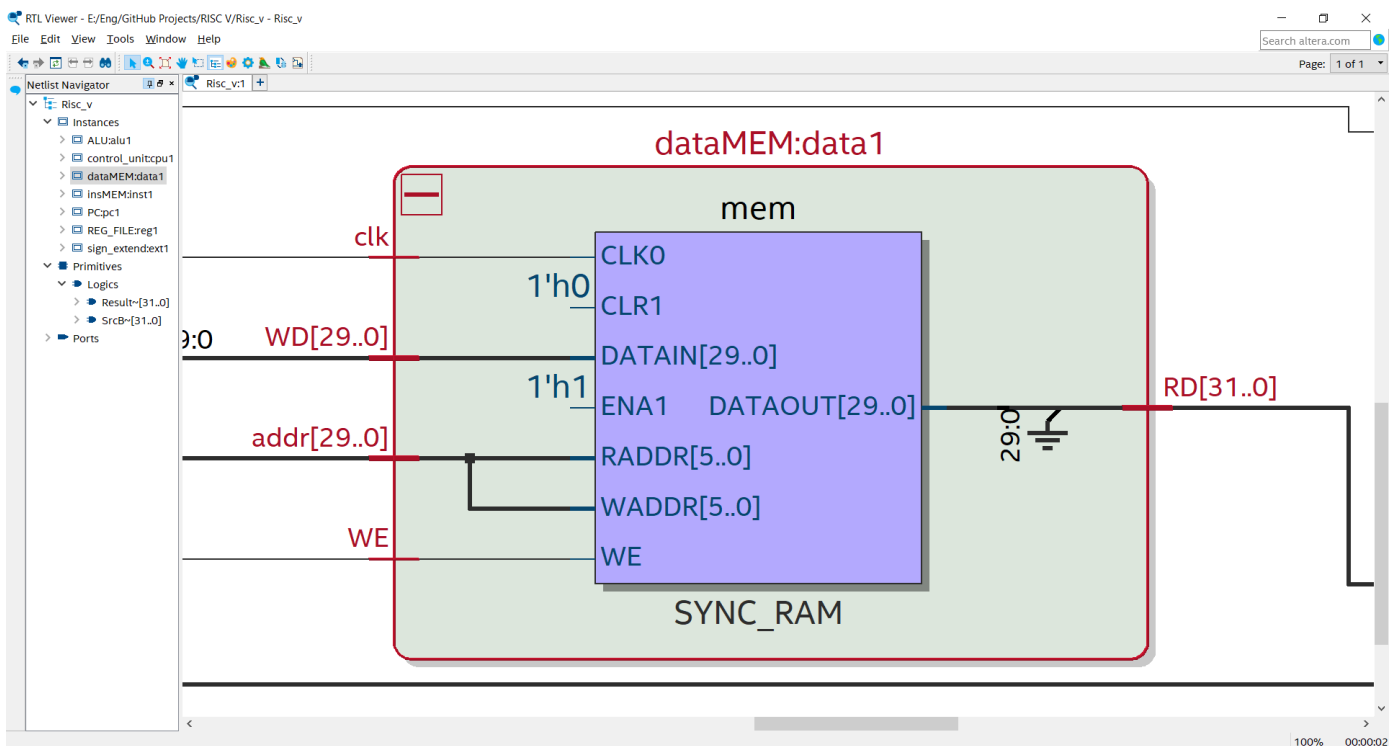
### 4- Program Counter:



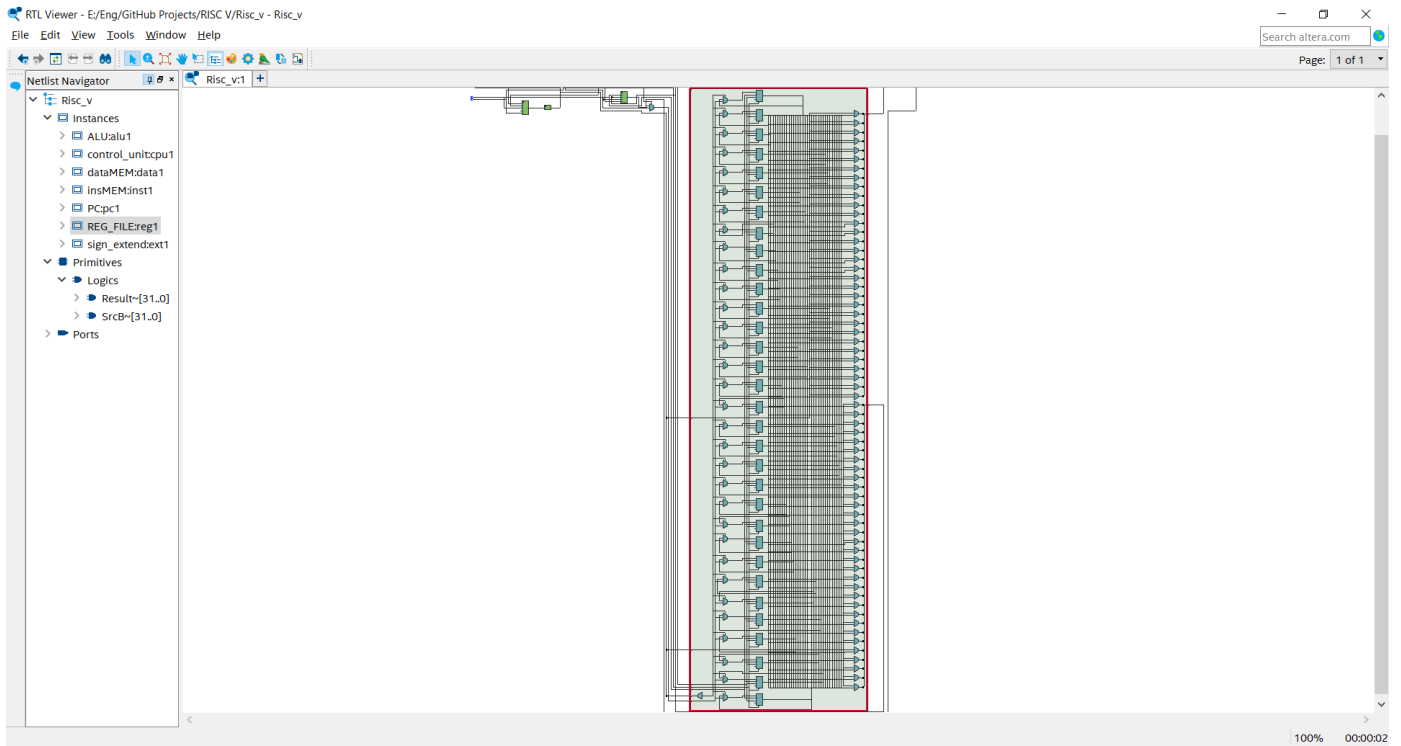
## 5- Instruction Memory:



## 6- Data Memory:



## 7- Register File:



## 8- ALU:

