

AHMED RAYHAN

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Education

AHSANULLAH UNIVERSITY OF SCIENCE AND TECHNOLOGY

Bachelor of Science in Electrical and Electronic Engineering

CGPA 3.5/4.00 (Up to 4th year 1st semester)

Thesis Project: Free space Optical Communication System

Dhaka, Bangladesh

December 2024

HIGHER SECONDARY SCHOOL CERTIFICATE

Birshrestha Noor Mohammad Public College

GPA 5.0/5.0

Dhaka, Bangladesh

July 2019

Experience

DHAKA POWER DISTRIBUTION COMPANY

Attachment Trainee Intern

Dhaka, Bangladesh

October 2023 – November 2023

- Observed DPDC's organizational structure, including SCADA system, call center, and voltage switching.
- Participated in field visits to understand grid operations and efficient power distribution.

NEURAL SEMICONDUCTOR LTD.

Attachment Trainee Intern

Dhaka, Bangladesh

July 2024 – July 2024

- Gained an overview of ASIC design flow and exposure to advanced semiconductor technologies through hands-on sessions with industry professionals through these short 5 days attachment session.

Leadership and Activities

AUST Career Development Club

Executive Director

Dhaka, Bangladesh

April 2024 – Present

- Manage all IT department functions: strategic planning, project management, and team supervision.
- Lead major event planning and execution, maximizing attendee engagement.

Sub Executive Member

April 2023 – April 2024

- Manage and coordinate IT aspects for club events, including website development, social media campaigns, and event registration software, ensuring smooth operation and information dissemination.
- Organized and executed successful career-related workshops and events like the AUST National Job Fair 2022 & 2024, and Checkmate: National Business Case Competition 2023, contributing to the professional development of the students.

Projects

- Automatic Voltage Stabilizer:** Built device to regulate AC power output.
- LED Thermometer:** Constructed a digital thermometer using **analog ICs**.
- RFID Locking System:** Designed access control system.
- Gas Leakage Detection:** Created **IoT** based system for gas leak detection.
- Pulse Amplitude Modulation:** Simulated and analyzed PAM signals using **MATLAB**.
- Implemented **Schematic and Layout** using virtuoso to implement Conventional Fat tree encoder.
- Implemented Home Security System using FSM and executed **Full Chip Design** with cadence Innovus & Genus.

Honors & Awards

- Champion of IEEE's **Research Revelry: EV batteries**
Issued by IEEE AUST STUDENT BRANCH · May 2024
- Champion of IEEE's **Poster Pavilion – A Showcase of Innovation**
Issued by IEEE AUST STUDENT BRANCH · Jun 2023
- 7th Place in **Research Article Writing Competition**
Issued by IEEE AUST STUDENT BRANCH · Aug 2023

Skills & Interests

Technical : Cadence Virtuoso, Innovus, Genus, Proteus, OrCAD PSpice, C++, MATLAB, IoT.
Interests : Researching cutting-edge technology & travelling.